Assurance Continuity Maintenance Report

BSI-DSZ-CC-0426-2007-MA-01

Smartcard with Java Card Platform

NXP P521G072V0P (JCOP 21 v2.3.1),

NXP P531G072V0P (JCOP 31 v2.3.1) and

NXP P531G072V0Q (JCOP 31 v2.3.1)

from

IBM Deutschland Entwicklung GmbH

Common Criteria Arrangement
for components up to EAL4

The IT product identified in this report was assessed according to the *Assurance Continuity: CCRA Requirements,* version 1.0, February 2004 and the distributors Impact Analysis Report (IAR). The baseline for this assessment was the Certification Report, the Security Target and the Evaluation Technical Report of the product certified by the Federal Office for Information Security (BSI) under BSI-DSZ-CC-0426-2007.

The change to the certified product is at the level of a security irrelevant change of the underlying hardware, a change that has no effect on assurance. The identification of the maintained product is indicated by a new version number compared to the certified product.

Consideration of the nature of the change leads to the conclusion that it is classified as a minor change and that certificate maintenance is the correct path to continuity of assurance.

Therefore, the assurance as outlined in the Certification Report BSI-DSZ-CC-0426-2007 is maintained for this version of the product. Details can be found on the following pages.

This report is an addendum to the Certification Report BSI-DSZ-CC-0426-2007.

Bonn, 13. August 2007

Assessment

The IT product identified in this report was assessed according to the *Assurance Continuity: CCRA Requirements* [1] and the Impact Analysis Report (IAR) [2]. The baseline for this assessment was the Certification Report of the certified TOE [3], the Security Target [4] and the Evaluation Technical Report as outlined in [3].

The distributor of the NXP P521G072V0P (JCOP 21 v2.3.1), NXP P531G072V0P (JCOP 31 v2.3.1) and NXP P531G072V0Q (JCOP 31 v2.3.1), NXP Semiconductors Germany GmbH, Business Line Identification, submitted an IAR [2] to the BSI for approval. The IAR is intended to satisfy requirements outlined in the document *Assurance Continuity: CCRA Requirements* [1]. In accordance with those requirements, the IAR describes the changes made to the certified TOE, the evidence updated as a result of the changes and the security impact of the changes.

The NXP P541G072V0P (JCOP 41 v2.3.1) was changed due to the integration of additional hardware plattforms.

The change of the hardware platform from P5CT072V0P to P5CC072V0P (both certified under BSI-DSZ-CC-0348-2006) was a change to integrate hardware with disabled USB Interface and disabled Contactless Interface as outlined in the hardware certification report BSI-DSZ-CC-0348-2006.

The change of the hardware platform from P5CT072V0P to P5CD072V0P (both certified under BSI-DSZ-CC-0348-2006) and to P5CD072V0Q (certified under BSI-DSZ-CC-0349-2006) was a change to integrate hardware with disabled USB Interface and disabled AES crypto co-processor as outlined in the hardware certification report BSI-DSZ-CC-0348-2006 [6] and BSI-DSZ-CC-0349-2006 [7].

The Embedded Software running on the P5CT072V0P (resulting in the composite product P541G072V0P) is identical with the Embedded Software running on the P5CC072V0P, P5CD072V0P and P5CD072V0Q (resulting in the composite products P521G072V0P, P531G072V0P and P531G072V0Q). The difference is related to the availability (accessibility) of the USB and Contactless Interfaces and the AES crypto co-processor configured as part of the configuration process at the end of the production testing phase. Relevant configuration data are defined in [5]. The ROM mask identification and the EEPROM code identification for the TOE did not change.

The Security Targets and the Security Target lite documents were editorially updated (see [8] to [10] and [11] to [13]).

The change is not significant from the standpoint of security, however Configuration Management procedures required a change in the version number from P541G072V0P (JCOP 41 v2.3.1) to P521G072V0P (JCOP 21 v2.3.1), to P531G072V0P (JCOP 31 v2.3.1) and to P531G072V0Q (JCOP 31 v2.3.1).

Conclusion

The change to the TOE is at the level of a security irrelevant change of the underlying hardware, a change that has no effect on assurance. Examination of the evidence indicates that the changes required are limited to the identification of the software

configuration data [5] and thus of the TOE. Changed configuration items related to the hardware are covered by the certificates BSI-DSZ-CC-0348-2006 and BSI-DSZ-CC-0349-2006. Consideration of the nature of the change leads to the conclusion that it is classified as a minor change and that certificate maintenance is the correct path to continuity of assurance.

Therefore, BSI agrees that the assurance as outlined in the Certification Report [3] is maintained for this version of the product. This report is an addendum to the Certification Report [3].

References

[1]   Common Criteria document CCIMB-2004-02-009 "Assurance Continuity: CCRA Requirements", version 1.0, February 2004

[2]   Impact Analysis Report, BSI-DSZ-CC-0426, Version 1.02, 15. June 2007, NXP Semiconductors Germany GmbH (confidential document)

[3]   Certification Report BSI-DSZ-CC-0426-2007 for NXP P541G072V0P (JCOP 41 v2.3.1) from NXP Semiconductors Germany GmbH, Bundesamt für Sicherheit in der Informationstechnik, 10. August 2007

[4]   Security Target lite NXP P541G072V0P (JCOP 41, v2.3.1) Secure Smart Card Controller, Version 1.0, 23. July 2007, IBM Deutschland Entwicklung GmbH (sanitized public document)

[5]   Configuration List NXP P541G072V0P (JCOP 41 v2.3.1)Secure Smart Card Controller, Version 1.3, 16. July 2007, IBM Deutschland Entwicklung GmbH (confidential document)

[6]   Certification Report BSI-DSZ-CC-0348-2006 for Philips Secure Smart Card Controller P5CT072V0P, P5CC072V0P, P5CD072V0P and P5CD036V0P each with specific IC Dedicated Software from Philips Semiconductors GmbH Business Line Identification, Version 1.0, 28. March 2006, BSI

[7]   Certification Report BSI-DSZ-CC-0349-2006 for Philips Secure Smart Card Controller P5CT072V0Q, P5CD072V0Q, P5CD036V0Q including specific Inlay Packages OM95xx each with specific IC Dedicated Software from Philips Semiconductors GmbH Business Line Identification, Version 1.0, 28. March 2006, BSI

[8]   Security Target, NXP P521G072V0P (JCOP 21, v2.3.1) Secure Smart Card Controller, Version 1.0, 15. June 2007, IBM Deutschland Entwicklung GmbH (confidential document)

[9]   Security Target, NXP P531G072V0P (JCOP 31, v2.3.1) Secure Smart Card Controller, Version 1.0, 15. June 2007, IBM Deutschland Entwicklung GmbH (confidential document)

[10]   Security Target, NXP P531G072V0Q (JCOP 31, v2.3.1) Secure Smart Card Controller, Version 1.0, 15. June 2007, IBM Deutschland Entwicklung GmbH (confidential document)

[11]   Security Target lite NXP P521G072V0P (JCOP 21, v2.3.1) Secure Smart
            Card Controller, Version 1.0, 23. July 2007, IBM Deutschland Entwicklung
            GmbH (sanitized public document)

[12]   Security Target lite NXP P531G072V0P (JCOP 31, v2.3.1) Secure Smart
            Card Controller, Version 1.0, 23. July 2007, IBM Deutschland Entwicklung
            GmbH (sanitized public document)

[13]   Security Target lite NXP P531G072V0Q (JCOP 31, v2.3.1) Secure Smart
            Card Controller, Version 1.0, 23. July 2007, IBM Deutschland Entwicklung
            GmbH (sanitized public document)