

1 **Security Target for BAROC/FISC TSAM 1.0**

2

3 **File Name: ST_FISCTSAM_1.0.0**

4 **Version: 1.0.0**

5 **Date: 2008-05-21**

6 **Authors: BAROC & FISC**

7 **TOE / TOE Version: BAROC/FISC TSAM 1.0**

8 Table of Contents

9	TABLE OF CONTENTS.....	2
10	LIST OF FIGURES AND TABLES.....	4
11	1 ST INTRODUCTION.....	5
12	1.1 ST IDENTIFICATION.....	5
13	1.2 ST OVERVIEW	5
14	1.3 CC CONFORMANCE CLAIMS	6
15	2 TOE DESCRIPTION	7
16	2.1 OVERVIEW.....	7
17	2.2 TOE DEFINITION.....	7
18	2.3 TOE BOUNDARIES	8
19	2.3.1 TOE Physical Scope and Boundary.....	8
20	2.3.2 TOE Logical Scope and Boundary.....	8
21	2.4 TOE LIFE CYCLE	9
22	2.5 ROLES	10
23	3 TOE SECURITY ENVIRONMENT.....	12
24	3.1 ASSETS.....	12
25	3.1.1 GlobalPlatform Keys (GPKs).....	12
26	3.1.2 Management Key (MK).....	12
27	3.1.3 Working Keys (WKs).....	12
28	3.1.4 Terminal Management Data (TMD).....	12
29	3.1.5 Retry Counter (RC).....	13
30	3.1.6 Life Cycle State (LCS).....	13
31	3.1.7 Transaction Data (TD).....	13
32	3.2 ASSUMPTIONS (ABOUT THE ENVIRONMENT)	13
33	3.3 THREATS	14
34	3.3.1 Threats not contained in [JCOP41V231ST].....	15
35	3.3.2 Threats from [JCOP41V231ST]	15
36	3.4 ORGANISATIONAL SECURITY POLICIES (OSP)	19
37	3.4.1 OSs not contained in [JCOP41V231ST].....	19
38	3.4.2 OSs from [JCOP41V231ST]	19
39	4 SECURITY OBJECTIVES.....	20
40	4.1 SECURITY OBJECTIVES FOR THE TOE.....	20
41	4.1.1 Security Objectives not contained in [JCOP41V231ST].....	20
42	4.1.2 Security Objectives from [JCOP41V231ST]	21
43	4.2 SECURITY OBJECTIVES FOR THE ENVIRONMENT	24
44	4.2.1 Security Objectives for The Environment not contained [JCOP41V231ST]	24
45	4.2.2 Security Objectives for the Environment from [JCOP41V231ST].....	25
46	5 SECURITY REQUIREMENTS.....	26
47	5.1 TOE SECURITY FUNCTIONAL REQUIREMENTS NOT CONTAINED IN [JCOP41V231ST].....	26
48	5.1.1 Cryptographic support (FCS).....	26
49	5.1.2 User data protection (FDP)	26
50	5.1.3 Identification and authentication (FIA).....	28
51	5.1.4 Security management (FMT).....	30
52	5.1.5 Trusted path/channels (FTP).....	30
53	5.2 TOE SECURITY FUNCTIONAL REQUIREMENTS FROM [JCOP41V231ST].....	31
54	5.3 TOE SECURITY ASSURANCE REQUIREMENTS	33
55	5.4 IT ENVIRONMENT SECURITY REQUIREMENTS NOT CONTAINED IN [JCOP41V231ST].....	33
56	5.4.1 Cryptographic key generation.....	34
57	5.4.2 User data protection	34
58	5.4.3 Trusted path/channels.....	34
59	5.5 IT ENVIRONMENT SECURITY REQUIREMENTS FROM [JCOP41V231ST].....	35
60	6 TOE SUMMARY SPECIFICATION	36

61	6.1	SECURITY FUNCTIONS	36
62	6.2	STRENGTH OF FUNCTION CLAIMS	39
63	6.3	ASSURANCE MEASURES	40
64	7	PP CLAIMS.....	41
65	7.1	PP REFERENCE.....	41
66	7.2	PP ADDITIONS AND REFINEMENTS	41
67	8	RATIONALE.....	42
68	8.1	SECURITY OBJECTIVES RATIONALE	42
69	8.1.1	<i>Traceability of the Security Objectives.....</i>	42
70	8.1.2	<i>Coverage of the assumptions.....</i>	43
71	8.1.3	<i>Countering of the threats.....</i>	43
72	8.1.4	<i>Coverage of the Organizational Security Policies.....</i>	43
73	8.1.5	<i>Security Objectives Rationale from [JCOP41V231ST].....</i>	44
74	8.2	SECURITY REQUIREMENTS RATIONALE.....	45
75	8.2.1	<i>Fulfilment of security objectives.....</i>	45
76	8.2.2	<i>Traceability of the Security Functional Requirements</i>	47
77	8.2.3	<i>Suitability of Security Assurance Requirements</i>	48
78	8.2.4	<i>Fulfillment of dependencies.....</i>	48
79	8.2.5	<i>Suitability of minimum strength of function (SoF) level.....</i>	49
80	8.3	TOE SUMMARY SPECIFICATION RATIONALE	50
81	8.3.1	<i>Traceability and Satisfaction of the TOE SFRs</i>	50
82	8.3.2	<i>Mutual Support of the Security Functions.....</i>	57
83	8.3.3	<i>Validity of SOF-claims.....</i>	58
84	8.3.4	<i>Compliance of assurance measures.....</i>	58
85	8.4	PP CLAIMS RATIONALE.....	58
86	8.4.1	<i>PP Conformance concerning Assumptions.....</i>	58
87	8.4.2	<i>PP Conformance concerning Threats</i>	58
88	8.4.3	<i>PP Conformance concerning Organizational Security Policies.....</i>	58
89	8.4.4	<i>PP Conformance concerning Security Objectives for the TOE</i>	58
90	8.4.5	<i>PP Conformance concerning Security Objectives for the Environment.....</i>	59
91	8.4.6	<i>PP Conformance concerning SFRs for the TOE.....</i>	59
92	8.4.7	<i>PP Conformance concerning SARs.....</i>	59
93	8.4.8	<i>PP Conformance concerning SFRs for the IT Environment.....</i>	59
94	9	APPENDIX.....	61
95	9.1	ABBREVIATIONS	61
96	9.2	REFERENCES.....	62
97			

98	List of Figures and Tables	
99	Figure 2-1: The TOE architecture	8
100	Figure 2-2: TOE life cycle	9
101	Table 3-1: Threats from [JCOP41V231ST]	16
102	Table 4-1: Security objectives from [JCOP41V231ST]	21
103	Table 4-2: Security objectives for the environment from [JCOP41V231ST]	25
104	Table 5-1: Actions on detection of integrity errors	28
105	Table 5-2: TOE SFRs from [JCOP41V231ST]	32
106	Table 5-3: Evaluation Assurance Requirements	33
107	Table 5-4: IT Environment SFRs from [JCOP41V231ST]	35
108	Table 8-1: Security objectives rationale	42
109	Table 8-2: Security objectives rationale from [JCOP41V231ST]	44
110	Table 8-3: Security requirements rationale	45
111	Table 8-4: Fulfillment of TOE SFR dependencies	49
112	Table 8-5: TSS rationale	50
113	Table 8-6: Traceability and Satisfaction of the TOE SFRs	56
114	Table 8-7: Analysis of Mutual Support of the SFs	57
115	Table 8-8: Coverage of Assumptions from [JCSPP] “Minimal Configuration”	58
116	Table 8-9: Coverage of Environment Security Objectives from [JCSPP]	59
117	Table 8-10: Coverage of IT Environment SFRs from [JCSPP]	60
118		

119 1 ST Introduction

120 1.1 ST Identification

121	Title:	Security Target for BAROC/FISC TSAM 1.0
122	TOE:	BAROC/FISC TSAM 1.0
123	Guidance:	Administrator and User Guidance for BAROC/FISC TSAM
124		1.0, version 1.0.0, date: 2008-05-21 BAROC/FISC
125		SHA-1 hash value of the PDF version:
126		94db00658c87902818433487eed82a88d8408114
127	Document Version:	1.0.0
128	Document Date:	2008-05-21
129	Author:	BAROC & FISC
130	CC version used:	CC V2.3, CEM V2.3, including all corresponding FIs, as
131		applicable
132	CC Conformance:	Conformant to CC V2.3 part 2 extended and conformant to
133		CC V2.3 part 3 augmented (EAL4 augmented by
134		ADV_IMP.2 and AVA_VLA.4)
135	Certification ID:	BSI-DSZ-CC-0442
136	Evaluation Body:	TÜViT GmbH, Germany
137	Certification Body:	BSI, Germany

138 1.2 ST Overview

139 After a successful chip migration of ATM cards in 2005 for conventional online
140 transactions of cash withdrawal and fund transfer via ATM in Taiwan, FISC
141 would in addition like to promote the debit solution for point of sales (POS) with
142 the chip ATM cards. For this to be done, the confidentiality and integrity of data
143 transfer between a POS terminal and its acquiring bank must be assured as a
144 prerequisite. FISC therefore comes up with the development of TSAM (Terminal
145 Security Access Module), the TOE, to be used by POS terminals to ensure the
146 confidentiality and integrity of data transfer. The TOE is composed of a
147 JavaCard applet (TSAM applet) and NXP P541G072V0P smart card controller
148 (the latter consists of JCP (JavaCard Platform) and SCP (Smart Card
149 Platform)). This security target is for the composite TSAM TOE.

150
151 The main objectives of this security target are:

- 152 • To describe the security environment of the TOE including assets to be
153 protected and threats to be countered by the TOE and its environment.
- 154 • To describe the security objectives of the TOE and its environment.
- 155 • To specify the security requirements, which include the TOE security
156 functional requirements as of CC part 2 and the assurance requirements as
157 of CC part 3.
- 158 • To setup the TOE summary specification that includes the TOE security
159 function specifications and the assurance measures.

160 **1.3 CC Conformance Claims**

161 This ST is claimed to be conformant with the Common Criteria Version 2.3
162 ([CC]):

- 163 • Security functional requirements are conformant to CC Part 2 extended
164 (extended requirements have been introduced for the underlying platform in
165 [JCOP41V231]).
- 166 • Security assurance requirements are conformant to CC Part 3 augmented:
167 EAL4 augmented by AVA_VLA.4 (highly resistant) and ADV_IMP.2
168 (implementation of the TSF).

169 The minimum strength level of the TOE security functions is SOF-high.

170 Concerning the use of random numbers additionally conformance to [AIS20],
171 class K3, SOF-high is claimed ([JCOP41V231ST] already does so for the
172 underlying platform).

173 This Security Target claims conformance to [JCSPP], Minimal Configuration
174 ([JCOP41V231ST] already does so for the underlying platform).

175 2 TOE Description

176 2.1 Overview

177 TSAM, the TOE, is short for Terminal Security Access Module and, as its name
178 implies, TSAM helps secure transactions in-between POS terminal and the
179 remote host application in a way that it assures integrity, authenticity and
180 confidentiality of POS transactions by encryption, decryption and MAC
181 generation. The functions of TSAM come as follows.

- 182 1. TSAM is provisioned with a management key, an encryption key, a
183 decryption key and a MAC generation key.
- 184 2. POS terminal is equipped with a TSAM in one of its slots. The terminal asks
185 for data encryption from TSAM when it is submitting a transaction to the
186 remote host. The terminal performs encryption of sensitive part of the
187 transaction message by sending it to TSAM via "Data Encryption by
188 Working Key" command and TSAM responds with encrypted datagram. The
189 terminal can also perform decryption of encrypted part of the received
190 transaction message by sending it to TSAM via "Data Decryption by
191 Working Key" command and TSAM responds with decrypted result.
- 192 3. By using TSAM, the terminal calculates MAC for each transaction. The
193 terminal prepares the transaction representation from the transaction
194 message and sends the transaction representation to TSAM via "Generate
195 MAC by Working Key" command. TSAM responds with a MAC over the
196 data it receives from its interface.
- 197 4. TSAM is managed by the remote host, which means the management key
198 and working keys (encryption, decryption and MAC) are subject to be
199 changed over time via online transaction. The key management must be
200 secure, and therefore, there is a unique management key for each TSAM
201 so that the remote host can assure the integrity, confidentiality and, of
202 course, authenticity of the key management process.

203 To sum up, the security relevant functions provided by TSAM help the remote
204 host to assure that every transaction comes from a terminal, equipped with an
205 authentic TSAM, is kept confidential and is not modified.

206 2.2 TOE Definition

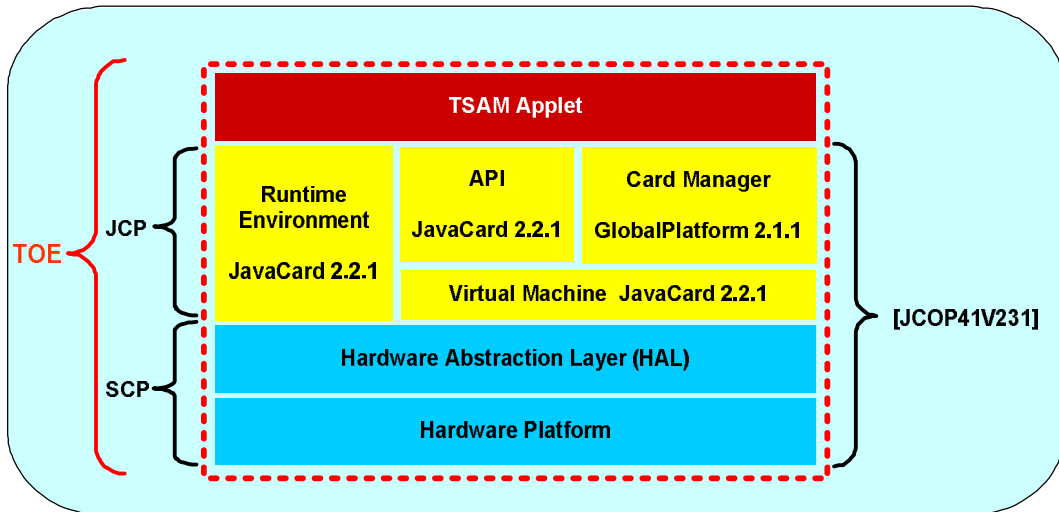
207 The TOE is composed of a JavaCard applet (TSAM applet) and NXP
208 P541G072V0P smart card controller ([JCOP41V231]), see Figure 2-1. While
209 JCP (JavaCard Platform) resides in ROM, the "TSAM Applet" resides in
210 EEPROM of [JCOP41V231].

211 [JCOP41V231] has been evaluated before as referred to [JCOP41V231ST] and
212 the respective certification report (BSI-DSZ-CC-0426). The TSAM applet is
213 loaded and installed into [JCOP41V231], and therefore, the TOE is a
214 composition of the TSAM applet and [JCOP41V231]. The GlobalPlatform keys
215 necessary for applet management are not delivered together with the TOE,
216 therefore it will not be possible to delete the TSAM applet from or install
217 additional applets into the smart card controller after delivery.

219

221

223



224

Figure 2-1: The TOE architecture

225

226

227

More information about the structure of JCP (JavaCard Platform) and SCP (Smart Card Platform) can be found in [JCOP41V231ST].

228

2.3 TOE Boundaries

229

2.3.1 TOE Physical Scope and Boundary

230

231

232

The physical boundary of the TOE is represented by the surface of [JCOP41V231]. This surface and the embedded physical interface are compliant to ISO 7816 part 2.

233

234

While JCP (JavaCard Platform) resides in ROM, the TSAM applet resides in EEPROM of [JCOP41V231].

235

236

237

238

239

240

241

242

[JCOP41V231] provides different external interfaces and corresponding protocols. In the TOE only the contact interface and only the corresponding protocol T=1 are available. Contactless interface and USB 2.0 interface implementations of [JCOP41V231] are physically present in the TOE, but are not usable, as these interfaces are not contacted and as the corresponding protocols T=CL, MIFARE and USB protocol are disabled in the TOE (disabled MIFARE part physically present in [JCOP41V231] is not shown in Figure 2-1.hereinabove).

243

244

245

In broadest sense also the guidance documentation can be seen as part of the physical scope of the TOE (see section 1.1 hereinbefore for a detailed reference).

246

2.3.2 TOE Logical Scope and Boundary

247

248

249

250

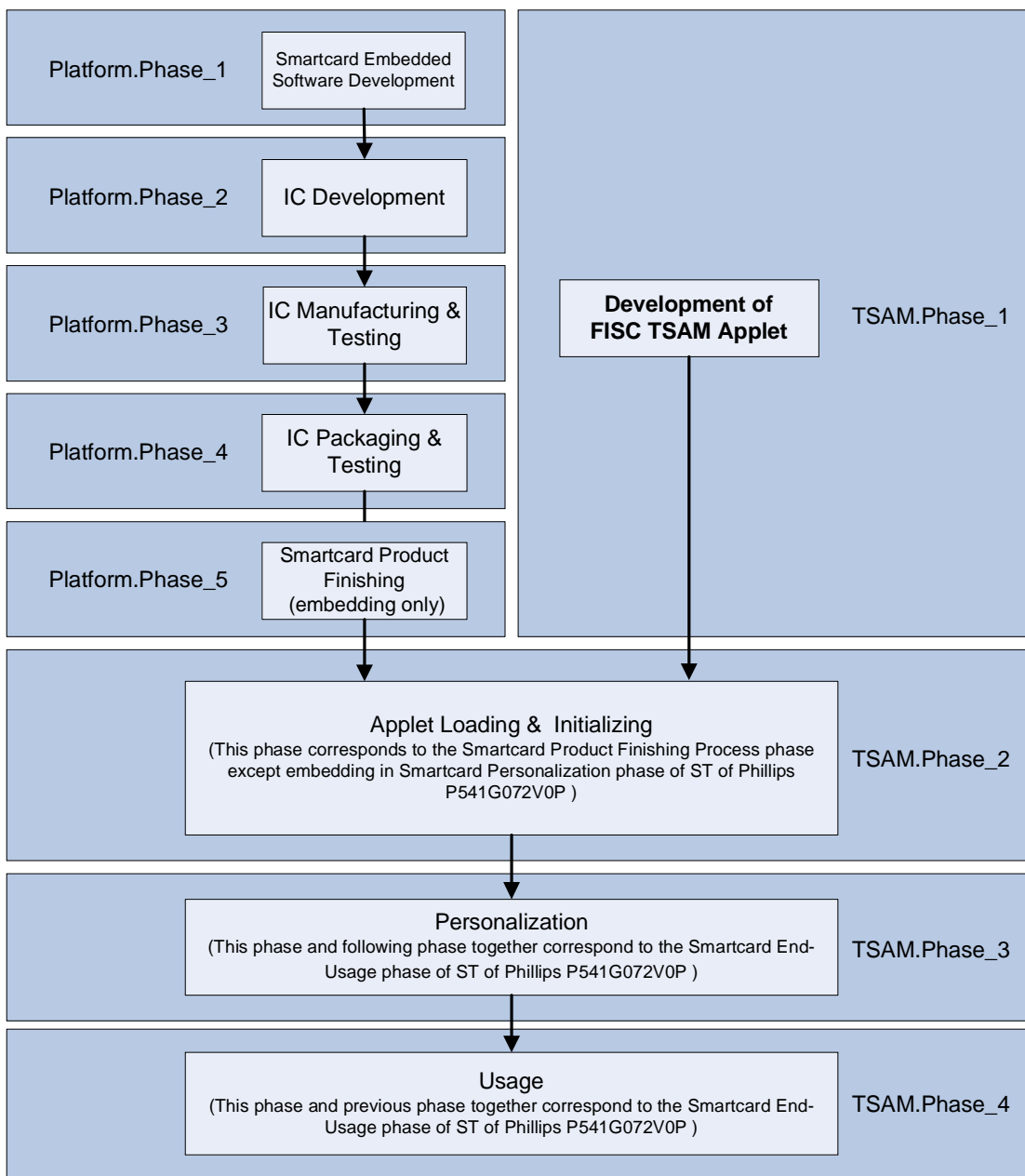
The TOE logical interface is represented by a set of APDU commands which are compliant to ISO 7816 part 4 (augmented with additional commands). At its logical boundary, the TOE provides functions of encryption, decryption, MAC generation and secure key updates.

251

The TOE provides the following security functionalities:

- 252 • encryption, decryption and MAC generation,
- 253 • user authentication,
- 254 • confidentiality and integrity protection of communication data,
- 255 • access control,
- 256 • life cycle management,
- 257 • stored data integrity protection, and
- 258 • increments of serial numbers.

259 **2.4 TOE Life Cycle**



260

261

Figure 2-2: TOE life cycle

262

Figure 2-2 shows development phases and operation phases of the TSAM TOE. Each of the phases is described as follows:

263

264 **Platform.Phase_1:** This phase corresponds to the Smartcard Embedded
265 Software Development phase of [JCOP41V231ST].

266 **Platform.Phase_2:** This phase corresponds to the IC Development phase of
267 [JCOP41V231ST].

268 **Platform.Phase_3:** This phase corresponds to the IC Manufacturing and Test
269 phase of [JCOP41V231ST].

270 **Platform.Phase_4:** This phase corresponds to the IC Packaging and Test of
271 [JCOP41V231ST].

272 **Platform.Phase_5:** This phase corresponds to the embedding part of the
273 Smartcard Product Finishing Process phase of [JCOP41V231ST]. Embedded
274 smartcard products, together with the GlobalPlatform keys, are delivered to the
275 TSAM production site in TSAM.Phase_2.

276 **TSAM.Phase_1:** This phase consists of the development of the TSAM applet
277 which is to be loaded and installed into [JCOP41V231]. GlobalPlatform keys of
278 [JCOP41V231] are needed for performing the loading and installing of the
279 TSAM applet. TSAM applet is delivered to the TSAM production site in
280 TSAM.Phase_2.

281 **TSAM.Phase_2:** This phase, together with Platform.Phase_5, corresponds to
282 the Smartcard Product Finishing Process phase of [JCOP41V231ST]. It
283 consists of the process for loading, installing and initializing the TSAM applet
284 with GlobalPlatform keys. This is done at the production site. After loading and
285 installation of the TSAM applet, the TOE is completed and its security
286 functionality is operative. During subsequent initialization, which is already
287 under control of TSAM's security functionality, the initial management key is
288 written, which is necessary for personalization. The initialized TOE and the
289 corresponding initial management key are delivered to the TSAM issuer site
290 (see TSAM.Phase_3).

291 **TSAM.Phase_3:** This phase, together with TSAM.Phase_4, corresponds to the
292 Smartcard End-usage phase of [JCOP41V231ST]. It consists of personalization
293 process of TSAM by the issuer, which includes doing the mandatory first update
294 of the management key and writing of terminal management data. The process
295 is done at TSAM issuer site.

296 **TSAM.Phase_4:** This phase, together with TSAM.Phase_3, corresponds to the
297 Smartcard End-usage phase of [JCOP41V231ST]. At this phase, POS terminal
298 uses TSAM for security operations. The TSAM issuer, at this phase, can do
299 update of the management key and writes of working keys.

300 **2.5 Roles**

301 **R.Initializer:** This is the role that instantiates and initializes the TSAM TOE.
302 This role belongs to the production environment of the TOE, nevertheless,
303 initialization of the management key is already controlled by TOE functionality.

304 **R.Issuer:** This is the user who issues TOE and performs management of the
305 management key, working keys and terminal management data.

306 **R.POS_Terminal:** This is the device that uses the TOE for data encryption,
307 decryption and MAC generation for POS transactions. The user guidance for
308 this role will be addressed to the developer of the POS terminal.

309 **3 TOE Security Environment**

310 **3.1 Assets**

311 The TSAM applet corresponds to D.APP_CODE asset of the ST of NXP
312 P541G072V0P [JCOP41V231ST].

313 The following assets are corresponding to D.APP_C_DATA (confidential
314 sensitive data of the TSAM applet), D.APP_I_DATA (integrity sensitive data of
315 the TSAM applet) and D.APP_KEYS (cryptographic keys owned by the TSAM
316 applet) of [JCOP41V231ST].

317 3.1.1 GlobalPlatform Keys (GPKs)

318 GlobalPlatform keys are 3/DES keys that are used by R.Initializer to protect
319 loading and installing of the TSAM applet by security functionalities of
320 [JCOP41V231].

321 GPKs also protect initialize of management key (see section 3.1.2 below). This
322 takes place during production; nevertheless, it is already controlled by security
323 functionalities of the TOE.

324 The TOE has to maintain the integrity and confidentiality of GPKs (this is
325 solely provided by functionality of [JCOP41V231]).

326 GPKs are not delivered with the TOE.

327 3.1.2 Management Key (MK)

328 Management key is a 3/DES key that's used by R.Issuer to protect writes of
329 working keys (see section 3.1.3 below) and key updates of MK itself. It
330 protects key updates and writes in a way that the confidentiality, integrity and
331 authenticity are assured. MK also protects writes of terminal management data
332 (see section 3.1.4 below) of the TOE in a similar way that the integrity and
333 authenticity of the data are assured.

334 MK is written into the EEPROM of [JCOP41V231] in TSAM.Phase_2. It can be
335 updated in TSAM.Phase_3 and TSAM.Phase_4. The TOE has to maintain the
336 integrity and confidentiality of MK.

337 3.1.3 Working Keys (WKs)

338 Working keys are 3/DES keys that are stored in the EEPROM of
339 [JCOP41V231]. There are three WKs in the TOE, which are encryption key,
340 decryption key and MAC generation key. The R.POS_Terminal requests for
341 cryptographic services supported by the TOE to encrypt, decrypt and/or
342 generate MACs over transaction data (see section 3.1.7) by the encryption key,
343 decryption key and/or MAC generation key, respectively.

344 WKs can be written during TSAM.Phase_4. The TOE has to maintain the
345 integrity and confidentiality of any of the WKs.

346 3.1.4 Terminal Management Data (TMD)

347 TMD is composed of a merchant identifier (MID), a terminal identifier (TID), a
348 transaction serial number (TSN) and a batch settlement number (BSN).

349 TMD is stored in the EEPROM of [JCOP41V231] in TSAM.Phase_3. In
350 TSAM.Phase_4, TMD can be read out of the TOE, and TSN and BSN can be
351 incremented.

352 The TOE has to ensure the integrity of TMD during writes and storage.

353 3.1.5 Retry Counter (RC)

354 This is TSF data which is the counter for accumulative consecutive failure
355 attempts of external authentication with MK. Whenever RC reaches 3, no
356 further attempts of external authentication with MK will be allowed. In this case,
357 there is no way to reset RC. The integrity of RC must be maintained by the
358 TOE.

359 3.1.6 Life Cycle State (LCS)

360 This is TSF data which is used to manage life cycle state of the TOE. The life
361 cycle state of the TOE starts from TSAM.Phase_2, changes to
362 TSAM.Phase_3 and finally changes to TSAM.Phase_4 subsequently. The
363 change of the life cycle state is irreversible. The integrity of LCS must be
364 maintained by the TOE.

365 3.1.7 Transaction Data (TD)

366 This is user data that the TOE receives from its interface. The data can be
367 subject to encryption, decryption, or MAC generation with the corresponding
368 WK. The data is not stored permanently in the TOE.

369 3.2 Assumptions (about the environment)

370 The following set of assumptions incorporates those assumptions made in
371 [JCOP41V231ST], which are still relevant for this composite TOE. Some of the
372 assumptions made in [JCOP41V231ST] are covered by development and
373 production of TSAM and are therefore not listed here. Please see remarks after
374 list of assumptions below and PP claims rationale in section 8.4 hereinafter.

375 **A.DLV** *Delivery of TOE and its guidance documents*

376 It is assumed that R.Issuer and the developer of the R.POS_Terminal verify the
377 hash values of their guidance documents to assure a secure delivery of it. It is
378 also assumed that R.Issuer will only issue the TOE after a correct MAC
379 verification of the delivered management key.

380 **A.USE_DIAG** *Use of secure communication protocols*

381 It is assumed that the environment supports and uses secure communication
382 protocols offered by the TOE.

383 **A.KEYS** *Key protection and key quality*

384 The management key and working keys which are stored and processed
385 outside the TOE during personalization and usage phases are assumed to be
386 protected for confidentiality and integrity.

387 Cryptographic keys created in the environment to be used within the TOE have
388 to have sufficient quality (e.g. by using a random number generator for key
389 generation).

390 **A.DEV** *Development security*

391 It is assumed that no native codes will be loaded into [JCOP41V231] during
392 development and production phases of the TOE. During development, byte
393 code verification will be performed on the TSAM applet. During production, only
394 TSAM applet will be installed. GlobalPlatform keys are not delivered to R.Issuer
395 and R.POS_Terminal.

396 It is also assumed that TOE development and test information during
397 TSAM.Phase_1 and TSAM.Phase_2 is protected in a secure environment for its
398 integrity and confidentiality. In case of delivery between different actors like
399 applet developers and applet installers, this information is also protected in the
400 same manner as aforementioned.

401 **Remarks:**

402 | A.DLV covers the assumption A.DLV_PROTECT of [JCOP41V231ST]
403 because the procedures addressed in A.DLV_PROTECT are mostly covered
404 by evaluation of development security, configuration management and
405 delivery for the TOE. A.DLV assumes the remaining responsibilities of the
406 users of the TOE to ensure a complete secure delivery process.

407 | A.TEST_OPERATE of [JCOP41V231ST] is completely covered by the
408 evaluation of development security, configuration management and delivery
409 for the TOE because the development and production of the TOE covers
410 phases of 4, 5 and 6 of [JCOP41V231ST].

411 | A.USE_DIAG is a mere re-statement of the assumption A.USE_DIAG of
412 [JCOP41V231ST].

413 | A.KEYS directly covers the assumption A.USE_KEYS of [JCOP41V231ST]
414 with additional refinements and extensions.

415 | A.NATIVE of [JCOP41V231ST] is covered by A.DEV because in the scope of
416 the TSAM production and operation no native code will be loaded into the
417 smart card controller.

418 | A.NO-DELETION and A.NO-INSTALL of [JCOP41V231ST] are covered by
419 A.DEV because the necessary GlobalPlatform keys are not delivered to the
420 R.Issuer and the R.POS_Terminal (or its developer), therefore it will not be
421 possible to delete the TSAM applet from or install additional applets into the
422 smart card controller.

423 | A.VERIFICATION of [JCOP41V231ST] is covered by A.DEV because byte
424 code verification will be performed during the development/production.

425 **3.3 Threats**

426 This section introduces the threats to the assets against which specific protection
427 within the TOE or its environment is required. It is assumed that all attackers have
428 high level of expertise, opportunity and resources. In [JCOP41V231ST], general
429 threats for smart card native operating systems were defined and supplemented by

430 Java Card specific threats from [JCSPP] (see section 3.3.2). Additionally in section
431 3.3.1 hereinafter the TSAM-specific threats are listed.

432 3.3.1 Threats not contained in [JCOP41V231ST]

433 **T.INTEGRITY** *Integrity of security relevant data*

434 An attacker or memory errors may change MK, WK, TMD, LCS and RC in
435 storage without the TOE being able to detect it, which leads to usage of
436 corrupted data.

437 **T.TMD_ACCESS** *Access to terminal management data*

438 An unauthorized user, other than R.Issuer, may perform writes of TMD. One
439 possibility would be that the unauthorized user records authorized update of
440 TMD during communication and resends it to the TOE (replay attack).

441 The TMD of an authorized update may be modified during communication but
442 the TOE does not detect the modification.

443 **T.KEY_ACCESS** *Access to MK and WK*

444 An unauthorized user, other than R.Initializer, may perform initialize of MK. An
445 unauthorized user, other than R.Issuer, may perform updates of MK or writes
446 of WKs. One possibility would be that the unauthorized user records
447 authorized initialize and updates of MK or writes of WK during communication
448 and resends them to the TOE (replay attack).

449 The MK or WKs of an authorized initialize, update or write may be modified
450 during communication but the TOE does not detect the modification.

451 An attacker may eavesdrop on the MK or WK during communication to get the
452 key value that's being used by the TOE. An attacker or a user may try to read
453 the MK or WKs from the TOE's user visible interfaces. An attacker may also
454 try to gain previous values of MK or WKs from the TOE.

455 3.3.2 Threats from [JCOP41V231ST]

456 The following threats have already been regarded during development and
457 manufacturing of [JCOP41V231] as confirmed by the corresponding
458 evaluation.

459 The threats listed here are just a brief summary. For corresponding
460 explanation and application note, please see [JCOP41V231ST].

461 Table 3-1 identifies the threats that are found in [JCOP41V231ST]. The
462 Source column of the table indicates the source protection profile, if there is
463 any, in which the corresponding threat is specified. The Life-Cycle column of
464 the table indicates the phases of the TOE life cycle in which the corresponding
465 threat can take place. Detailed explanation of the phases can be found in
466 section 2.4.

Name	Source	Life-Cycle
T.DEV_IC	-	Platform.Phase_2, Platform.Phase_3
T.DEV_NOS	-	Platform.Phase_1

Name	Source	Life-Cycle
T.DEL_IC_NOS	-	Platform.Phase_1, Platform.Phase_2
T.DEL	-	Platform.Phase_4, TSAM.Phase_2
T.ACCESS_DATA	-	TSAM.Phase_3, TSAM.Phase_4
T.OS_OPERATE	-	TSAM.Phase_3, TSAM.Phase_4
T.OS_DECEIVE	-	TSAM.Phase_3, TSAM.Phase_4
T.LEAKAGE	-	TSAM.Phase_3, TSAM.Phase_4
T.FAULT	-	TSAM.Phase_3, TSAM.Phase_4
T.RND	[PP0002]	TSAM.Phase_3, TSAM.Phase_4
T.PHYSICAL	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.CONFID-JCS-CODE	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.CONFID-APPLI-DATA	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.CONFID-JCS-DATA	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.INTEG-APPLI-CODE	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.INTEG-JCS-CODE	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.INTEG-APPLI-DATA	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.INTEG-JCS-DATA	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.SID.1	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.SID.2	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.EXE-CODE.1	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.EXE-CODE.2	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.NATIVE	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4
T.RESOURCES	[JCSPP]	TSAM.Phase_3, TSAM.Phase_4

467

Table 3-1: Threats from [JCOP41V231ST]

468

T.DEV_IC

469

Theft, modification, disclosure of information related to IC development and manufacturing.

470

471

T.DEV_NOS

472

Theft, modification, or disclosure of NOS related information during NOS development.

473

474

T.DEL_IC_NOS

475

Theft, modification, disclosure of information related to IC or NOS during delivery between IC manufacturer and NOS Developer.

476

477

T.DEL

478

Theft, modification, disclosure of information related to TOE during delivery to IC packaging manufacturer or Smart Card manufacturer or personalizer.

479

- 480 **T.ACCESS_DATA**
- 481 Unauthorized access to sensitive information stored in memories in order to
482 disclose or to corrupt the TOE data (TSF and user data).
- 483 **T.OS_OPERATE**
- 484 Modification of the correct NOS behavior by unauthorized use of TOE or use
485 of incorrect or unauthorized instructions or commands or sequence of
486 commands, in order to obtain an unauthorized execution of the TOE code.
- 487 **T.OS_DECEIVE**
- 488 Modification of the expected TOE configuration by
- 489 ○ unauthorized loading of code,
- 490 ○ unauthorized execution of code
- 491 ○ unauthorized modification of code behavior
- 492 **T.LEAKAGE**
- 493 An attacker may exploit information which is leaked from the TOE during
494 usage of the Smart Card in order to disclose the confidential primary assets.
- 495 **T.FAULT**
- 496 An attacker may cause a malfunction of TSF or of the Smart Card embedded
497 NOS by applying environmental stress in order to (1) deactivate or modify
498 security features or functions of the TOE or (2) deactivate or modify security
499 functions of the Smart Card embedded NOS. This may be achieved by
500 operating the Smart Card outside the normal operating conditions
- 501 **T.RND**
- 502 Deficiency of Random Numbers: An attacker may predict or obtain information
503 about random numbers generated by the TOE for instance because of a lack
504 of entropy of the random numbers provided.
- 505 **T.PHYSICAL**
- 506 The attacker discloses or modifies the design of the TOE, its sensitive data
507 (TSF and User Data) or application code or disables security features of the
508 TOE using pure invasive, physical (opposed to logical) attacks on the
509 hardware part of the TOE.
- 510 **T.CONFID-JCS-CODE**
- 511 The attacker executes an application without authorization to disclose the Java
512 Card System code.
- 513 **T.CONFID-APPLI-DATA**
- 514 The attacker executes an application without authorization to disclose data
515 belonging to another application.

516 **T.CONFID-JCS-DATA**
517 The attacker executes an application without authorization to disclose data
518 belonging to the Java Card System.

519 **T.INTEG-APPLI-CODE**
520 The attacker executes an application to alter (part of) its own or another
521 application's code.

522 **T.INTEG-JCS-CODE**
523 The attacker executes an application to alter (part of) the Java Card System
524 code.

525 **T.INTEG-APPLI-DATA**
526 The attacker executes an application to alter (part of) another application's
527 data.

528 **T.INTEG-JCS-DATA**
529 The attacker executes an application to alter (part of) Java Card System or
530 API data.

531 **T.SID.1**
532 An applet impersonates another application, or even the JCRE, in order to
533 gain illegal access to some resources of the card or with respect to the end
534 user or the terminal.

535 **T.SID.2**
536 The attacker modifies the identity of the privileged roles.

537 **T.EXE-CODE.1**
538 An applet performs an unauthorized execution of a method.

539 **T.EXE-CODE.2**
540 An applet performs an unauthorized execution of a method fragment or
541 arbitrary data.

542 **T.NATIVE**
543 An applet tries to execute a native method to bypass some security function
544 such as the firewall.

545 **T.RESOURCES**
546 An attacker prevents correct operation of the Java Card System through
547 consumption of some resources of the card: RAM or NVRAM.

548 **3.4 Organisational Security Policies (OSP)**

549 3.4.1 OSPs not contained in [JCOP41V231ST]

550 **OSP.TXN_SECURE**

551 The TOE has to provide a function to encrypt, decrypt or generate a MAC over
552 TD with the corresponding WK using 3/DES. No external authentication
553 against the TOE is necessary before the TOE's performing such function.

554 **OSP.SN**

555 The TOE has to provide a function to increment TSN and/or BSN of TMD. The
556 increment of TSN and/or BSN shall not exceed specified limits. No external
557 authentication against the TOE is necessary before the TOE's performing
558 such function.

559 3.4.2 OSPs from [JCOP41V231ST]

560 The following OSP has already been regarded during development and
561 manufacturing of [JCOP41V231] as confirmed by the corresponding
562 evaluation.

563 **OSP.IC_ORG**

564 Procedures dealing with physical, personnel, organizational, technical
565 measures for the confidentiality and integrity, of Smart Card Native Operating
566 System (e.g. source code mask and any associated documents) and IC
567 Manufacturer proprietary information (tools, software, documentation, dies ...)
568 shall exist and be applied in IC development and manufacturing .

569 Procedures shall also ensure the confidentiality and integrity and information
570 during exchange with the NOS developer.

571 4 Security Objectives

572 4.1 Security Objectives for the TOE

573 4.1.1 Security Objectives not contained in [JCOP41V231ST]

574 **SO.KEY_ACCESS** *Secure access to MK and WKs*

575 The TOE has to provide a secure mechanism for R.Initializer to initialize MK.
576 The TOE has to provide a secure mechanism for R.Issuer to perform updates
577 of MK and writes of WKs. This includes mechanisms to ensure the
578 confidentiality and integrity of the keys transferred to the TOE as well as the
579 authentication of R.Initializer or R.Issuer who sends the keys.

580 Nobody shall be able to read out the MK and WKs. The TOE shall provide
581 safe destruction techniques for the cryptographic keys in case of key updates.

582 **SO.TMD_ACCESS** *Secure access to TMD*

583 The TOE has to provide a secure mechanism for R.Issuer to write TMD. This
584 includes mechanisms to ensure the integrity of the TMD transferred to the
585 TOE as well as the authentication of R.Issuer who sends the TMD.

586 **SO.REPLAY** *Replay protection in key access and TMD access*

587 The TOE has to provide a secure mechanism to assure the same command
588 data used in MK initialize and update, WK write and TMD write cannot be used
589 successfully at the second time.

590 **SO.TXN_SECURE** *Cryptographic algorithm security for TD*

591 On request of R.POS_Terminal, the TOE uses 3/DES to encrypt, decrypt or
592 generate MAC over TD with the corresponding WK.

593 **SO.SN** *Increments of TSN and BSN*

594 On request of R.POS_Terminal, the TOE increments TSN and/or BSN of TMD
595 without exceeding specified limits.

596 **SO.INTEGRITY** *Integrity error detection*

597 The TOE protects RC, LCS and TMD in its storage against undetected
598 modifications by an attacker or due to memory errors. On detection of integrity
599 errors, the following actions shall be performed:

- 600 | Prohibit the use of the altered data.
- 601 | Inform the user about integrity errors.

602 Remark:

603 Integrity protection for MK and WKs is provided by [JCOP41V231] already, as
604 its key objects holding MK and WKs are integrity-protected (this is reflected by
605 O.PROTECT_DATA of [JCOP41V231ST], see following section).

606 4.1.2 Security Objectives from [JCOP41V231ST]

607 The following security objectives have already been regarded during
 608 development and manufacturing of [JCOP41V231] as confirmed by the
 609 corresponding evaluation.

610 The security objectives listed here are just a brief summary. For corresponding
 611 explanation and application note, please see [JCOP41V231ST].

612 Table 4-1 identifies the security objectives that are found in [JCOP41V231ST].
 613 The Source column of the table indicates the source protection profile, if there
 614 is any, in which the corresponding security objective is specified.

Name	Source	Name	Source
O.PROTECT_DATA	-	O.SHARD_VAR_CONFID	[JCSPP]
O.SIDE_CHANNEL	-	O.SHARD_VAR_INTEG	[JCSPP]
O.OS_DECEIVE	-	O.ALARM	[JCSPP]
O.FAULT_PROTECT	-	O.TRANSACTION	[JCSPP]
O.PHYSICAL	-	O.CIPHER	[JCSPP]
O.RND	[PP0002]	O.PIN-MNGT	[JCSPP]
O.SID	[JCSPP]	O.KEY-MNGT	[JCSPP]
O.OPERATE	[JCSPP]	O.CARD-MANAGEMENT	[JCSPP]
O.RESOURCES	[JCSPP]	O.SCP.RECOVERY	[JCSPP]
O.FIREWALL	[JCSPP]	O.SCP.SUPPORT	[JCSPP]
O.NATIVE	[JCSPP]	O.SCP.IC	[JCSPP]
O.REALLOCATION	[JCSPP]		

615 **Table 4-1: Security objectives from [JCOP41V231ST]**

616 **O.PROTECT_DATA**

617 The TOE shall ensure that sensitive information stored in memories is
 618 protected against unauthorized disclosure and any corruption or unauthorized
 619 modification. Moreover, the TOE shall ensure that sensitive information stored
 620 in memories is protected against unauthorized access. The TOE has to
 621 provide appropriate security mechanisms to avoid fraudulent access to any
 622 sensitive data, such as passwords, cryptographic keys or authentication data.

623 **O.SIDE_CHANNEL**

624 The TOE must provide protection against disclosure of primary assets
 625 including confidential data (User Data or TSF data) stored and/or processed in
 626 the Smart Card IC by measurement and analysis of the shape and amplitude
 627 or by measurement and analysis of the time between events found by
 628 measuring signals (for example on the power, clock, or I/O lines).

629 **O.OS_DECEIVE**

630 The TOE must guarantee that only secure values are used for its management
 631 and operations, especially system flags or cryptographic assets.

632 Moreover, the integrity of the whole TOE including the NOS must be
633 guaranteed to prevent disclosing/bypassing of the NOS mechanisms or
634 modifying the expected NOS behavior (for instance, unauthorized code patch,
635 or rewriting).

636 **O.FAULT_PROTECT**

637 The TOE must ensure its correct operation even outside the normal operating
638 conditions where reliability and secure operation has not been proven or
639 tested. This is to prevent errors. The environmental conditions may include
640 voltage, clock frequency, temperature, or external energy fields that can be
641 applied on all interfaces of the TOE (physical or electrical).

642 **O.PHYSICAL**

643 The TOE hardware provides the following protection against physical
644 manipulation of the IC, and prevent reverse-engineering (understanding the
645 design and its properties and functions), physical access to the IC active
646 surface (probing) allowing unauthorized memory content disclosure,
647 manipulation of the hardware security parts (e.g. sensors, cryptographic
648 engine or RNG) or manipulation of the IC, including the embedded NOS and
649 its application data (e.g. lock and life cycle status, authentication flags, etc.).

650 **O.RND**

651 The TOE will ensure the cryptographic quality of random number generation.
652 For instance random numbers shall not be predictable and shall have
653 sufficient entropy.

654 The TOE will ensure that no information about the produced random numbers
655 is available to an attacker since they might be used for instance to generate
656 cryptographic keys.

657 **O.SID**

658 The TOE shall uniquely identify every subject (applet, or package) before
659 granting him access to any service.

660 **O.OPERATE**

661 The TOE must ensure continued correct operation of its security functions.
662 Especially, the TOE must prevent the unauthorized use of TOE or use of
663 incorrect or unauthorized instructions or commands or sequence of
664 commands.

665 **O.RESOURCES**

666 The TOE shall control the availability of resources for the applications.

667 **O.FIREWALL**

668 The TOE shall ensure controlled sharing of data containers owned by applets
669 of different packages, and between applets and the TSFs.

670 **O.NATIVE**

671 The only means that the JCVM shall provide for an application to execute
672 native code is the invocation of a method of the Java Card API, or any
673 additional API.

674 **O.REALLOCATION**

675 The TOE shall ensure that the re-allocation of a memory block for the runtime
676 areas of the JCVM does not disclose any information that was previously
677 stored in that block.

678 **O.SHRD_VAR_CONFID**

679 The TOE shall ensure that any data container that is shared by all applications
680 is always cleaned after the execution of an application. Examples of such
681 shared containers are the APDU buffer, the byte array used for the invocation
682 of the process method of the selected applet, or any public global variable
683 exported by the API.

684 **O.SHRD_VAR_INTEG**

685 The TOE shall ensure that only the currently selected application may grant
686 write access to a data memory area that is shared by all applications, like the
687 APDU buffer, the byte array used for the invocation of the process method of
688 the selected applet, or any public global variable exported by the API. Even
689 though the memory area is shared by all applications, the TOE shall restrict
690 the possibility of getting a reference to such memory area to the application
691 that has been selected for execution. The selected application may decide to
692 temporarily hand over the reference to other applications at its own risk, but
693 the TOE shall prevent those applications from storing the reference as part of
694 their persistent states.

695 **O.ALARM**

696 The TOE shall provide appropriate feedback information upon detection of a
697 potential security violation.

698 **O.TRANSACTION**

699 The TOE must provide a means to execute a set of operations atomically.

700 **O.CIPHER**

701 The TOE shall provide a means to cipher sensitive data for applications in a
702 secure way. In particular, the TOE must support cryptographic algorithms
703 consistent with cryptographic usage policies and standards.

704 **O.PIN-MNGT**

705 The TOE shall provide a means to securely manage PIN objects.

706 **O.KEY-MNGT**

707 The TOE shall provide a means to securely manage cryptographic keys. This
708 concerns the correct generation, distribution, access and destruction of
709 cryptographic keys.

710 **O.CARD-MANAGEMENT**

711 The card manager shall control the access to card management functions
712 such as the installation, update or deletion of applets. It shall also implement
713 the card issuer's policy on the card.

714 **O.SCP.RECOVERY**

715 If there is a loss of power, or if the smart card is withdrawn from the CAD while
716 an operation is in progress, the SCP must allow the TOE to eventually
717 complete the interrupted operation successfully, or recover to a consistent and
718 secure state.

719 **O.SCP.SUPPORT**

720 The SCP shall provide functionalities that support the well-functioning of the
721 TSFs of the TOE (avoiding they are bypassed or altered) and by controlling
722 the access to information proper of the TSFs. In addition, the smart card
723 platform should also provide basic services which are required by the runtime
724 environment to implement security mechanisms such as atomic transactions,
725 management of persistent and transient objects and cryptographic functions.
726 These mechanisms are likely to be used by security functions implementing
727 the security requirements defined for the TOE.

728 **O.SCP.IC**

729 The SCP shall possess IC security features.

730 **4.2 Security Objectives for the Environment**

731 4.2.1 Security Objectives for The Environment not contained [JCOP41V231ST]

732 **SOE.DLV**

733 R.Issuer and the developer of the R.POS_Terminal shall verify the hash
734 values of their guidance documents as stated in the ST introduction to assure
735 a secure delivery of it. R.Issuer shall only issue the TOE after he could
736 successfully verify the MAC returned by the TOE during first update of MK with
737 the delivered management key.

738 **SOE.USE_DIAG**

739 The environment shall support and use secure communication protocols
740 offered by the TOE.

741 **SOE.KEYS**

742 The management key and working keys which are stored and processed
743 outside the TOE during personalization and usage phases shall be protected
744 for confidentiality and integrity.

745 Cryptographic keys created in the environment to be used within the TOE
746 have to have sufficient quality by using a random number generator for key
747 generation.

748 **SOE.DEV**

749 No native codes shall be loaded into [JCOP41V231] during development and
 750 production phases of the TOE. During development, byte code verification
 751 shall be performed on the TSAM applet. During production, only TSAM applet
 752 shall be installed. GlobalPlatform keys shall be not delivered to R.Issuer and
 753 R.POS_Terminal.

754 TOE development and test information during TSAM.Phase_1 and
 755 TSAM.Phase_2 shall be protected in a secure environment for its integrity and
 756 confidentiality. In case of delivery between different actors like applet
 757 developers and applet installers, this information shall be also protected in the
 758 same manner as aforementioned.

759 4.2.2 Security Objectives for the Environment from [JCOP41V231ST]

760 The following security objectives for the environment either have been
 761 regarded during development and manufacturing of [JCOP41V231] as
 762 confirmed by the corresponding evaluation or are covered by objectives in
 763 section 4.2.1.

764 Table 4-2 identifies the initial security objectives for the environment from
 765 [JCOP41V231ST]. For the complete details, please refer to [JCOP41V231ST].

Name	Source	Regards to	Remark
OE.DEV_NOS	-	Platform.Phase_1	Regarded by platform evaluation
OE.DEL_NOS	-	Platform.Phase_1	Regarded by platform evaluation
OE.IC_ORG	-	Platform.Phase_2 Platform.Phase_3	Regarded by platform evaluation
OE.DLV_PROTECT	-	Platform.Phase_3 Platform.Phase_4 TSAM.Phase_2 TSAM.Phase_3 TSAM.Phase_4	Covered by SOE.DLV
OE.DLV_DATA	-	Platform.Phase_4 TSAM.Phase_2	Covered by SOE.DEV
OE.TEST_OPERATE	-	Platform.Phase_4 TSAM.Phase_2	Covered by SOE.DEV
OE.USE_DIAG	-	TSAM.Phase_3 TSAM.Phase_4	Covered by SOE.USE_DIAG
OE.USE_KEYS	-	TSAM.Phase_3 TSAM.Phase_4	Covered by SOE.KEYS
OE.NATIVE	[JCSPP]	TSAM_Phase_2	Covered by SOE.DEV
OE.NO-DELETION	[JCSPP]	TSAM_Phase_2	Covered by SOE.DEV
OE.NO-INSTALL	[JCSPP]	TSAM_Phase_2	Covered by SOE.DEV
OE.VERIFICATION	[JCSPP]	TSAM_Phase_1	Covered by SOE.DEV

766 **Table 4-2: Security objectives for the environment from [JCOP41V231ST]**

767 5 Security Requirements

768 The minimum strength of function level for the TOE is claimed to be SOF-high. For
769 random number usage conformance to [AIS20] class K3, SOF-high is claimed.

770 5.1 TOE Security Functional Requirements not contained in [JCOP41V231ST]

771 5.1.1 Cryptographic support (FCS)

772 5.1.1.1 Cryptographic key destruction (FCS_CKM.4/TSAM)

773 FCS_CKM.4.1/TSAM The TSF shall destroy cryptographic keys in accordance with a
774 specified cryptographic key destruction method [*previous MK and WKS*
775 *are physically overwritten by new keys*] that meets the following:
776 [*none*].

777 5.1.1.2 Cryptographic operation (FCS_COP.1/TSAM)

778 FCS_COP.1.1/TSAM The TSF shall perform [*encryption, decryption, MAC generation for TD*
779 *with dedicated keys in TSAM.Phase.4*] in accordance with a specified
780 cryptographic algorithm [*3/DES in ECB or CBC mode*] and
781 cryptographic key sizes [*112 bits*] that meet the following: [*ANSI X 9.52*
782 *TECB for encryption/decryption, ANSI X 9.9 with ANSI X 9.52 TCBC*
783 *Encryption for MAC generation*].

784 5.1.2 User data protection (FDP)

785 5.1.2.1 Subset access control (FDP_ACC.1/KEY and FDP_ACC.1/TMD)

786 FDP_ACC.1.1/KEY The TSF shall enforce the [*Key Access SFP*] on [*subjects: users,*
787 *objects: MK, WKS and operation: initialize, first update, update, write,*
788 *read and use*].

789 FDP_ACC.1.1/TMD The TSF shall enforce the [*TMD Access SFP*] on [*subjects: users,*
790 *objects: TMD and operation: read, write and increment*].

791 Application Note:

792 The operation “use” is applicable to WKS. It means encryption, decryption or MAC generation
793 with the corresponding WK. The operation “increment” is applicable to TSN or BSN of TMD.

794 5.1.2.2 Security attribute based access control (FDP_ACF.1/KEY and FDP_ACF.1/TMD)

795 FDP_ACF.1.1/KEY The TSF shall enforce the [*Key Access SFP*] to objects based on the
796 following: [*subject attribute: user role {R.Initializer, R.Issuer,*
797 *R.POS_Terminal}*] and object attribute: life cycle state { *TSAM.Phase_2,*
798 *TSAM.Phase_3, TSAM.Phase_4*}].

799 FDP_ACF.1.2/KEY The TSF shall enforce the following rules to determine if an operation
800 among controlled subjects and controlled objects is allowed: [
801 1. A user with user role {*R.Initializer*} is allowed to initialize the MK if
802 the life cycle state is {*TSAM.Phase_2*}.
803 2. A user with user role {*R.Issuer*} is allowed to do first update of the
804 MK if the life cycle state is {*TSAM.Phase_3*}.
805 3. A user with user role {*R.Issuer*} is allowed to do updates of the MK
806 if the life cycle state is {*TSAM.Phase_4*}.
807 4. A user with user role {*R.Issuer*} is allowed to do writes of the WK if
808 the life cycle state is {*TSAM.Phase_4*}.

809		5. A user with user role {R.POS_Terminal} is allowed to use the WK if
810		the life cycle state is {TSAM.Phase_4}.
811]
812	FDP_ACF.1.3/KEY	The TSF shall explicitly authorise access of subjects to objects based
813		on the following additional rules: [no other rule].
814	FDP_ACF.1.4/KEY	The TSF shall explicitly deny access of subjects to objects based on
815		the [rule that no user can read any of the MK and WKS out of the TOE].
816		
817	FDP_ACF.1.1/TMD	The TSF shall enforce the [TMD Access SFP] to objects based on the
818		following: [subject attribute: user role {R.Issuer, R.POS_Terminal} and
819		object attribute: life cycle state {TSAM.Phase_3, TSAM.Phase_4}].
820	FDP_ACF.1.2/TMD	The TSF shall enforce the following rules to determine if an operation
821		among controlled subjects and controlled objects is allowed: [
822		1. A user with user role {R.Issuer} is allowed to write TMD if the life
823		cycle state is {TSAM.Phase_3}.
824		2. A user with user role {R.POS_Terminal} is allowed to read TMD
825		and increment TSN/BSN of TMD if the life cycle state is
826		{TSAM.Phase_4}.
827]
828	FDP_ACF.1.3/TMD	The TSF shall explicitly authorise access of subjects to objects based
829		on the following additional rules: [no other rule].
830	FDP_ACF.1.4/TMD	The TSF shall explicitly deny access of subjects to objects based on
831		the [following rules:
832		1. Increment of TSN is denied if the value of TSN is equal to 999999.
833		2. Increment of BSN is denied if the value of BSN is equal to 9999.
834].

835 **Application Note:**

836 R.Initializer and R.Issuer need to be authenticated. R.POS_Terminal doesn't need
837 authentication, i.e., it is an anonymous user.

838 *5.1.2.3 Import of user data without security attributes (FDP_ITC.1/KEY and*
839 *FDP_ITC.1/TMD)*

840	FDP_ITC.1.1/KEY	The TSF shall enforce the [Key Access SFP] when importing user data,
841		controlled under the SFP, from outside of the TSC.
842	FDP_ITC.1.2/KEY	The TSF shall ignore any security attributes associated with the user
843		data when imported from outside the TSC.
844	FDP_ITC.1.3/KEY	The TSF shall enforce the following rules when importing user data
845		controlled under the SFP from outside the TSC: [
846		1. After import of MK by initialize operation, the security attribute life
847		cycle state shall change from TSAM.Phase_2 to TSAM.Phase_3.
848]
849	FDP_ITC.1.1/TMD	The TSF shall enforce the [TMD Access SFP] when importing user
850		data, controlled under the SFP, from outside of the TSC.
851	FDP_ITC.1.2/TMD	The TSF shall ignore any security attributes associated with the user
852		data when imported from outside the TSC.

853 FDP_ITC.1.3/TMD The TSF shall enforce the following rules when importing user data
854 controlled under the SFP from outside the TSC: [
855 1. After import of TMD by write operation, the security attribute life
856 cycle state shall change from TSAM.Phase_3 to TSAM.Phase_4.
857]

858 5.1.2.4 Stored data integrity monitoring and action (FDP_SDI.2/TSAM)

859 FDP_SDI.2.1/TSAM The TSF shall monitor user data stored within the TSC for [*integrity*
860 *errors*] on all objects, based on the following attributes [*checksum for*
861 *TMD, LCS and RC*].

862 FDP_SDI.2.2/TSAM Upon detection of a data integrity error, the TSF shall [*inform the user*
863 *and perform the actions in Table 5-1 depending on which object is*
864 *incurred in the data integrity error*].

Object	Action
RC	No more usage of the MK is allowed (e.g. for authentication).
LCS	Stop operation of the TOE.
TMD	No more read or increment of TMD is allowed.

865 **Table 5-1: Actions on detection of integrity errors**

866 **Application Note:**

867 The integrity status for application keys (MK and Wks) are maintained by [JCOP41V231],
868 which monitors integrity when application keys are accessed and stops operation
869 immediately when detecting a corresponding integrity error (therefore preventing that
870 corrupted MK or Wks can be used).

871 5.1.2.5 Basic data exchange confidentiality (FDP_UCT.1/KEY)

872 FDP_UCT.1.1/KEY The TSF shall enforce the [*Key Access SFP*] to be able to [*receive*]
873 objects in a manner protected from unauthorised disclosure.

874 **Application Note:**

875 This SFR applies to initialize and/or updates of MK and writes of Wks.

876 5.1.2.6 Data exchange integrity (FDP_UIT.1/TSAM)

877 FDP_UIT.1.1/TSAM The TSF shall enforce the [*Key Access SFP and TMD Access SFP*] to
878 be able to [*receive*] user data in a manner protected from [*modification,*
879 *insertion, replay*] errors.

880 FDP_UIT.1.2/TSAM The TSF shall be able to determine on receipt of user data, whether
881 [*modification, insertion, replay*] has occurred.

882 **Application Note:**

883 The TOE can detect modification, insertion or replay, but it is not able to distinguish between
884 them. Concerning Key Access SFP, this SFR applies to initializes and/or updates of MK and
885 writes of Wks. Concerning TMD Access SFP, this SFR applies to writes of TMD.

886 5.1.3 Identification and authentication (FIA)

887 5.1.3.1 Authentication failure handling (FIA_AFL.1/TSAM)

888 FIA_AFL.1.1/TSAM The TSF shall detect when [*three consecutive*] unsuccessful
889 authentication attempts occur related to [*authentication with MK*].

890 FIA_AFL.1.2/TSAM When the defined number of unsuccessful authentication attempts has
891 been met or surpassed, the TSF shall [*no longer allow authentication*
892 *with MK*].

893 5.1.3.2 *Timing of authentication (FIA_UAU.1/TSAM)*

894 FIA_UAU.1.1/TSAM The TSF shall allow [*encryption, decryption and MAC generation by*
895 *corresponding WK, reading TMD, incrementing TSN and/or BSN of*
896 *TMD*] on behalf of the user to be performed before the user is
897 authenticated.

898 FIA_UAU.1.2/TSAM The TSF shall require each user to be successfully authenticated
899 before allowing any other TSF-mediated actions on behalf of that user.

900 5.1.3.3 *Single-use authentication mechanisms (FIA_UAU.4/TSAM)*

901 FIA_UAU.4.1/TSAM The TSF shall prevent reuse of authentication data related to
902 [*GlobalPlatform card manager authentication, authentication with MK*].

903 5.1.3.4 *Multiple authentication mechanisms (FIA_UAU.5/TSAM)*

904 FIA_UAU.5.1/TSAM The TSF shall provide [*GlobalPlatform card manager authentication,*
905 *authentication with MK*] to support user authentication.

906 FIA_UAU.5.2/TSAM The TSF shall authenticate any user's claimed identity according to the
907 [following rules:

- 908 1. *GlobalPlatform card manager authentication is used for*
909 *authentication of R.Initializer in TSAM.Phase_2.*
- 910 2. *Authentication with MK is used for authentication of R.Issuer in*
911 *TSAM.Phase_3 and TSAM.Phase_4*

912].

913 **Application Note:**

914 Although GlobalPlatform card manager authentication and authentication with MK are both
915 based on 3/DES-based challenge-response protocols, FIA_UAU.5 was chosen for the
916 following three reasons:

- 917 1. to explicitly require which authentication mechanism (i.e. based on which key) shall
918 be used for authentication of which user,
- 919 2. because the two authentication mechanisms use two different dedicated external
920 interfaces of TSAM,
- 921 3. because the two authentication mechanisms differ in their realization: whereas for
922 authentication of R.Initializer internally card manager authentication (SF.I&A) of
923 [JCOP41V231] according to FIA_UAU.1 of [JCOP41V231ST] is used, authentication
924 of R.Issuer using MK is solely implemented in TSAM applet (only using cryptographic
925 primitives of the platform).

926 5.1.3.5 *Timing of identification (FIA_UID.1)*

927 FIA_UID.1.1/TSAM The TSF shall allow [*encryption, decryption and MAC generation by*
928 *corresponding WK, reading TMD, incrementing TSN and/or BSN of*
929 *TMD*] on behalf of the user to be performed before the user is
930 identified.

931 FIA_UID.1.2/TSAM The TSF shall require each user to be successfully identified before
932 allowing any other TSF-mediated actions on behalf of that user.

933 5.1.4 Security management (FMT)

934 5.1.4.1 Management of security attributes (FMT_MSA.1/TSAM)

935 FMT_MSA.1.1/TSAM The TSF shall enforce the [Key Access SFP and TMD Access SFP] to
936 restrict the ability to [modify] the security attributes [life cycle state] to
937 [R.Initializer and R.Issuer].

938 5.1.4.2 Secure security attributes (FMT_MSA.2/TSAM)

939 FMT_MSA.2.1/TSAM The TSF shall ensure that only secure values are accepted for security
940 attributes.

941 5.1.4.3 Static attribute initialisation (FMT_MSA.3/TSAM)

942 FMT_MSA.3.1/TSAM The TSF shall enforce the [Key Access SFP and TMD Access SFP] to
943 provide [restrictive] default values for security attributes that are used
944 to enforce the SFP.

945 FMT_MSA.3.2/TSAM The TSF shall allow ~~the~~ [nobody] to specify alternative initial values to
946 override the default values when an object or information is created.

947 **Application Note:**

948 The TSAM TOE operates on one fixed set of objects and can not create additional ones.
949 Therefore, the requirement above is only about the initialization of the security attribute life
950 cycle state. "Restrictive" corresponds to a setting to TSAM.Phase_2, which only allows
951 access by R.Initializer.

952 5.1.4.4 Specification of Management Functions (FMT_SMF.1/TSAM)

953 FMT_SMF.1.1/TSAM The TSF shall be capable of performing the following security
954 management functions: [modification of the life state according to
955 FMT_MSA.1.1/TSAM, FDP_ITC.1.3 /KEY and FDP_ITC.1.3 /TMD].

956 5.1.4.5 Security roles (FMT_SMR.1)

957 FMT_SMR.1.1/TSAM The TSF shall maintain the roles [R.Initializer, R.Issuer and
958 R.POS_Terminal].

959 FMT_SMR.1.2/TSAM The TSF shall be able to associate users with roles.

960 **Application Note:**

961 R.Initializer and R.Issuer need to be authenticated. R.POS_Terminal doesn't need
962 authentication, i.e., it is an anonymous user.

963 5.1.5 Trusted path/channels (FTP)

964 5.1.5.1 Inter-TSF trusted channel (FTP_ITC.1/TSAM)

965 FTP_ITC.1.1/TSAM The TSF shall provide a communication channel between itself and a
966 remote trusted IT product that is logically distinct from other
967 communication channels and provides assured identification of its end
968 points and protection of the channel data from modification or
969 disclosure.

970 FTP_ITC.1.2/TSAM The TSF shall permit [the remote trusted IT product] to initiate
971 communication via the trusted channel.

972 FTP_ITC.1.3/TSAM The TSF shall initiate communication via the trusted channel for
973 [performing initialize, first update, updates and writes of MK, WKs and
974 TMD, as applicable].

975 **5.2 TOE Security Functional Requirements from [JCOP41V231ST]**

976 In the following table the TOE security functional requirements from
 977 [JCOP41V231] are referenced. For details, please refer to [JCOP41V231ST]
 978 section 5.1.

Functional Class	Functional Components	
FAU: Security audit	FAU_ARP.1	Security alarms
	FAU_SAA.1	Potential violation analysis
FCS: Cryptographic support	FCS_CKM.1	Cryptographic key generation
	FCS_CKM.2	Cryptographic key distribution
	FCS_CKM.3	Cryptographic key access
	FCS_CKM.4	Cryptographic key destruction
	FCS_COP.1	Cryptographic operation
	FCS_RND.1	Quality metric for random numbers
FDP: User data protection	FDP_ACC.1	Subset access control
	FDP_ACC.2	Complete access control
	FDP_ACF.1	Security attribute based access control
	FDP_ETC.1	Export of user data without security attributes
	FDP_IFC.1	Subset Information flow control
	FDP_IFF.1	Simple security attributes
	FDP_ITC.1	Import of user data without security attributes
	FDP_RIP.1	Subset residual information protection
	FDP_ROL.1	Basic rollback
	FDP_SDI.2	Stored data integrity monitoring and action
FIA: Identification and authentication	FIA_AFL.1	Authentication failure handling
	FIA_ATD.1	User attribute definition
	FIA_UAU.1	Timing of authentication
	FIA_UAU.3	Unforgeable authentication
	FIA_UAU.4	Single-use authentication mechanisms
	FIA_UID.1	Timing of identification
	FIA_UID.2	User identification before any action
	FIA_USB.1	User-subject binding
FMT: Security management	FMT_LIM.1	Limited capabilities
	FMT_LIM.2	Limited availability
	FMT_MSA.1	Management of security attributes
	FMT_MSA.2	Secure security attributes
	FMT_MSA.3	Static attribute initialization
	FMT_MTD.1	Management of TSF data

Functional Class	Functional Components
	FMT_MTD.3 Secure TSF data
	FMT_SMF.1 Specification of Management Functions
	FMT_SMR.1 Security roles
FPR: Privacy	FPR_UNO.1 Unobservability
FPT: Protection of the TSF	FPT_AMT.1 Abstract machine testing
	FPT_EMSEC.1 TOE Emanation
	FPT_FLS.1 Failure with preservation of secure state
	FPT_PHP.1 Passive detection of physical attack
	FPT_PHP.3 Resistance to physical attack
	FPT_RVM.1 Reference mediation
	FPT_SEP.1 TSF domain separation
	FPT_RCV.3 Trusted Recovery
	FPT_RCV.4 Trusted Recovery
	FPT_TDC.1 Inter-TSF basic TSF data consistency
	FPT_TST.1 TSF testing
FRU: Resource utilization	FRU_FLT.2 Limited fault tolerance
FTP: Trusted path/channels	FTP_ITC.1 Inter-TSF trusted channel

979

Table 5-2: TOE SFRs from [JCOP41V231ST]

980 **5.3 TOE Security Assurance Requirements**

981 The evaluation assurance package is EAL 4 augmented by AVA_VLA.4 and
 982 ADV_IMP.2.

Assurance Class	Assurance Components
ACM: Configuration management	ACM_AUT.1 Partial CM automation
	ACM_CAP.4 Generation support and acceptance procedures
	ACM_SCP.2 Problem tracking CM coverage
ADO: Delivery and operation	ADO_DEL.2 Detection of modification
	ADO_IGS.1 Installation, generation, and start-up procedures
ADV: Development	ADV_FSP.2 Fully defined external interfaces
	ADV_HLD.2 Security enforcing high-level design
	ADV_IMP.2 Implementation of the TSF
	ADV_LLD.1 Descriptive low-level design
	ADV_RCR.1 Informal correspondence demonstration
	ADV_SPM.1 Informal TOE security policy model
AGD: Guidance documents	AGD_ADM.1 Administrator guidance
	AGD_USR.1 User guidance
ALC: Life cycle support	ALC_DVS.1 Identification of security measures
	ALC_LCD.1 Developer defined life-cycle model
	ALC_TAT.1 Well-defined development tools
ATE: Tests	ATE_COV.2 Analysis of coverage
	ATE_DPT.1 Testing: high-level design
	ATE_FUN.1 Functional testing
	ATE_IND.2 Independent testing - sample
AVA: Vulnerability assessment	AVA_MSU.2 Validation of analysis
	AVA_SOF.1 Strength of TOE security function evaluation
	AVA_VLA.4 Highly resistant

983 **Table 5-3: Evaluation Assurance Requirements**

984 Remark: [JCOP41V231] has been evaluated according to EAL4 augmented by
 985 AVA_VLA.4, ADV_IMP.2, ALC_DVS.2 and AVA_MSU.3. For TSAM
 986 TOE evaluation, the same set of security assurance requirements is
 987 used except ALC_DVS.2 and AVA_MSU.3. ALC_DVS.1 and
 988 AVA_MSU.2 are taken in this evaluation instead of ALC_DVS.2 and
 989 AVA_MSU.3, respectively.

990
 991 **5.4 IT Environment Security Requirements not contained in [JCOP41V231ST]**

992 In this section the term “TSF” inside SFRs has been refined to “environment” for
 993 clarification. Furthermore the term “a remote trusted IT product” has been

994 refined to “the TOE”. Please note that the dependencies of the following SFRs
995 for the IT environment have not been considered.

996 5.4.1 Cryptographic key generation

997 5.4.1.1 *Cryptographic key generation (FCS_CKM.1/ENV)*

998 FCS_CKM.1.1/ENV The environment shall generate cryptographic keys in accordance with
999 a specified cryptographic key generation algorithm [*random number*
1000 *generation*] and specified cryptographic key sizes [*112 bits*] that meet
1001 the following: [*none*].

1002 **Application Note:**

1003 FDP_CKM.1/ENV refers to production of the TOE as well as to usage after TOE delivery.

1004 5.4.2 User data protection

1005 5.4.2.1 *Basic data exchange confidentiality (FDP_UCT.1/ENV)*

1006 FDP_UCT.1.1/ENV The environment shall enforce the [*Key Access SFP*] to be able to
1007 [transmit] objects in a manner protected from unauthorised disclosure.

1008 5.4.2.2 *Data exchange integrity (FDP_UIT.1/ENV)*

1009 FDP_UIT.1.1/ENV The environment shall enforce the [*Key Access SFP and TMD Access*
1010 *SFP*] to be able to [transmit] user data in a manner protected from
1011 [modification, insertion, replay] errors.

1012 FDP_UIT.1.2/ENV The environment shall be able to determine on receipt of user data,
1013 whether [*selection: modification, deletion, insertion, replay*] has
1014 occurred.

1015 **Application Note:**

1016 FDP_UCT.1/ENV and FDP_UIT.1/ENV refer to production of the TOE as well as to usage
1017 after TOE delivery. In both phases the environment is only transmitting user data to the TOE,
1018 therefore FDP_UIT.1.2/ENV is not applicable.

1019 5.4.3 Trusted path/channels

1020 5.4.3.1 *Inter-TSF trusted channel (FTP_ITC.1/ENV)*

1021 FTP_ITC.1.1/ENV The environment shall provide a communication channel between itself
1022 and the TOE that is logically distinct from other communication
1023 channels and provides assured identification of its end points and
1024 protection of the channel data from modification or disclosure.

1025 FTP_ITC.1.2/ENV The environment shall permit [the environment] to initiate
1026 communication via the trusted channel.

1027 FTP_ITC.1.3/ENV The environment shall initiate communication via the trusted channel
1028 for [*loading of D.App_Code, setting the Card Life Cycle State,*
1029 *initializing MK during TSAM.Phase_2 and first update of MK during*
1030 *TSAM.Phase_3*].

1031 **Application Note:**

1032 Concerning loading of D.App_Code, setting the Card Life Cycle State and initializing MK
1033 FTP_ITC.1/ENV refers to production of the TOE, concerning first update of MK
1034 FTP_ITC.1/ENV refers to usage after TOE delivery. FTP_ITC.1.1/ENV was already included

1035 in [JCOP41V231ST] as an SFR for the IT environment, but there its scope was limited to
 1036 loading of D.App_Code and setting the Card Life Cycle State.

1037 **5.5 IT Environment Security Requirements from [JCOP41V231ST]**

1038 In the following table the security functional requirements for the IT environment
 1039 from [JCOP41V231] (concerning byte code verification) are referenced. For
 1040 details, please refer to [JCOP41V231ST] section 5.3.1. These requirements
 1041 refer solely to production of the TOE, not to usage after TOE delivery. Please
 1042 note that (1) FTP_ITC.1/ENV, which is also defined as an SFR for the IT
 1043 environment in [JCOP41V231ST], is listed in section 5.4 above as its scope has
 1044 been extended compared to [JCOP41V231ST], and (2) that all SFRs in the
 1045 table below except FMT_SMF.1/BCV are defined by [JCSPP].

Functional Class	Functional Components	
FDP: User Data Protection	FDP_IFC.2/BCV	Complete information flow control
	FDP_IFF.2/BCV	Hierarchical security attributes
FMT: Security Management	FMT_MSA.1/BCV	Management of security attributes
	FMT_MSA.2/BCV	Secure security attributes
	FMT_MSA.3/BCV	Static attribute initialization
	FMT_SMF.1/BCV	Specification of Management Functions
	FMT_SMR.1/BCV	Security roles
FRU: Resource Utilization	FRU_RSA.1/BCV	Maximum Quotas

1046 **Table 5-4: IT Environment SFRs from [JCOP41V231ST]**

1047 6 TOE Summary Specification

1048 6.1 Security Functions

1049 **SF.AUT_GP** *TSAM_GlobalPlatform authentication*

1050 SF.AUT_GP will authenticate the user by a challenge-response mechanism
1051 using GlobalPlatform keys. For each authentication attempt, SF.AUT_GP will
1052 present a new random number¹ as a challenge. Only if the user provides the
1053 corresponding correct response, the user is authenticated as the initializer
1054 (R.Initializer). In case of a successful authentication, SF.AUT_GP will establish
1055 session keys that are later on used by SF.CP_GP.

1056 SF.AUT_GP is only available in TSAM.Phase_2.

1057 Remark: This security function has to be used by the initializer (R.Initializer)
1058 before the initializer being able to initialize MK in TSAM.Phase_2. The
1059 initializer belongs to the production environment of the TOE,
1060 nevertheless, MK initialize is already access controlled by TOE
1061 functionality.

1062 **SF.CP_GP** *TSAM_GlobalPlatform communication protection*

1063 SF.CP_GP provides confidentiality and integrity protection of communication
1064 data between the user and the TOE. This is done by decryption and verification
1065 of cryptographic checksum using session keys. The corresponding session
1066 keys are established after a successful authentication by SF.AUT_GP.

1067 SF.CP_GP is only available in TSAM.Phase_2.

1068 Remark: This security function is used by the initializer (R.Initializer) to protect
1069 the transfer of MK while initializing it in TSAM.Phase_2. The initializer
1070 belongs to the production environment of the TOE, nevertheless, MK
1071 initialize is already access controlled by TOE functionality.

1072 **SF.CP_MK** *Communication protection with MK*

1073 SF.CP_MK assures integrity, authenticity and optionally confidentiality of
1074 communication data between the user and the TOE for a single command. This
1075 is done by MAC verification and decryption using session keys which are only
1076 valid for this command. To do so, SF.CP_MK performs the following five steps:

- 1077 1. For establishing the session keys, a random number RN¹ is provided by
1078 SF.CP_MK to the user as the very first step.
- 1079 2. SF.CP_MK receives the command from the user.
- 1080 3. SF.CP_MK checks the value of RC. If it is equal to 3, SF.CP_MK returns an
1081 error code and stops processing. Otherwise, it continues with the next step.

¹ Used random numbers are taken from [JCOP41V231], which implements a random number generator conformant to [AIS20], class K3, SOF-high (see SF.EmbeddedSoftware).

1082 4. SF.CP_MK generates the session key for MAC verification by encrypting
1083 RN with MK. SF.CP_MK verifies the MAC within the command. If
1084 verification fails, it increases RC, returns an error code and stops
1085 processing. Otherwise, the issuer (R.Issuer) is authenticated, and
1086 SF.CP_MK resets RC to zero and continues with the next step.

1087 5. If the command includes encrypted data, SF.CP_MK generates the session
1088 key for decryption by encrypting the inverse of RN with MK and SF.CP_MK
1089 decrypts the encrypted data.

1090 SF.CP_MK is only available in TSAM.Phase_3 and TSAM.Phase_4.

1091 Remark 1: This security function is used to protect confidentiality and integrity
1092 of the MK during first update. It is also used to protect confidentiality
1093 and integrity of MK and WKS during updates and writes,
1094 respectively. This security function protects integrity of TMD during
1095 writes.

1096 Remark 2: The MAC verification assures authentication of the issuer as well as
1097 integrity of the communication data.

1098 **SF.AC** *Access control*

1099 SF.AC enforces access control rules based on commands, user roles and life
1100 cycle state. For commands needing authentication, SF.AC identifies user roles
1101 R.Initializer and R.Issuer with SF.AUT_GP and SF.CP_MK, respectively. For
1102 commands not needing authentication, SF.AC identifies the user role as
1103 R.POS_Terminal.

1104 The following is SF.AC-enforced access control rules:

- 1105 1. The initializer (R.Initializer) is allowed to initialize MK in TSAM.Phase_2.
- 1106 2. The issuer (R.Issuer) is allowed to perform first update of MK in
1107 TSAM.Phase_3. The issuer is also allowed to perform updates of MK and
1108 writes of WKS in TSAM.Phase_4.
- 1109 3. No user can read any of the MK and WKS out of the TOE.
- 1110 4. The issuer (R.Issuer) is allowed to write TMD in TSAM.Phase_3.
- 1111 5. The user R.POS_Terminal is allowed to read TMD out of the TOE in
1112 TSAM.Phase_4.
- 1113 6. The user R.POS_Terminal is allowed to increment TSN of TMD in
1114 TSAM.Phase_4 unless the value of TSN is equal to 999999.
- 1115 7. The user R.POS_Terminal is allowed to increment BSN of TMD in
1116 TSAM.Phase_4 unless the value of BSN is equal to 9999.
- 1117 8. The user R.POS_Terminal is allowed to use WKS according to SF.USE_WK
1118 in TSAM.Phase_4.

1119 Access attempts not matching any of these rules will be rejected by SF.AC.

1120 **SF.LCM** *Life cycle management*

1121 SF.LCM manages life cycle state of the TOE. It does so by the following:

- 1122 1. SF.LCM automatically initializes the life cycle state to TSAM.Phase_2 during
1123 applet installation in TSAM production.

1124 2. When MK has been successfully initialized by R.Initializer in TSAM.Phase_2,
1125 SF.LCM will change the life cycle state to TSAM.Phase_3.

1126 3. When TMD has been successfully written by R.Issuer in TSAM.Phase_3,
1127 SF.LCM will change the life cycle state to TSAM.Phase_4.

1128 Life cycle state changes are irreversible. No other life cycle state changes are
1129 performed except the aforementioned ones.

1130 **SF.SDP** *Stored data protection*

1131 SF.SDP checks the integrity of RC, LCS and TMD stored in EEPROM. If an
1132 integrity violation is detected, the related command is cancelled and an output
1133 error code is provided to the external user.

1134 1. Every time a value of RC, LCS or TMD is written to EEPROM, SF.SDP will
1135 generate a corresponding checksum in EEPROM.

1136 2. On receipt of a command, SF.SDP will verify the checksum of LCS and
1137 check whether LCS has a valid value. If inconsistent checksum is detected
1138 or the value of LCS is out of range, SF.SDP will block processing of the
1139 command and return the corresponding error code.

1140 3. If RC is accessed internally, SF.SDP will first of all verify the corresponding
1141 checksum. If inconsistent checksum is detected, SF.SDP blocks usage of
1142 RC and responds with a corresponding error code. This also indirectly
1143 blocks the usage of the corresponding MK.

1144 4. If TMD is accessed internally, SF.SDP will first of all verify the corresponding
1145 checksum. If inconsistent checksum is detected, SF.SDP blocks usage of
1146 TMD and responds with a corresponding error code.

1147 Furthermore SF.SDP stores MK and Wks in key objects of [JCOP41V231], and
1148 every time a value of MK or WK is written to EEPROM, the previous value is
1149 physically overwritten in the memory assigned to the corresponding key object.²

1150 **SF.USE_WK** *Use of working keys*

1151 SF.USE_WK provides the following cryptographic services applicable to TD
1152 (transaction data):

1153 1. 3/DES encryption in ECB mode with key size of 112 bits according to ANSI
1154 X 9.52 TECB for encryption/decryption.

1155 2. 3/DES decryption in ECB mode with key size of 112 bits according to ANSI
1156 X 9.52 TECB for encryption/decryption

1157 3. 3/DES MAC generation in CBC mode with key size 112 bits according to
1158 ANSI X 9.9 with ANSI X 9.52 TCBC Encryption for MAC generation.

1159 For each of the services there is one dedicated WK in the TOE. SF.USE_WK is
1160 only available in TSAM.Phase_4.

² Using key objects furthermore provides integrity protection for MK and Wks according to SF.Audit of [JCOP41V231ST], which locks the card session in case of corruption of check-summed objects.

- 1161 **SF.Embedded_Software** (from [JCOP41V231ST])
- 1162 The certified JavaCard platform (part of the TOE) features the following TSF.
1163 The exact formulation can be found in [JCOP41V231ST] (SF.Hardware from
1164 [JCOP41V231ST] is restated separately below):
- 1165 1. Access control (SF.AccessControl)
 - 1166 2. Audit functionality (SF.Audit)
 - 1167 3. Cryptographic key management (SF.CryptoKey)
 - 1168 4. Cryptographic operation (SF.CryptoOperation), including random number
1169 generation according to [AIS 20] class K3 with SOf-high
 - 1170 5. Identification and authentication (SF.I&A)
 - 1171 6. Secure management of TOE resources (SF.SecureManagement)
 - 1172 7. PIN management (SF.PIN)
 - 1173 8. Transaction management (SF.Transaction)

1174 **SF.Hardware** (from [JCOP41V231ST])

1175 The certified hardware (part of the TOE) features the following TSF. The exact
1176 formulation can be found in [ST0348]:

- 1177 1. Random Number Generator (F.RNG)
- 1178 2. Triple-DES Co-processor (F.HW_DES)
- 1179 3. AES Co-processor (F.HW_AES)
- 1180 4. Control of Operating Conditions (F.OPC)
- 1181 5. Protection against Physical Manipulation (F.PHY)
- 1182 6. Logical Protection (F.LOG)
- 1183 7. Protection of Mode Control (F.COMP)
- 1184 8. Memory Access Control (F.MEM_ACC)
- 1185 9. Special Function Register Access Control (F.SFR_ACC)

1186 **6.2 Strength of Function Claims**

1187 The minimum strength of function level claimed for this evaluation is SOf-high,
1188 therefore the following SOf-rateable security functions are also claimed to
1189 reach SOf-high. The security functions and corresponding permutational or
1190 probabilistic mechanisms to be SOf-rated are: **SF.AUT_GP** (Challenge-
1191 response authentication), **SF.CP_GP** (Cryptographic checksum verification),
1192 **SF.CP_MK** (Challenge-response authentication) and **SF.SDP** (Checksum
1193 verification). Any cryptographic algorithms in these functions will not be rated,
1194 but the rating will be performed with respect to protocols (e.g. whether
1195 challenge and response are sufficiently long).

1196 The security functions **SF.AC** and **SF.LCM** are not based on any permutational
1197 or probabilistic mechanisms and, therefore, they don't have to be rated.
1198 **SF.USE_WK** provides merely cryptographic mechanisms and, therefore, is also
1199 excluded from SOf rating.

1200 Furthermore **SF.AUT_GP** and **SF.CP_MK** (both realizing challenge-response
1201 authentications) use random numbers. These random numbers are claimed to
1202 be conformant to [AIS20], class K3, SOF-high. (This conformance has already
1203 been evaluated for [JCOP41V231], and this composite TOE uses only random
1204 numbers from the corresponding evaluated random number generator).

1205 **6.3 Assurance Measures**

1206 The TOE is to fulfill the assurance requirements of assessment class ASE and
1207 of evaluation level EAL4 augmented by ADV_IMP.2 and AVA_VLA.4. The
1208 present document "Security Target" serves to fulfill the requirements according
1209 to ASE. Besides provision of the TOE (according to ATE_IND.2), the
1210 manufacturer will apply the following additional assurance measures within the
1211 frame of the evaluation, to evidently prove the fulfilling of the requirements
1212 according to EAL4 augmented by ADV_IMP.2 and AVA_VLA.4:

- 1213 • Application of a compliant configuration management system and provision of
1214 corresponding documentation (according to ACM_AUT.1 and ACM_CAP.4)
- 1215 • Application of secure delivery procedures and provision of delivery and operational
1216 documentation (according to ADO_DEL.2 and ADO_IGS.1)
- 1217 • Provision of functional specification documentation (according to ADV_FSP.2)
- 1218 • Provision of high-level design documentation (according to ADV_HLD.2)
- 1219 • Provision of implementation representation (according to ADV_IMP.2)
- 1220 • Provision of low-level design documentation (according to ADV_LLD.1)
- 1221 • Provision of representation correspondence documentation (according to
1222 ADV_RCR.1)
- 1223 • Provision of security policy modeling documentation (according to ADV_SPM.1)
- 1224 • Provision of guidance documentation (according to AGD_ADM.1 and
1225 AGD_USR.1)
- 1226 • Application of development security measures and provision of Life cycle support
1227 documentation (according to ALC_DVS.1, ALC_LCD.1, and ALC_TAT.1)
- 1228 • Performance of functional tests and provision of corresponding test documentation
1229 (according to ATE_COV.2, ATE_DPT.1, and ATE_FUN.1)
- 1230 • Provision of vulnerability assessment documentation (according to AVA_MSU.2,
1231 AVA_SOF.1, and AVA_VLA.4)

1232 The assignment of the assurance measures to the assurance requirements
1233 (see section 5.3) is straight forward, as for all assurance components (with
1234 exception of the independent testing of the evaluator ATE_IND.2)
1235 corresponding documentation will be is provided.

1236 **7 PP claims**

1237 **7.1 PP Reference**

1238 [JCOP41V231ST] and also this ST claim conformance to the following
1239 protection profile:

- 1240 • Java Card System – Minimal Configuration Protection Profile, Version: 1.0b,
1241 August 2003 [JCSPP]

1242 **7.2 PP Additions and Refinements**

1243 See corresponding section of [JCOP41V231ST] and PP claims rationale in
1244 section 8.4 hereinafter.

1245 **8 Rationale**

1246 **8.1 Security Objectives Rationale**

	SO.REPLAY	SO.KEY_ACCESS	SO.TMD_ACCESS	SO.INTEGRITY & O.PROTECT_DATA	SO.TXN_SECURE	SO.SN	SOE.DLV	SOE.USE_DIAG	SOE.KEYS	SOE.DEV
T.KEY_ACCESS	X	X								
T.TMD_ACCESS	X		X							
T.INTEGRITY				X						
OSP.TXN_SECURE					X					
OSP.SN						X				
A.DLV							X			
A.USE_DIAG								X		
A.KEYS									X	
A.DEV										X

1247 **Table 8-1: Security objectives rationale**

1248 8.1.1 Traceability of the Security Objectives

1249 **SO.REPLAY** directly traces back to the replay attack aspect of
 1250 **T.TMD_ACCESS** and **T.KEY_ACCESS** concerning TMD writes and key
 1251 initialization/updates/writes.

1252 **SO.TMD_ACCESS** directly traces back to the authentication and integrity
 1253 protection aspects of **T.TMD_ACCESS**.

1254 **SO.KEY_ACCESS** directly traces back to the authentication, integrity
 1255 protection and confidentiality protection aspects of **T.KEY_ACCESS**.

1256 **SO.TXN_SECURE** directly traces back to **OSP.TXN_SECURE**, where
 1257 introduction of R.POS_Terminal in **SO.TXN_SECURE** corresponds to no need
 1258 for authentication as expressed in **OSP.TXN_SECURE**.

1259 **SO.SN** directly traces back to **OSP.SN**, where introduction of R.POS_Terminal
 1260 in **SO.SN** corresponds to no need for authentication as expressed in **OSP.SN**.

1261 **SO.INTEGRITY** directly traces back to **T.INTEGRITY** concerning RC, LCS
 1262 and TMD. Integrity protection for MK and Wks was already expressed by
 1263 **O.PROTECT_DATA** of the platform, which traces back to **T.INTEGRITY**
 1264 concerning MK and Wks.

1265 **SOE.DLV** directly traces back to **A.DLV** (it is a re-statement of **A.DLV**).

1266 **SOE.USE_DIAG** directly traces back to **A.USE_DIAG** (it is a re-statement of
 1267 **A.USE_DIAG**).

1268 SOE.KEYS directly traces back to A.KEYS (it is a re-statement of A.KEYS).
1269 SOE.DEV directly traces back to A.DEV (it is a re-statement of A.DEV).

1270 8.1.2 Coverage of the assumptions

1271 **A.DLV** is covered by **SOE.DLV**, as **SOE.DLV** is a re-statement of **A.DLV**.

1272 **A.USE_DIAG** is covered by **SOE.USE_DIAG**, as **SOE.USE_DIAG** is a re-
1273 statement of **A.USE_DIAG**.

1274 **A.DLV** is covered by **SOE.DLV**, as **SOE.DLV** is a re-statement of **A.DLV**.

1275 **A.KEYS** is covered by **SOE.KEYS**, as **SOE.KEYS** is a re-statement of
1276 **A.KEYS**.

1277 8.1.3 Countering of the threats

1278 **T.INTEGRITY** breaks down in two different aspects: (1) undetected integrity
1279 errors concerning MK, WKs, TMD, LCS and RC in storage, and (2) usage of
1280 corresponding corrupted data. Concerning TMD, LCS and RC both aspects
1281 are countered by **SO.INTEGRITY**, which defines as well integrity error
1282 detection concerning MK, WKs, TMD, LCS and RC in storage as the
1283 corresponding error response (prohibit use of corrupted data and give back
1284 error message). For MK and WKs, **T.INTEGRITY** is countered by
1285 **O.PROTECT_DATA** of the platform, which ensures integrity of any application
1286 keys (and other application data).

1287 **T.TMD_ACCESS** is about (1) writes of TMD by roles other than R.Issuer, (2)
1288 undetected modification of TMD during authorized writes of TMD, and (3)
1289 replay of a TMD write. **SO.TMD_ACCESS** counters the first two of these
1290 aspects, as it defines (1) a secure mechanism for TMD writes that provides
1291 authentication of the R.Issuer and (2) integrity protection for the transferred
1292 TMD. The third aspect is countered by **SO.REPLAY**, which – among others –
1293 defines protection against replay attacks concerning TMD writes.

1294 **T.KEY_ACCESS** is about (1) initialization and updates/writes of keys by roles
1295 other than R.Initializer and R.Issuer, respectively, (2) undetected modification
1296 of keys during transfer, (3) eavesdropping of keys during transfer, (4) reading
1297 out current keys from the TOE, (4) reading out previous key values from the
1298 TOE, and (5) replay of a key initialization/update/write. **SO.KEY_ACCESS**
1299 counters the first four of these aspects, as it defines (1) a secure mechanism
1300 for key initialization/updates/writes that provides authentication of the
1301 corresponding user, (2) integrity protection for the transferred keys, (3)
1302 confidentiality protection for the transferred keys, and (4) non-readability of
1303 keys. The fifth aspect is countered by **SO.REPLAY**, which – among others –
1304 defines protection against replay attacks concerning key
1305 initialization/updates/writes.

1306 8.1.4 Coverage of the Organizational Security Policies

1307 **OSP.TXN_SECURE** is covered by **SO.TXN_SECURE**, as **SO.TXN_SECURE**
1308 just defines the cryptographic functionalities as requested by
1309 **OSP.TXN_SECURE**. The aspect of **OSP.TXN_SECURE** that no authentication
1310 is needed is covered in **SO.TXN_SECURE** by the fact that the cryptographic
1311 functionalities shall be provided to R.POS_Terminal, which is an
1312 unauthenticated role.

1313 **OSP.SN** is covered by **SO.SN**, as **SO.SN** just defines the serial number
 1314 increment functionalities as requested by **OSP.SN**. The aspect of **OSP.SN** that
 1315 no authentication is needed is covered in **SO.SN** by the fact that the increment
 1316 functionalities shall be provided to R.POS_Terminal, which is an
 1317 unauthenticated role.

1318 8.1.5 Security Objectives Rationale from [JCOP41V231ST]

1319 The following table is reproduced from [JCOP41V231ST] to illustrate the
 1320 coverage of the threats by the security objectives concerning [JCOP41V231].
 1321 The corresponding justification text has not been reproduced here, please
 1322 consult [JCOP41V231ST] if needed.

	O.PROTECT_DATA	O.OS_DECEIVE	O.SIDE_CHANNEL	O.FAULT_PROTECT	O.PHYSICAL	O.CARD-MANAGEMENT	O.SHRED_VAR_INTEG	O.SHRED_VAR_CONFID	O.FIREWALL	O.NATIVE	O.OPERATE	O.ALARM	O.RESOURCES	O.REALLOCATION	O.SID	O.SCP.IC	O.SCP.RECOVERY	O.SCP.SUPPORT	O.CIPHER	O.PIN-MNGT	O.KEY-MNGT	O.TRANSACTION	O.RND
T.ACCESS_DATA	X																						
T.OS_OPERATE	X										X												
T.OS_DECEIVE		X																					
T.LEAKAGE			X																				
T.FAULT				X																			
T.PHYSICAL					X										X								
T.CONFID-JCS-DATA						X		X		X	X			X		X	X						
T.INTEG-JCS-DATA									X														
T.CONFID-APPLI-DATA						X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
T.INTEG-APPLI-DATA						X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X	X
T.SID.1						X		X						X									
T.SID.2								X		X				X		X	X						
T.NATIVE										X													
T.RESOURCES											X		X			X	X						
T.RND																							X

1323 Table 8-2: Security objectives rationale from [JCOP41V231ST]

1324

1325 8.2 Security Requirements Rationale

	SO.REPLAY	SO.KEY_ACCESS	SO.TMD_ACCESS	SO.INTEGRITY	SO.TXN_SECURE	SOE.DLV	SOE.USE_DIAG	SOE.KEYS	SOE.DEV
FCS_CKM.4/TSAM		X							
FCS_COP.1/TSAM					X				
FDP_ACC.1/KEY		X							
FDP_ACC.1/TMD			X						
FDP_ACF.1/KEY		X							
FDP_ACF.1/TMD			X						
FDP_ITC.1/KEY		X							
FDP_ITC.1/TMD			X						
FDP_SDI.2/TSAM				X					
FDP_UCT.1/KEY		X							
FDP_UIT.1/TSAM	X	X	X						
FIA_AFL.1/TSAM		X	X						
FIA_UAU.1/TSAM		X	X						
FIA_UAU.4/TSAM		X	X						
FIA_UAU.5/TSAM		X	X						
FIA_UID.1/TSAM		X	X						
FMT_MSA.1/TSAM		X	X						
FMT_MSA.2/TSAM		X	X						
FMT_MSA.3/TSAM		X	X						
FMT_SMF.1/TSAM		X	X						
FMT_SMR.1/TSAM		X	X						
FTP_ITC.1/TSAM		X	X						
FCS_CKM.1/ENV								X	
FDP_UCT.1/ENV							X		
FDP_UIT.1/ENV							X		
FTP_ITC.1/ENV							X		
FDP_IFC.2/BCV									X
FDP_IFF.2/BCV									X
FMT_MSA.1/BCV									X
FMT_MSA.2/BCV									X
FMT_MSA.3/BCV									X
FMT_SMF.1/BCV									X
FMT_SMR.1/BCV									X
FRU_RSA.1/BCV									X

1326 Table 8-3: Security requirements rationale

1327 8.2.1 Fulfilment of security objectives

1328 **SO.REPLAY** is met by FDP_UIT.1/TSAM, as this requires replay protection
 1329 concerning key initialization/updates/writes as defined in SO.REPLAY.

1330 **SO.KEY_ACCESS** defines (1) access control for key initialization, writes and
1331 updates, and (2) confidentiality protection and (3) integrity protection during
1332 those operations.
1333 The first aspect is met by the access control requirements FDP_ACC.1/KEY
1334 and FDP_ACF.1/KEY. Management for the governing security attribute life-
1335 cycle state is provided by FMT_MSA.1/TSAM, FMT_MSA.2/TSAM,
1336 FMT_MSA.3/TSAM and FMT_SMF.1/TSAM. The identification and
1337 authentication and ability to distinguish roles, which are a precondition for
1338 performing the access control, are provided by FIA_AFL.1/TSAM,
1339 FIA_UAU.1/TSAM, FIA_UAU.4/TSAM, FIA_UAU.5/TSAM and
1340 FIA_UID.1/TSAM and FMT_SMR.1/TSAM.
1341 The second aspect, key confidentiality, is met by FDP_UCT.1/KEY and
1342 FTP_ITC.1/TSAM, furthermore FCS_CKM.4/TSAM supports it by ensuring
1343 that values of previous keys are no longer physically available after a key
1344 update/write.
1345 The third aspect, key integrity, is provided by FDP_UIT.1/TSAM and
1346 FTP_ITC.1/TSAM.
1347 Finally, the key import operations are defined by FDP_ITC.1/KEY.

1348 **SO.TMD_ACCESS** defines (1) access control for TMD writes, and (2) integrity
1349 protection during those operations.
1350 The first aspect is met by the access control requirements FDP_ACC.1/TMD
1351 and FDP_ACF.1/TMD. Management for the governing security attribute life-
1352 cycle state is provided by FMT_MSA.1/TSAM, FMT_MSA.2/TSAM,
1353 FMT_MSA.3/TSAM and FMT_SMF.1/TSAM. The identification and
1354 authentication and ability to distinguish roles, which are a precondition for
1355 performing the access control, are provided by FIA_AFL.1/TSAM,
1356 FIA_UAU.1/TSAM, FIA_UAU.5/TSAM, FIA_UID.1/TSAM and
1357 FMT_SMF.1/TSAM.
1358 The second aspect, TMD integrity, is provided by FDP_UIT.1/TSAM and
1359 FTP_ITC.1/TSAM.
1360 Finally, the TMD import operation is defined by FDP_ITC.1/TMD.

1361 **SO.INTEGRITY** defines (1) integrity detection concerning RC, LCS and TMD
1362 in storage and (2) prevention to use corrupted data and provision of an error
1363 message.
1364 The first aspect is met by FDP_SDI.2.1/TSAM, which requests the
1365 corresponding stored data integrity monitoring. The second aspect is met by
1366 FDP_SDI.2.2/TSAM, which requests a message to the user and dedicated
1367 response actions that prevent usage of the corrupted data.

1368 **SO.TXN_SECURE** defines cryptographic services of encryption, decryption
1369 and MAC generation using 3/DES. This is directly met by FCS_COP.1/TSAM.

1370 **SOE.DLV** is purely related to non-IT environmental aspects (organizational
1371 measures concerning verification of delivered items), therefore there are no
1372 related SFRs.

1373 **SOE.USE_DIAG** defines the capabilities of the IT environment concerning
1374 integrity and confidentiality protection during data transfer. This is directly met
1375 by FDP_UCT.1/ENV, FDP_UIT.1/ENV and FTP_ITC.1/ENV.

1376 **SOE.KEYS** defines random number generation as the method to generate
1377 keys. This is directly met by FCS_CKM.1/ENV. The remaining aspects of

1378 SOE.KEYS (confidentiality and integrity protection of keys in the environment)
1379 may be solely related to non-IT measures, therefore there are no related SFRs.

1380 **SOE.DEV** defines performance of byte code verification during development.
1381 This is met by the FDP_IFC.2/BCV, FDP_IFF.2/BCV, FMT_MSA.1/BCV,
1382 FMT_MSA.2/BCV, FMT_MSA.3/BCV, FMT_SMF.1/BCV, FMT_SMR.1/BCV,
1383 FRU_RSA.1/BCV concerning byte code verification. The remaining aspects of
1384 SOE.DEV (no native code loading, no delivery of GlobalPlatform keys and
1385 general development security aspects) may be solely related to non-IT
1386 measures, therefore there are no related SFRs.

1387 8.2.2 Traceability of the Security Functional Requirements

1388 **FCS_CKM.4/TSAM** requires physical overwriting of previous key values keys
1389 during updates/writes and therefore traces back to the aspect of
1390 SO.KEY_ACCESS that previous key values shall not be accessible.

1391 **FCS_COP.1/TSAM** requires the cryptographic functions needed to secure
1392 transactions and therefore traces back to SO.TXN_SECURE.

1393 **FDP_ACC.1/KEY** and **FDP_ACF.1/KEY** define access control concerning
1394 keys and therefore trace back to SO.KEY_ACCESS.

1395 **FDP_ACC.1/TMD** and **FDP_ACF.1/TMD** define access control concerning
1396 TMD and therefore trace back to SO.TMD_ACCESS.

1397 **FDP_ITC.1/KEY** defines details about the key import operation and therefore
1398 traces back to SO.KEY_ACCESS.

1399 **FDP_ITC.1/TMD** defines details about the TMD import operation and therefore
1400 traces back to SO.TMD_ACCESS.

1401 **FDP_SDI.2/TSAM** requires integrity protection concerning stored RC, LCS
1402 and TMD, and therefore traces back to SO.INTEGRITY.

1403 **FDP_UCT.1/KEY** requires confidentiality protection concerning transfer of
1404 keys and therefore traces back to SO.KEY_ACCESS.

1405 **FDP_UIT.1/TSAM** requires integrity and replay protection concerning transfer
1406 of keys and TMD and therefore traces back to SO.KEY_ACCESS,
1407 SO.TMD_ACCESS and SO.REPLAY.

1408 **FIA_AFL.1/TSAM, FIA_UAU.1/TSAM, FIA_UAU.4/TSAM, FIA_UAU.5/TSAM**
1409 **and FIA_UID.1/TSAM** define requirements about identification and
1410 authentication necessary as a precondition for the access control about keys
1411 and TMD, and therefore trace back to SO.KEY_ACCESS and
1412 SO.TMD_ACCESS.

1413 **FMT_MSA.1/TSAM, FMT_MSA.2/TSAM, FMT_MSA.3/TSAM** and
1414 **FMT_SMF.1/TSAM** define requirements about the management of the life-
1415 cycle state, which is the governing security attribute for access control for keys
1416 and TMD, and therefore traces back to SO.KEY_ACCESS and
1417 SO.TMD_ACCESS.

1418 **FMT_SMR.1/TSAM** requires the ability to distinguish roles, which is a
1419 precondition for access control for keys and TMD, and therefore traces back to
1420 SO.KEY_ACCESS and SO.TMD_ACCESS

1421 **FTP_ITC.1/TSAM** defines a trusted channel that provides confidentiality
 1422 and/or integrity protection for key/TMD transfer, and therefore traces back to
 1423 SO.KEY_ACCESS and SO.TMD_ACCESS.

1424 **FCS_CKM.1/ENV** defines random number generation as key generation
 1425 algorithm to be used, and therefore traces back to the corresponding aspect of
 1426 SOE.KEYS.

1427 **FDP_UCT.1/ENV, FDP_UIT.1/ENV and FTP_ITC.1/ENV** define requirements
 1428 for remote IT products in the environment concerning confidentiality and
 1429 integrity protection during data transfer to the TOE, and therefore trace back to
 1430 SOE.USE_DIAG.

1431 **FDP_IFC.2/BCV, FDP_IFF.2/BCV, FMT_MSA.1/BCV, FMT_MSA.2/BCV,**
 1432 **FMT_MSA.3/BCV, FMT_SMF.1/BCV, FMT_SMR.1/BCV, FRU_RSA.1/BCV**
 1433 define requirements concerning byte code verification, and therefore trace
 1434 back to the corresponding aspect of SOE.DEV.

1435 **8.2.3 Suitability of Security Assurance Requirements**

1436 As the TOE shall be used in a financial context and its assets will have high financial
 1437 value, a corresponding high level of robustness of and confidence in the TOE is
 1438 required. Therefore as assurance requirements EAL4 augmented by ADV_IMP.2 and
 1439 AVA_VLA.4 have been chosen.

1440 Confidence will be provided, as EAL4 requires a thorough evaluation, in particular of
 1441 the design of the TOE (which even has been extended by the augmentation of
 1442 ADV_IMP.2).

1443 Sufficient robustness of the TOE against penetration attacks shall be provided by
 1444 application of AVA_VLA.4, which provides for a systematic vulnerability analysis and
 1445 finally for a TOE being resistant even to attackers owing a high attack potential.

1446 **8.2.4 Fulfillment of dependencies**

SFR used	Dependencies acc. to CC	Fulfilled by
FCS_CKM.4/TSAM	[FDP_ITC.1 or FCS_CKM.1] FMT_MSA.2	FDP_ITC.1/KEY FMT_MSA.2/TSAM
FCS_COP.1/TSAM	[FDP_ITC.1 or FCS_CKM.1] FCS_CKM.4 FMT_MSA.2	FDP_ITC.1/KEY FCS_CKM.4/TSAM FMT_MSA.2/TSAM
FDP_ACC.1/KEY	FDP_ACF.1	FDP_ACF.1/KEY
FDP_ACC.1/TMD	FDP_ACF.1	FDP_ACF.1/TMD
FDP_ACF.1/KEY	FDP_ACC.1 FMT_MSA.3	FDP_ACC.1/KEY FMT_MSA.3/TSAM
FDP_ACF.1/TMD	FDP_ACC.1 FMT_MSA.3	FDP_ACC.1/TMD FMT_MSA.3/TSAM
FDP_ITC.1/KEY	[FDP_ACC.1 or FDP_IFC.1] FMT_MSA.3	FDP_ACC.1/KEY FMT_MSA.3/TSAM
FDP_ITC.1/TMD	[FDP_ACC.1 or FDP_IFC.1] FMT_MSA.3	FDP_ACC.1/TMD FMT_MSA.3/TSAM

SFR used	Dependencies acc. to CC	Fulfilled by
FDP_SDI.2/TSAM	No dependencies	Not applicable
FDP_UCT.1/KEY	[FTP_ITC.1 or FTP_TRP.1] [FDP_ACC.1 or FDP_IFC.1]	FTP_ITC.1/TSAM FDP_ACC.1/KEY
FDP_UIT.1/TSAM	[FDP_ACC.1 or FDP_IFC.1] [FTP_ITC.1 or FTP_TRP.1]	FDP_ACC.1/KEY, FDP_ACC.1/TMD FTP_ITC.1/TSAM
FIA_AFL.1/TSAM	FIA_UAU.1	FIA_UAU.1/TSAM
FIA_UAU.1/TSAM	FIA_UID.1	FIA_UID.1/TSAM
FIA_UAU.4/TSAM	No dependencies	Not applicable
FIA_UAU.5/TSAM	No dependencies	Not applicable
FIA_UID.1/TSAM	No dependencies	Not applicable
FMT_MSA.1/TSAM	[FDP_ACC.1 or FDP_IFC.1] FMT_SMR.1 FMT_SMF.1	FDP_ACC.1/KEY, FDP_ACC.1/TMD FMT_SMR.1/TSAM FMT_SMF.1/TSAM
FMT_MSA.2/TSAM	ADV_SPM.1 [FDP_ACC.1 or FDP_IFC.1] FMT_MSA.1 FMT_SMR.1	ADV_SPM.1 FDP_ACC.1/KEY, FDP_ACC.1/TMD FMT_SMR.1/TSAM FMT_SMF.1/TSAM
FMT_MSA.3/TSAM	FMT_MSA.1 FMT_SMR.1	FMT_MSA.1/TSAM FMT_SMR.1/TSAM
FMT_SMF.1/TSAM	No dependencies	Not applicable
FMT_SMR.1/TSAM	FIA_UID.1	FIA_UID.1/TSAM
FTP_ITC.1/TSAM	No dependencies	Not applicable

Table 8-4: Fulfillment of TOE SFR dependencies

1447

1448

Concerning the security assurance requirements all dependencies are fulfilled, as

1449

- all dependencies within an evaluation assurance level (here: EAL4) are automatically fulfilled,

1450

1451

- the dependencies of the augmented component ADV_IMP.2 (i.e. ADV_LLD.1, ADV_RCR.1 and ALC_TAT.1) are already satisfied within EAL4,

1452

1453

- and the dependencies of the augmented component AVA_VLA.4 (i.e. ADV_FSP.1, ADV_HLD.2, ADV_IMP.1, ADV_LLD.1, AGD_ADM.1 and AGD_USR.1) are already satisfied within EAL4.

1454

1455

1456

8.2.5 Suitability of minimum strength of function (SoF) level

1457

As the TOE shall be used in a financial context and its assets will have high financial value, the TOE also shall be highly resistant against attacks on its functions. The protection against attacks with a high attack potential dictates a strength of function rating of “high”.

1458

1459

1460

1461 **8.3 TOE Summary Specification Rationale**

	SF.AUT_GP	SF.CP_GP	SF.CP_MK	SF.AC	SF.LCM	SF.SDP	SF.USE_WK
FCS_CKM.4/TSAM						X	
FCS_COP.1/TSAM							X
FDP_ACC.1/KEY				X			
FDP_ACC.1/TMD				X			
FDP_ACF.1/KEY				X			
FDP_ACF.1/TMD				X			
FDP_ITC.1/KEY				X	X		
FDP_ITC.1/TMD				X	X		
FDP_SDI.2/TSAM						X	
FDP_UCT.1/KEY		X	X				
FDP_UIT.1/TSAM		X	X				
FIA_AFL.1/TSAM			X				
FIA_UAU.1/TSAM				X			
FIA_UAU.4/TSAM	X						
FIA_UAU.5/TSAM	X	X	X				
FIA_UID.1/TSAM				X			
FMT_MSA.1/TSAM					X		
FMT_MSA.2/TSAM					X		
FMT_MSA.3/TSAM					X		
FMT_SMF.1/TSAM					X		
FMT_SMR.1/TSAM				X			
FTP_ITC.1/TSAM	X	X					

1462 **Table 8-5: TSS rationale**

1463 **8.3.1 Traceability and Satisfaction of the TOE SFRs**

1464 The following table shows that the TOE security functions satisfy the
 1465 corresponding SFRs, that all SFRs are addressed and that there is no aspect
 1466 of a security function that cannot be traced back to an SFR. This is achieved
 1467 by breaking down the security functions in individual statements and mapping
 1468 each statement to the corresponding SFR(s).

Statements of Security Function	Fulfilled SFR(s)
SF.AUT_GP: SF.AUT_GP will authenticate the user by a challenge-response mechanism using GlobalPlatform keys. ...	FIA_UAU.5.1/TSAM: The TSF shall provide [GlobalPlatform card manager authentication, ...] to support user authentication.
SF.AUT_GP: ... For each authentication attempt, SF.AUT_GP will present a new random number as a challenge. ...	FIA_UAU.4.1/TSAM: The TSF shall prevent reuse of authentication data related to [GlobalPlatform card manager authentication, ...].
SF.AUT_GP: ... Only if the user provides the corresponding correct response, the user is authenticated as the initializer (R.Initializer). ...	FIA_UAU.5.2/TSAM: The TSF shall authenticate any user's claimed identity according to the [following rules: ... GlobalPlatform card manager authentication is used for authentication of

Statements of Security Function	Fulfilled SFR(s)
	R.Initializer ...]
<p>SF.AUT_GP: ... In case of a successful authentication, SF.AUT_GP will establish session keys that are later on used by SF.CP_GP. ...</p>	<p>FTP_ITC.1.1/TSAM The TSF shall provide a communication channel between itself and a remote trusted IT product that is logically distinct from other communication channels and provides assured identification of its end points and protection of the channel data from modification or disclosure.</p> <p>FTP_ITC.1.2/TSAM The TSF shall permit [the remote trusted IT product] to initiate communication via the trusted channel.</p> <p>FTP_ITC.1.3/TSAM The TSF shall initiate communication via the trusted channel for [performing initialize, ... of MK ...].</p>
<p>SF.AUT_GP: ... SF.AUT_GP is only available in TSAM.Phase_2.</p>	<p>FIA_UAU.5.2/TSAM: The TSF shall authenticate any user's claimed identity according to the [following rules: ... GlobalPlatform card manager authentication is used for authentication ... in TSAM.Phase_2. ...]</p>
<p>SF.CP_GP: SF.CP_GP provides confidentiality ... protection of communication data between the user and the TOE. This is done by decryption ... using session keys. The corresponding session keys are established after a successful authentication by SF.AUT_GP. ...</p>	<p>FDP_UCT.1.1/KEY The TSF shall enforce the [Key Access SFP] to be able to [receive] objects in a manner protected from unauthorised disclosure.</p> <p>FTP_ITC.1.1/TSAM The TSF shall provide a communication channel between itself and a remote trusted IT product that is logically distinct from other communication channels and provides assured identification of its end points and protection of the channel data from ... disclosure.</p> <p>FTP_ITC.1.2/TSAM The TSF shall permit [the remote trusted IT product] to initiate communication via the trusted channel.</p> <p>FTP_ITC.1.3/TSAM The TSF shall initiate communication via the trusted channel for [performing initialize, ... of MK ...].</p>
<p>SF.CP_GP: SF.CP_GP provides ... integrity protection of communication data between the user and the TOE. This is done by ... verification of cryptographic checksum using session keys. The corresponding session keys are established after a successful authentication by SF.AUT_GP. ...</p>	<p>FDP_UIT.1.1/TSAM The TSF shall enforce the [Key Access SFP and TMD Access SFP] to be able to [receive] user data in a manner protected from [modification, insertion, replay] errors.</p> <p>FDP_UIT.1.2/TSAM The TSF shall be able to determine on receipt of user data, whether [modification, insertion, replay] has occurred.</p> <p>FTP_ITC.1.1/TSAM The TSF shall provide a communication channel between itself and a remote trusted IT product that is logically distinct from other communication channels and provides assured identification of its end points and protection of the channel data from modification ...</p> <p>FTP_ITC.1.2/TSAM The TSF shall permit [the remote trusted IT product] to initiate communication via the trusted channel.</p>

Statements of Security Function	Fulfilled SFR(s)
	FTP_ITC.1.3/TSAM The TSF shall initiate communication via the trusted channel for [performing initialize, ... of MK ...].
SF.CP_GP: ... SF.CP_GP is only available in TSAM.Phase_2.	FIA_UAU.5.2/TSAM The TSF shall authenticate any user's claimed identity according to the [following rules: 1. GlobalPlatform card manager authentication is used for authentication of R.Initializer in TSAM.Phase_2. ...]. (Remark: SF.CP_GP uses the same GlobalPlatform functionality as SF.AUT_GP.)
SF.CP_MK: SF.CP_MK assures integrity, authenticity ... confidentiality of communication data between the user and the TOE for a single command. This is done by MAC verification ... using session keys which are only valid for this command. To do so, SF.CP_MK performs the following ... steps: 1. For establishing the session keys, a random number RN is provided by SF.CP_MK to the user as the very first step. 2. SF.CP_MK receives the command from the user. ... 4. SF.CP_MK generates the session key for MAC verification by encrypting RN with MK. SF.CP_MK verifies the MAC within the command. If verification fails, it ... returns an error code and stops processing. Otherwise, the issuer (R.Issuer) is authenticated, and SF.CP_MK ... continues with the next step. ...	FDP_UIT.1.1/TSAM The TSF shall enforce the [Key Access SFP and TMD Access SFP] to be able to [receive] user data in a manner protected from [modification, insertion, replay] errors. FDP_UIT.1.2/TSAM The TSF shall be able to determine on receipt of user data, whether [modification, insertion, replay] has occurred.
SF.CP_MK: SF.CP_MK assures ... optionally confidentiality of communication data between the user and the TOE for a single command. This is done by ... decryption using session keys which are only valid for this command. To do so, SF.CP_MK performs the following ... steps: 1. For establishing the session keys, a random number RN is provided by SF.CP_MK to the user as the very first step. 2. SF.CP_MK receives the command from the user. ... 5. If the command includes encrypted data, SF.CP_MK generates the session key for decryption by encrypting the inverse of RN with MK and SF.CP_MK decrypts the encrypted data. ...	FDP_UCT.1.1/KEY The TSF shall enforce the [Key Access SFP] to be able to [receive] objects in a manner protected from unauthorised disclosure.
SF.CP_MK: ... 3. SF.CP_MK checks the value of RC. If it is equal to 3, SF.CP_MK returns an error code and stops processing. Otherwise, it continues with the next step. 4. ... If verification fails, it increases RC, returns an error code and stops processing. Otherwise, ... SF.CP_MK resets RC to zero and	FIA_AFL.1.1/TSAM The TSF shall detect when [three consecutive] unsuccessful authentication attempts occur related to [authentication with MK]. FIA_AFL.1.2/TSAM When the defined number of unsuccessful authentication attempts has been met or surpassed, the TSF shall [no longer

Statements of Security Function	Fulfilled SFR(s)
continues with the next step. ...	allow authentication with MK].
SF.CP_MK: ...SF.CP_MK is only available in TSAM.Phase_3 and TSAM.Phase_4.	FIA_UAU.5.2/TSAM The TSF shall authenticate any user's claimed identity according to the [following rules: ... 2. Authentication with MK is used for authentication of R.Issuer in TSAM.Phase_3 and TSAM.Phase_4].
SF.AC: SF.AC enforces access control rules based on commands, user roles and life cycle state. ...	FDP_ACC.1.1/KEY The TSF shall enforce the [Key Access SFP] on [subjects: users, objects: MK, Wks and operation: initialize, first update, update, write, read and use]. FDP_ACC.1.1/TMD The TSF shall enforce the [TMD Access SFP] on [subjects: users, objects: TMD and operation: read, write and increment].
SF.AC: ... For commands needing authentication, SF.AC identifies user roles R.Initializer and R.Issuer with SF.AUT_GP and SF.CP_MK, respectively. For commands not needing authentication, SF.AC identifies the user role as R.POS_Terminal. ...	FMT_SMR.1.1/TSAM The TSF shall maintain the roles [R.Initializer, R.Issuer and R.POS_Terminal]. FMT_SMR.1.2/TSAM The TSF shall be able to associate users with roles. FIA_UAU.1.1/TSAM The TSF shall allow [encryption, decryption and MAC generation by corresponding WK, reading TMD, incrementing TSN and/or BSN of TMD] on behalf of the user to be performed before the user is authenticated. FIA_UAU.1.2/TSAM The TSF shall require each user to be successfully authenticated before allowing any other TSF-mediated actions on behalf of that user. FIA_UID.1.1/TSAM The TSF shall allow [encryption, decryption and MAC generation by corresponding WK, reading TMD, incrementing TSN and/or BSN of TMD] on behalf of the user to be performed before the user is identified. FIA_UID.1.2/TSAM The TSF shall require each user to be successfully identified before allowing any other TSF-mediated actions on behalf of that user. (see also SF.AUT_GP and SF.CP_MK)
SF.AC: ... The following is SF.AC-enforced access control rules: 1. The initializer (R.Initializer) is allowed to initialize MK in TSAM.Phase_2. 2. The issuer (R.Issuer) is allowed to perform first update of MK in TSAM.Phase_3. The issuer is also allowed to perform updates of MK and writes of Wks in TSAM.Phase_4. 3. No user can read any of the MK and Wks out of the TOE. 4. The issuer (R.Issuer) is allowed to write TMD in TSAM.Phase_3. 5. The user R.POS_Terminal is allowed to	FDP_ACF.1.1/KEY The TSF shall enforce the [Key Access SFP] to objects based on the following: [subject attribute: user role {R.Initializer, R.Issuer, R.POS_Terminal} and object attribute: life cycle state { TSAM.Phase_2, TSAM.Phase_3, TSAM.Phase_4}]. FDP_ACF.1.2/KEY The TSF shall enforce the following rules to determine if an operation among controlled subjects and controlled objects is allowed: [1. A user with user role {R.Initializer} is allowed to initialize the MK if the life cycle state is {TSAM.Phase_2}. 2. A user with user role {R.Issuer} is

Statements of Security Function	Fulfilled SFR(s)
<p>read TMD out of the TOE in TSAM.Phase_4.</p> <p>6. The user R.POS_Terminal is allowed to increment TSN of TMD in TSAM.Phase_4 unless the value of TSN is equal to 999999.</p> <p>7. The user R.POS_Terminal is allowed to increment BSN of TMD in TSAM.Phase_4 unless the value of BSN is equal to 9999.</p> <p>8. The user R.POS_Terminal is allowed to use WKS according to SF.USE_WK in TSAM.Phase_4.</p> <p>Access attempts not matching any of these rules will be rejected by SF.AC.</p>	<p>allowed to do first update of the MK if the life cycle state is {TSAM.Phase_3}.</p> <p>3. A user with user role {R.Issuer} is allowed to do updates of the MK if the life cycle state is {TSAM.Phase_4}.</p> <p>4. A user with user role {R.Issuer} is allowed to do writes of the WK if the life cycle state is {TSAM.Phase_4}.</p> <p>5. A user with user role {R.POS_Terminal} is allowed to use the WK if the life cycle state is {TSAM.Phase_4}.</p> <p>FDP_ACF.1.3/KEY The TSF shall explicitly authorise access of subjects to objects based on the following additional rules: [no other rule].</p> <p>FDP_ACF.1.4/KEY The TSF shall explicitly deny access of subjects to objects based on the [rule that no user can read any of the MK and WKS out of the TOE].</p> <p>FDP_ACF.1.1/TMD The TSF shall enforce the [TMD Access SFP] to objects based on the following: [subject attribute: user role {R.Issuer, R.POS_Terminal} and object attribute: life cycle state {TSAM.Phase_3, TSAM.Phase_4}].</p> <p>FDP_ACF.1.2/TMD The TSF shall enforce the following rules to determine if an operation among controlled subjects and controlled objects is allowed: [</p> <p>1. A user with user role {R.Issuer} is allowed to write TMD if the life cycle state is {TSAM.Phase_3}.</p> <p>2. A user with user role {R.POS_Terminal} is allowed to read TMD and increment TSN/BSN of TMD if the life cycle state is {TSAM.Phase_4}.</p> <p>FDP_ACF.1.3/TMD The TSF shall explicitly authorise access of subjects to objects based on the following additional rules: [no other rule].</p> <p>FDP_ACF.1.4/TMD The TSF shall explicitly deny access of subjects to objects based on the [following rules:</p> <p>1. Increment of TSN is denied if the value of TSN is equal to 999999.</p> <p>2. Increment of BSN is denied if the value of BSN is equal to 9999.]</p>
<p>SF.AC: ... 1. The initializer (R.Initializer) is allowed to initialize MK ...</p> <p>2. The issuer (R.Issuer) is allowed to perform first update of MK ... The issuer is also allowed to perform updates of MK and writes of WKS ...</p>	<p>FDP_ITC.1.1/KEY The TSF shall enforce the [Key Access SFP] when importing user data, controlled under the SFP, from outside of the TSC.</p> <p>FDP_ITC.1.2/KEY The TSF shall ignore any security attributes associated with the user data when imported from outside the TSC.</p>
<p>SF.AC: ... 4. The issuer (R.Issuer) is allowed</p>	<p>FDP_ITC.1.1/TMD The TSF shall enforce the</p>

Statements of Security Function	Fulfilled SFR(s)
to write TMD ...	<p>[TMD Access SFP] when importing user data, controlled under the SFP, from outside of the TSC.</p> <p>FDP_ITC.1.2/TMD The TSF shall ignore any security attributes associated with the user data when imported from outside the TSC.</p>
<p>SF.LCM: SF.LCM provides management of the life cycle state of the TOE, ... Life cycle state changes are irreversible. No other life cycle state changes are performed except the aforementioned ones.</p>	<p>FMT_SMF.1.1/TSAM The TSF shall be capable of performing the following security management functions: [modification of the life state according to FMT_MSA.1.1/TSAM, FDP_ITC.1.3 /KEY and FDP_ITC.1.3 /TMD].</p>
<p>SF.LCM: ... It does so by the following:</p> <p>1. SF.LCM automatically initializes the life cycle state to TSAM.Phase_2 during applet installation in TSAM production. ...</p>	<p>FMT_MSA.3.1/TSAM The TSF shall enforce the [Key Access SFP and TMD Access SFP] to provide [restrictive] default values for security attributes that are used to enforce the SFP.</p> <p>FMT_MSA.3.2/TSAM The TSF shall allow the [nobody] to specify alternative initial values to override the default values when an object or information is created.</p> <p>FMT_MSA.2.1/TSAM The TSF shall ensure that only secure values are accepted for security attributes.</p>
<p>SF.LCM: ... 2. When MK has been successfully initialized by R.Initializer in TSAM.Phase_2, SF.LCM will change the life cycle state to TSAM.Phase_3. ...</p>	<p>FDP_ITC.1.3/KEY The TSF shall enforce the following rules when importing user data controlled under the SFP from outside the TSC: [</p> <p>1. After import of MK by initialize operation, the security attribute life cycle state shall change from TSAM.Phase_2 to TSAM.Phase_3.]</p> <p>FMT_MSA.1.1/TSAM The TSF shall enforce the [Key Access SFP and TMD Access SFP] to restrict the ability to [modify] the security attributes [life cycle state] to [R.Initializer and ...]. (Remark: SF.AC enforces that initialization can only be done by R.Initializer, and SF.LCM links the LCS transition to initialization operation.)</p> <p>FMT_MSA.2.1/TSAM The TSF shall ensure that only secure values are accepted for security attributes.</p>
<p>SF.LCM: ... 3. When TMD has been successfully written by R.Issuer in TSAM.Phase_3, SF.LCM will change the life cycle state to TSAM.Phase_4. ...</p>	<p>FDP_ITC.1.3/TMD The TSF shall enforce the following rules when importing user data controlled under the SFP from outside the TSC: [</p> <p>1. After import of TMD by write operation, the security attribute life cycle state shall change from TSAM.Phase_3 to TSAM.Phase_4.]</p> <p>FMT_MSA.1.1/TSAM The TSF shall enforce the [Key Access SFP and TMD Access SFP] to restrict the ability to [modify] the security attributes [life cycle state] to [R.Initializer and R.Issuer]. (Remark: SF.AC enforces that TMD writing can only be done by R.Issuer, and SF.LCM links the LCS transition to the TMD writing operation.)</p> <p>FMT_MSA.2.1/TSAM The TSF shall ensure that</p>

Statements of Security Function	Fulfilled SFR(s)
	only secure values are accepted for security attributes.
<p>SF.SDP: SF.SDP checks the integrity of RC, LCS and TMD stored in EEPROM. If an integrity violation is detected, the related command is cancelled and an output error code is provided to the external user.</p> <ol style="list-style-type: none"> Every time a value of RC, LCS or TMD is written to EEPROM, SF.SDP will generate a corresponding checksum in EEPROM. On receipt of a command, SF.SDP will verify the checksum of LCS and check whether LCS has a valid value. If inconsistent checksum is detected or the value of LCS is out of range, SF.SDP will block processing of the command and return the corresponding error code. If RC is accessed internally, SF.SDP will first of all verify the corresponding checksum. If inconsistent checksum is detected, SF.SDP blocks usage of RC and responds with a corresponding error code. This also indirectly blocks the usage of the corresponding MK. If TMD is accessed internally, SF.SDP will first of all verify the corresponding checksum. If inconsistent checksum is detected, SF.SDP blocks usage of TMD and responds with a corresponding error code. ... 	<p>FDP_SDI.2.1/TSAM The TSF shall monitor user data stored within the TSC for [integrity errors] on all objects, based on the following attributes [checksum for TMD, LCS and RC].</p> <p>FDP_SDI.2.2/TSAM Upon detection of a data integrity error, the TSF shall [inform the user and perform the actions in Table 5 1 depending on which object is incurred in the data integrity error].</p>
<p>SF.SDP: ... Furthermore SF.SDP stores MK and Wks in key objects of [JCOP41V231], and every time a value of MK or WK is written to EEPROM, the previous value is physically overwritten in the memory assigned to the corresponding key object.</p>	<p>FCS_CKM.4.1/TSAM The TSF shall destroy cryptographic keys in accordance with a specified cryptographic key destruction method [previous MK and Wks are physically overwritten by new keys] that meets the following: [none].</p>
<p>SF.USE_WK: SF.USE_WK provides the following cryptographic services applicable to TD (transaction data):</p> <ol style="list-style-type: none"> 3/DES encryption in ECB mode with key size of 112 bits according to ANSI X 9.52 TECB for encryption/decryption. 3/DES decryption in ECB mode with key size of 112 bits according to ANSI X 9.52 TECB for encryption/decryption 3/DES MAC generation in CBC mode with key size 112 bits according to ANSI X 9.9 with ANSI X 9.52 TCBC Encryption for MAC generation. <p>For each of the services there is one dedicated WK in the TOE. SF.USE_WK is only available in TSAM.Phase_4.</p>	<p>FCS_COP.1.1/TSAM The TSF shall perform [encryption, decryption, MAC generation for TD with dedicated keys in TSAM.Phase.4] in accordance with a specified cryptographic algorithm [3/DES in ECB or CBC mode] and cryptographic key sizes [112 bits] that meet the following: [ANSI X 9.52 TECB for encryption/decryption, ANSI X 9.9 with ANSI X 9.52 TCBC Encryption for MAC generation].</p>

Table 8-6: Traceability and Satisfaction of the TOE SFRs

1470 8.3.2 Mutual Support of the Security Functions

1471 Based on the results of the security requirements rationale, which has shown
 1472 that the set of TOE SFRs forms a mutually supportive whole to fulfil the TOE
 1473 security objectives, according to [CEM] § 491 the remaining question here is
 1474 whether the additional information included in the security functions introduces
 1475 no potential security weakness, such as possibilities to bypass tamper with, or
 1476 deactivate other IT security functions. Based on the mapping of security
 1477 functions and TOE SFRs in section 8.3.1 above, in the following this additional
 1478 information in the security functions is listed, and its impact on the mutual
 1479 support is discussed:

Security Function	Additional Information in SF compared to corresponding SFR(s)	Impact on Mutual Support of Security Functions
SF.AUT_GP	Mechanism <i>challenge-response authentication</i> (with a new random number for each authentication attempt) introduced.	Only impact on SF_AUT_GP itself; not in conflict with any other SF.
	Mechanism <i>session key generation</i> introduced; link to SF.CP_GP introduced.	Impact on SF.AUT_GP itself; is a precondition to support SF_CP_GP; not in conflict with any other SF.
SF.CP_GP	Mechanism <i>decryption using session keys</i> introduced; link to SF.AUT_GP introduced.	Impact on SF_CP_GP itself; is supported by session key generation of SF.AUT_GP; not in conflict with any other SF.
	Mechanism <i>verification of cryptographic checksum using session keys</i> introduced; link to SF.AUT_GP introduced.	Impact on SF_CP_GP itself; is supported by session key generation of SF.AUT_GP; not in conflict with any other SF.
SF.CP_MK	Mechanism <i>command-wise MAC verification using session keys</i> introduced.	Only impact on SF_CP_MK itself; not in conflict with any other SF.
	Mechanism <i>command-wise decryption using session keys</i> introduced.	Only impact on SF_CP_MK itself; not in conflict with any other SF.
SF.AC	None.	N/A
SF.LCM	None.	N/A
SF.SDP	Details concerning kind and time of checksum verification introduced.	Impact on SF.SDP and all other SFs, as the TOE will stop a running session in case of a detected integrity error, but not in conflict with the other SFs or the corresponding security objectives.
SF.USE_WK	None.	N/A.

1480

Table 8-7: Analysis of Mutual Support of the SFs

1481 8.3.3 Validity of SOF-claims
 1482 In section 5 the minimum strength of function level for this TOE is claimed to
 1483 be SOF-high. This is consistent to the strength of function claims in section
 1484 6.2, which is also SOF-high for all the rateable functions.

1485 8.3.4 Compliance of assurance measures
 1486 The assurance measures as stated in section 6.3 address all aspects of the
 1487 assurance requirements of the chosen set EAL4+ and are therefore compliant
 1488 in principle. Whether this is actually the case will be inspected during
 1489 evaluation of the corresponding evidence.

1490 **8.4 PP Claims Rationale**

1491 This security target claims conformance to the protection profile [JCSPP],
 1492 “Minimal Configuration”, as the security target of the underlying platform,
 1493 [JCOP41V231ST], does. The following subsections show that this ST is in fact
 1494 conformant to [JCSPP], “Minimal Configuration”

1495 8.4.1 PP Conformance concerning Assumptions

1496 All assumptions from [JCSPP] “Minimal Configuration” (which are restated in
 1497 [JCOP41V231ST]) are covered by this ST the following way:

Assumption from [JCSPP] (and [JCOP41V231ST])	Coverage in this ST
A.NATIVE (native code APIs/applications ensure that security policies/objectives are not violated)	by A.DEV (during TSAM production/operation no native code will be loaded into the smart card controller)
A.NO-DELETION (no deletion of installed applets/packages possible)	by A.DEV (GlobalPlatform keys are not delivered, therefore no applet management possible)
A.NO-INSTALL (no post-issuance installation of applets)	
A.VERIFICATION (byte code is verified to ensure its validity at execution time)	by A.DEV (byte code verification will be performed during TSAM development/production)

1498 **Table 8-8: Coverage of Assumptions from [JCSPP] “Minimal Configuration”**

1499 8.4.2 PP Conformance concerning Threats

1500 All threats from [JCSPP] “Minimal Configuration” (regarding the Java Card
 1501 platform level) are contained in [JCOP41V231ST] and also this ST, see Table
 1502 3-1 hereinbefore. The additional threats in this ST regard the TSAM application
 1503 level and are not in contradiction to the ones from [JCSPP].

1504 8.4.3 PP Conformance concerning Organizational Security Policies

1505 There is no OSP in [JCSPP] “Minimal Configuration”.

1506 8.4.4 PP Conformance concerning Security Objectives for the TOE

1507 The security objectives for the TOE from [JCSPP] Minimal Configuration, all
 1508 regarding the Java Card platform level, are contained in [JCOP41V231ST] and
 1509 also this ST, see section 4.1.2 hereinbefore. The additional security objectives

1510 for the TOE in this ST regard the TSAM application level and are not in
 1511 contradiction to the ones from [JCSPP].

1512 8.4.5 PP Conformance concerning Security Objectives for the Environment

1513 All security objectives for the environment from [JCSPP] “Minimal Configuration”
 1514 (which are restated in [JCOP41V231ST]) are covered by this ST the following
 1515 way:

Security Objective from [JCSPP] (and [JCOP41V231ST])	Coverage in this ST
OE.NATIVE (native code APIs/applications ensure that security policies/objectives are not violated)	by SOE.DEV (during TSAM production/operation no native code will be loaded into the smart card controller)
OE.NO-DELETION (no deletion of installed applets/packages possible)	by SOE.DEV (GlobalPlatform keys are not delivered, therefore no applet management possible)
OE.NO-INSTALL (no post-issuance installation of applets)	
OE.VERIFICATION (byte code is verified to ensure its validity at execution time)	by SOE.DEV (byte code verification will be performed during TSAM development/production)

1516 **Table 8-9: Coverage of Environment Security Objectives from [JCSPP]**

1517 8.4.6 PP Conformance concerning SFRs for the TOE

1518 The SFRs for the TOE from [JCSPP] Minimal Configuration, all regarding the
 1519 Java Card platform level, are contained in [JCOP41V231ST] and also this ST,
 1520 see section 5.2 hereinbefore. The additional SFRs for the TOE in this ST regard
 1521 the TSAM application level and are not in contradiction to the ones from
 1522 [JCSPP].

1523 Furthermore [JCSPP] defines the minimum strength of function level to be SoF-
 1524 medium. Here this claim is exceeded by using SoF-high, therefore this ST is
 1525 conformant to [JCSPP] concerning the minimum SoF-claim.

1526 8.4.7 PP Conformance concerning SARs

1527 [JCSPP] claims conformance to EAL4 augmented by AVA_VLA.3 and
 1528 ADV_IMP.2. In this ST conformance to EAL4 augmented by AVA_VLA.4 and
 1529 ADV_IMP.2 is claimed. As AVA_VLA.4 is hierarchical to AVA_VLA.3, this ST is
 1530 conformant to [JCSPP] concerning the security assurance requirements.

1531 8.4.8 PP Conformance concerning SFRs for the IT Environment

Environment SFRs from [JCSPP]	Coverage in this ST
[JCSPP] section 5.1.3, “BCVG” SFRs (for byte code verification)	Included in section 5.5 of this ST; will be regarded during production of the composite TSAM TOE
[JCSPP] section 5.1.9, “SCPG” SFRs (for smart card platform, i.e. operating system and chip)	Already regarded in terms of SFRs for [JCOP41V231] during the corresponding evaluation, not relevant for the IT environment of the composite TSAM TOE

Environment SFRs from [JCSPP]	Coverage in this ST
[JCSPP] section 5.1.10, "CMGRG" SFRs (for card manager)	Already regarded in terms of SFRs for [JCOP41V231] during the corresponding evaluation, not relevant for the IT environment of the composite TSAM TOE

1532

Table 8-10: Coverage of IT Environment SFRs from [JCSPP]

1533 **9 Appendix**

1534 **9.1 Abbreviations**

1535	3/DES	Triple Data Encryption Standard
1536	APDU	Application Protocol Data Unit
1537	BCV	Byte Code Verification
1538	BSN	Batch Settlement Number
1539	DES	Data Encryption Standard
1540	EEPROM	Electrically Erasable Programmable Read Only Memory
1541	GP	GlobalPlatform
1542	GPK	GlobalPlatform Key
1543	IC	Integrated Circuit
1544	JC	JavaCard
1545	JCP	JavaCard Platform
1546	LCS	Life Cycle State
1547	MAC	Message Authentication Code
1548	MID	Merchant Identifier
1549	MK	Management Key
1550	OSP	Organizational Security Policy
1551	POS	Point Of Sales
1552	PP	Protection Profile
1553	RC	Retry Counter
1554	ROM	Read Only Memory
1555	SAR	Security Assurance Requirement
1556	SCP	Smart Card Platform
1557	SF	Security Function
1558	SFP	Security Function Policy
1559	SFR	Security Functional Requirement
1560	SO	Security Objective
1561	SOE	Security Objective for the Environment
1562	ST	Security Target
1563	TD	Transaction Data
1564	TID	Terminal Identifier
1565	TMD	Terminal Management Data
1566	TOE	Target of evaluation
1567	TSAM	Terminal Security Access Module
1568	TSN	Transaction Serial Number
1569	TSF	TOE Security Functions
1570	TSP	TOE Security Policy
1571	WK	Working Key

1572 **9.2 References**

1573 [3/DES] Federal Information Processing Standard Publication, FIPS
1574 PUB 46-3 October 1999.

1575 [AIS20] Anwendungshinweise und Interpretationen zum Schema,
1576 AIS 20: Funktionalitätsklassen und
1577 Evaluationsmethodologie für deterministische
1578 Zufallszahlengeneratoren, Version 1, 02.12.1999,
1579 Bundesamt für Sicherheit in der Informationstechnik

1580 [ANSI X9.52] Triple Data Encryption Algorithm Modes of Operation

1581 [ANSI X9.9] Financial Institution Message Authentication

1582 [CC] Common Criteria for Information Technology Security
1583 Evaluation, August 2005, Version 2.3

1584 [CEM] Common Evaluation Methodology for Information
1585 Technology Security Evaluation, August 2005, Version 2.3

1586 [ISO7816-4] *ISO/IEC 7816-4, Information technology - Identification
1587 cards - Integrated circuit(s) cards with contacts - Part 4:
1588 Interindustry commands for interchange*

1589 [JCOP41V231] NXP P541G072V0P (JCOP 41 v2.3.1), Secure Smart Card
1590 Controller

1591 [JCOP41V231ST] *SECURITY TARGET, NXP P541G072V0P (JCOP 41
1592 v2.3.1), Secure Smart Card Controller, version 2.13, date
1593 2007-06-01, IBM Deutschland Entwicklung GmbH*

1594 [JCSPP] *Java Card System Protection Profile Collection, Version:
1595 1.0b, August 2003. This Document contains 4 protection
1596 profile, whereas "Java Card System – Minimal
1597 Configuration Protection Profile" (registered at DCSSI under
1598 Registration number PP/0303) is relevant for this ST*

1599 [PP0002] *Smartcard IC Platform Protection Profile, Version 1.0, July
1600 2001 (registered at BSI under Registration number BSI-PP-
1601 0002)*

1602 [ST0348] Security Target Lite, BSI-DSZ-CC-0348, Version 1.2,
1603 17.01.2006, *Evaluation of the Philips P5CT072V0P,
1604 P5CC072V0P, P5CD072V0P and P5CD036V0P Secure
1605 Smart Card Controllers*