Bundesamt
für Sicherheit in der
Informationstechnik

Assurance Continuity Maintenance Report

BSI-DSZ-CC-0501-2008-MA-01

S3CC9LA 16-bit RISC Microcontroller for Smart Card, Revision 0

from

Samsung Electronics Co., Ltd.

Common Criteria Arrangement
for components up to EAL4

The IT product identified in this report was assessed according to the *Assurance Continuity: CCRA Requirements,* version 1.0, February 2004 and the developers Impact Analysis Report (IAR). The baseline for this assessment was the Certification Report, the Security Target and the Evaluation Technical Report of the product certified by the Federal Office for Information Security (BSI) under BSI-DSZ-CC-0501-2008.

The change to the certified product is at the level of a different configuration setting by blocking of the EEPROM size from 72 kByte to 36 kByte, a change that has no effect on assurance. The identification of the maintained product is indicated by a new version number compared to the certified product.

Consideration of the nature of the change leads to the conclusion that it is classified as a minor change and that certificate maintenance is the correct path to continuity of assurance.

Therefore, the assurance as outlined in the Certification Report BSI-DSZ-CC-0501-2008 is maintained for this version of the product. Details can be found on the following pages.

This report is an addendum to the Certification Report BSI-DSZ-CC-0501-2008.

Bonn, 10 July 2008

Common Criteria

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Assessment

The IT product identified in this report was assessed according to the *Assurance Continuity: CCRA Requirements* [1] and the Impact Analysis Report (IAR) [2]. The baseline for this assessment was the Certification Report of the certified product (Target of Evaluation, TOE) [3], the Security Target Lite [4] and the Evaluation Technical Report as outlined in [3].

The vendor for the S3CC9LA 16-bit RISC Microcontroller for Smart Card, Revision 0 , Samsung Electronics Co., Ltd., submitted an IAR [2] to the BSI for approval. The IAR is intended to satisfy the requirements outlined in the document *Assurance Continuity: CCRA Requirements* [1]. In accordance with those requirements, the IAR describes (i) the changes made to the certified TOE, (ii) the evidence updated as a result of the changes and (iii) the security impact of the changes.

The S3CC9LA 16-bit RISC Microcontroller for Smart Card, Revision 0 was changed due to reduce of the EEPROM size from 72 kByte to 36 kByte. The change is not significant from the standpoint of security, however Configuration Management procedures required a change in the version number from S3CC9LC, Revision 2 to S3CC9LA, Revision 0. The device type for S3CC9LA, Revision 0 is identified by 150Ah. This information is stored in the EEPROM and can be read out by the user of the card via the normal EEPROM read command.

Conclusion

The change to the TOE is at the level of different configuration setting by blocking of the EEPROM size from 72 kByte to 36 kByte, a change that has no effect on assurance. Examination of the evidence indicates that the changes peformed are limited to the Manuals [6] and [7] of the TOE. Security Target Lite [4] was editorially updated. Consideration of the nature of the change leads to the conclusion that it is classified as a minor change and that certificate maintenance is the correct path to continuity of assurance.

Therefore, BSI agrees that the assurance as outlined in the Certification Report [3] is maintained for this version of the product. This report is an addendum to the Certification Report [3].

References

[1] Common Criteria document CCIMB-2004-02-009 "Assurance Continuity: CCRA Requirements", version 1.0, February 2004

[2] Security Impact Analysis, S3CC9LC and S3CC9LA Comparison, version 1.2, issued on 2th July 2008, Samsung Electronics (confidential document)

[3] Certification Report BSI-DSZ-CC-0501-2008 for S3CC9LC 16-bit RISC Microcontroller for Smart Card, revision 2 from Samsung Electronics Co., Ltd., Bundesamt für Sicherheit in der Informationstechnik, 01. July 2008.

[4] Security Target Lite of S3CC9LA 16-bit RISC Microcontroller for Smart Cards, version 1.0, 16th June 2008, Samsung Electronics

[5] Configuration Management Documentation (Class ACM_AUT/CAP/SCP), <CHEYENNE(S3CC9LA)>, version 1.4, issued on 3rd July, 2008, Samsung Electronics (confidential document),

[6] User's Manual, S3CC9LC/LA/L5,16-Bit CMOS Microcontroller for Smart Card, version 2.0, June 2008, Samsung Electronics

[7] Security Application Note, S3CC9LC family, version 1.2, Samsung Electronics