STMICROELECTRONICS

COMMON CRITERIA FOR IT SECURITY EVALUATION

TRUSTED PLATFORM MODULES

ST33KTPM2A & ST33KTPM2I

TPM FIRMWARE
10.257

SECURITY TARGET

DOCUMENT REVISION

Version	Date	Author	Modifications
01-00	18/Jan/2022	Olivier Collart	First draft for ST33KTPM2A & ST33KTPM2I
01-01	13/Sep/2023	Olivier Collart	Update sites in TOE lifecycle and TOE documentation
01-02	6/Feb/2024	Olivier Collart	Include evaluator's comments and NesLib 6.7.4 certificate reference
01-02p	6/Feb/2024	Olivier Collart	Public release

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1 INTRODUCTION (ASE_INT)

This section contains the necessary information to identify the Security Target (ST). This information may be used to cross-reference this document.

1.1 ST Reference

This security target is referenced with the following information:

- Filename: ST33KTPM2AI_ST
- Revision: 1.2
- Internal documentation system reference: SMD_ST33KTPM2AI_ST_23_001
- Date: 6/Feb/2024

1.2 Purpose

This document presents the Security Target (ST) of the Target of Evaluation covering both products ST33KTPM2A and ST33KTPM2I.

The product references and definitions of the TOE are provided in Chapter 2.

A list of acronyms is provided in Chapter 8.2

2 TOE DESCRIPTION

2.1 TOE identification

Table 1: Target of evaluation reference

Devices	TPM embedded software Version Major.Minor[1] (hexadecimal)	User documentation label
ST33KTPM2A	10.257 (0x00 0x0A.0x01 0x01)	10.257.UD01
ST33KTPM2I		

The TOE is a composite TOE built up with the combination of

- The hardware platforms ST33K1M5A and ST33K1M5M B02, designed by STMicroelectronics and used as certified platform
  - IC Maskset name: K4A0
  - Master identification number:
    - 0x0260 for ST33K1M5A
    - 0x024B for ST33K1M5M
  - IC version: B
  - IC Firmware version 3.1.4
- The cryptographic library NesLib developed by STMicroelectronics and used as a certified library
  - NesLib version: 6.7.4
- The TPM embedded software (ES) including the TPM firmware 10.257 supporting an exclusive selection I2C or SPI for both ST33KTPM2I and ST33KTPM2A. This TPM firmware is compiled with the cryptographic library NesLib 6.7.4.

And includes also

- the set of user documentation described in Table 2, labelled globally with a tag "**10.257.UD01"**, including TCG standard specifications and ST proprietary specifications (Products datasheets and Security recommendations).

The chip package is not included in the TOE.

2.2 Target of evaluation Overview

The products ST33KTPM2A and ST33KTPM2I are TPM 2.0 products targeting PC, server platforms and embedded systems. The product ST33KTPM2A complies with automotive qualification standard AEC-Q100 whereas the product ST33KTPM2I meets the qualification criteria for industrial and consumer applications.

The products ST33KTPM2A and ST33KTPM2I implement an SPI interface as defined in [12] and an I²C interface as defined in [12]. The support of the SPI or I²C interface is exclusive and can be configured by the end user.

The security target describes the target of evaluation (TOE) named "ST33KTPM2AI" and provides a product summary.

[1] The firmware major and minor versions may be retrieved from the TOE with the command TPM2_GetCapability [9], in the response field TPM_PT_FIRMWARE_VERSION_1 and formatted with the value 0x00 0x0A 0x01 0x01 according to [11], Table 1.
End user tools report the version in decimal value. In that case, the version retrieved is 10.257.

The TOE are devices that implement the functions defined in the TCG Trusted Platform Module Library Specification, version 2.0, [7], [8], [9], [10] and the PC Client Specific Platform TPM Profile for TPM 2.0 [11]. The TCG Trusted Platform Module Library specification describes the design principles, the TPM structures, the TPM commands and supporting routines for the commands. The PC Client Specific Platform TPM Profile for TPM 2.0 specification describes the additional features and communication interfaces that must be implemented by a TPM for a PC Client platform.

The product lines ST33KTPM2A and ST33KTPM2I are also compliant with the PC Client Specific Platform TPM Profile for TPM 2.0 [11] for the communication interfaces SPI and I²C to leverage the drivers and software stacks already available.

The TOE consists of

- ST33K1M5A and ST33K1M5M hardware, loaded with TPM embedded software (including the Cryptographic Library NesLib)
- TPM firmware loadable image (including the Cryptographic Library NesLib)
- TPM user documentation.

The TOE components are described in 2.3

2.2.1 TOE Usage and Security Features

The TPM library specification describes the TPM protections in terms of Protected Capabilities and Protected Objects. A Protected Capability is an operation that must be correctly performed for a TPM to be trusted and therefore is in the scope of the CC evaluation as part of the TOE security functionality (TSF). A Protected Object is data that must be protected for a TPM operation to be trusted. The TSF performs all operations with Protected Objects inside the TPM. The TSF protects the confidentiality of Protected Objects when exported from the TPM and checks the integrity of Protected objects when imported into the TPM. The TOE provides physical protection for Protected Objects residing in the TPM.

The TPM provides methods for collecting and reporting identities of hardware and software components of a computer system platform. The computer system report generated by the trusted computing base (TCB) the TPM is part of allows determination of expected behaviour and from that expectation of trust in the computer system platform.

There are commonly three Roots of Trust in a trusted platform, a root of trust for measurement (RTM), root of trust for reporting (RTR) and root of trust for storage (RTS). In TCG systems roots of trust are components that must be trusted because misbehaviour might not be detected. The RTM is a computing engine capable of making inherently reliable integrity measurements and maintaining an accurate summary of values of integrity digests and the sequence of digests. The RTR is a computing engine capable of reliably reporting information held by the RTM. The RTS provides secure storage for a practically unlimited number of private keys or other data by means of exporting and importing encrypted data.

Support for the Root of Trust for Measurement

The TPM supports the integrity measurement of the trusted platform by calculation and reporting of measurement digests of measured values. Typically, the RTM is controlled by the Core Root of Trust for Measurement (CRTM) as the starting point of the measurement. The measurement values are representations of embedded data or program code scanned and provided to the TPM by the measurement agent. The TPM supports cryptographic hashing of measured values and calculates the measurement digest by extending the value of a PCR with a calculated or provided hash value. The PCRs are shielded locations of the TPM which can be reset by TPM reset or a trusted process, written only through measurement digest extensions and read.

Root of Trust for Reporting

The EK and the corresponding Endorsement Certificates define the trusted platform identities for RTR. The ST33KTPM2AI are shipped with EKs and for each EK, a Certificate of the Authenticity of this EK is also provided. The EK may be bound to the Platform via Platform Certificate, providing assurance from the certification body of the physical binding and connection through a trusted path between the platform (the RTM) and the genuine TPM (the RTR). The attestation of the EK and the Platform Certificates builds the base for attestation of other keys and measurements.

Root of Trust for Storage

The TPM holds the Storage Primary Seed (SPS) and generates Storage Root Keys (SRK) from SPS. The SRK are roots of Protected Storage Hierarchies associated with a TPM. The storage keys in these hierarchies are used for symmetric encryption and signing of other keys and data together with their security attributes. The resulting encrypted file, which contains header information in addition to the data or the key, is called a BLOB (Binary Large Object) and is output by the TPM and can be loaded in the TPM when needed. The private keys generated on the TPM can be stored outside the TPM (encrypted) in a way that allows the TPM to use them later without ever exposing such keys in the clear outside the TPM. The TPM uses symmetric cryptographic algorithms to encrypt data and keys and may implement cryptographic algorithms of equivalent strength.

Platform Key Hierarchy

The TPM may hold a Platform Primary Seed (PPS) and generate Platform Keys from PPS. The platform key hierarchy is controlled by the Platform Firmware. The PPS is generated by the TOE.

Other Security Services and Features

The TOE provides cryptographic services for hashing, asymmetric encryption and decryption, asymmetric signing and signature verification, symmetric encryption and decryption, symmetric signing and signature verification by means of and key generation. Hash functions SHA-1, SHA-256, SHA_384, SHA3_256 and SHA3_384 are provided as cryptographic service to external entities for measurements and used internally for user authentication, signing and key derivation. A TOE is required to implement asymmetric algorithms, where the current specification supports RSA with 2048, 3072 bits and 4096 for digital signature, secret sharing and encryption and ECC algorithms ECDSA, ECDAA and ECSchnorr with P-256, P-384 and BN-256 curves for digital signatures and secret sharing. The TOE provides symmetric encryption and decryption of AES-128 192 and 256 in CFB, CTR, OFB, CBC and ECB modes. The TOE implements symmetric signing and signature verification by means of HMAC. The TOE generates two types of keys: Ordinary keys are generated using the random number generator to seed the key computation. Primary Keys are derived from a Primary Seed and key parameters by means of a key derivation function.

The TPM stores persistent state associated with the TPM in NV memory and provides NV memory as a shielded location for data of external entities. The platform and entities authorised by the TPM owner controls allocation and use of the provided NV memory. The access control may include the need for authentication of the user, delegations, PCR values and other controls.

The TSF also includes random number generation, self-test and physical protection.

Generation and import of the Endorsement key pair and certificate

The Endorsement Key (EK) and associated EK certificate (EK credential) are stored in the TPM during the manufacturing process at the TOE lifecycle phase “Manufacturing”.

Each TOE supports three Endorsement keys

- One 2048-bit RSA key pair
- One 256-bit ECC key pair generated with curve TPM_ECC_NIST_P256.
- One 384-bit ECC key pair generated with curve TPM_ECC_NIST_P384

Each Endorsement key is generated by a HSM (Hardware Security Module) and then stored encrypted on a key server.

The Endorsement Key certificate is generated also by a HSM that stores the STMicroelectronics intermediate CA (Certification Authority) keys. The certificates are stored on a certificate server. CA keys are stored outside the HSM in backup encrypted with a 3-DES key. This backup key is generated under segregated control by 3 different security officers.

The RSA 2048 EK, ECC_NIST_P256 EKs and ECC_NIST_P384 EKs are certified by three specific intermediate CAs using a NIST_P384 key.

Both certificates comply with the templates defined in the TCG specification for TPM 2.0 EK certificates [45].

The importation of the EK and EK certificate in the TOE is done by the personalization infrastructure that requests EK and EK certificate to the key and certificate servers. The personalization infrastructure decrypts the EK private key and writes it encrypted on the chip with the EK certificate.

The key server, certificate server, HSM and the personalization infrastructure are all located within the secure production area of the TOE.

The STMicroelectronics intermediate certificates are described in a document publicly available [42].

2.3 TOE Description

2.3.1 TOE hardware description

The ST33K1M5A and ST33K1M5M hardware platforms configuration B02 are covered by the common criteria certificate NSCIB-CC-2300112-01.

The description of the ST33K1M5A and ST33K1M5M hardware platforms can be found in the Security target for composition [48].

The Security target for composition [48] describes the Security Functional Requirements and the Security Assurance Requirements applicable to the ST33K1M5A and ST33K1M5M.

2.3.2 *TOE embedded software description*

The cryptographic library NesLib 6.7.4 is covered by the common criteria certificate NSCIB-CC-2300177-01.

The Security target for composition [49] describes the Security Functional Requirements and the Security Assurance Requirements applicable to NesLib 6.7.4.

The TPM firmware architecture “2X” is common to all products included in the TOE. The TPM ES is composed of three independent blocks:

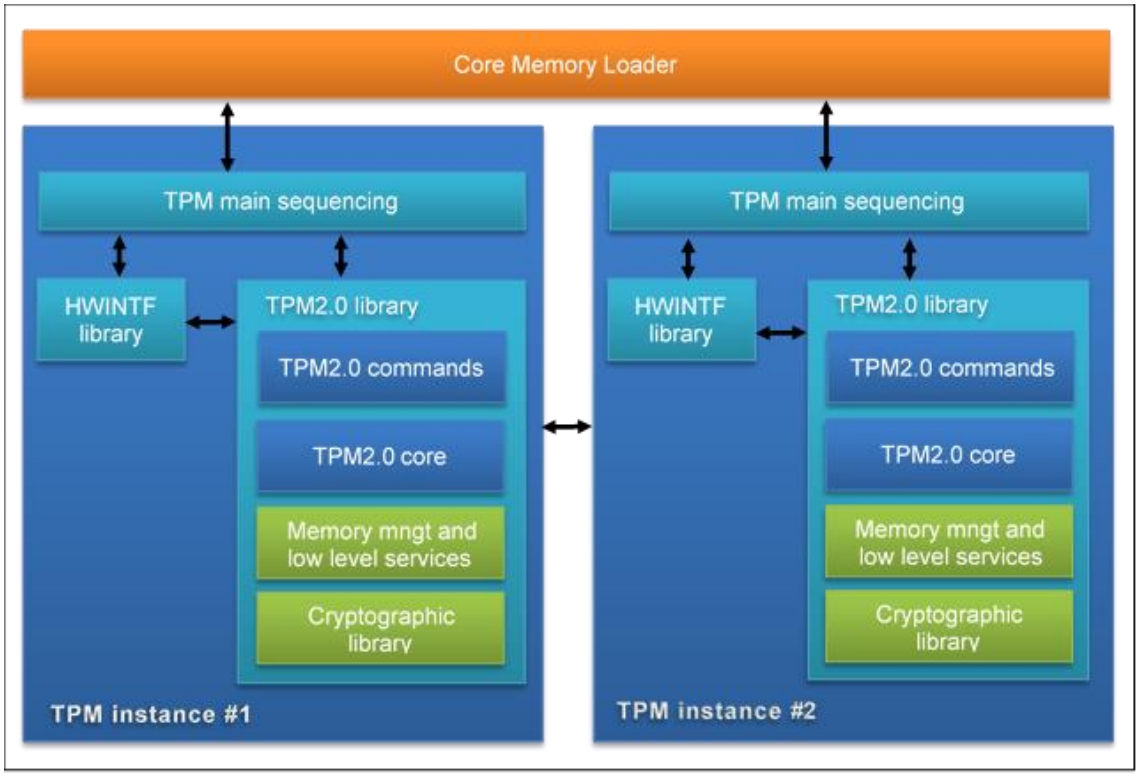
- A non-upgradable code block located in ROM & flash memories (orange box)
  - Core memory loader (CML) in charge of verifying integrity of the TPM instance to be executed.
- Two independent code blocks upgradable via secure field upgrade mechanism (TPM instances #1 and #2). They are composed of:
  - TPM2.0 commands code
  - TPM2.0 core
  - Memory management and low-level services
  - Cryptographic library (NesLib 6.7.4)

From the two code block instances, only one is executed.

The two-instance code architecture provides two resilience features.

- Fault tolerant TPM firmware upgrade: if the TPM firmware loading process is interrupted, the loading instance remains fully functional. The TPM doesn’t enter any limited mode.
- Self-recovery: in case of TPM firmware integrity error of one instance, the second instance becomes active.

Figure 1: TPM firmware block diagram



The TPM loadable firmware image includes the two independent code blocks in a format optimized for code size, encrypted for confidentiality and signed for authenticity.

2.3.3 *TOE guidance documentation*

The following documents must be used by the TOE user in order to configure and operate the TOE.

Table 2: User Documentation

User Documentation	Version	Date	Ref
TPM Library Part 1: Architecture, Specification Version 2.0, Revision 1.59	Revision 1.59	November 8, 2019	[7]
TPM Library Part 2: Architecture, Specification Version 2.0, Revision 1.59	Revision 1.59	November 8, 2019	[8]
TPM Library Part 3: Architecture, Specification Version 2.0, Revision 1.59	Revision 1.59	November 8, 2019	[9]
TPM Library Part 4: Architecture, Specification Version 2.0, Revision 1.59	Revision 1.59	November 8, 2019	[10]
Errata version 1.4 for TCG TPM library version 2.0 revision 1.59	1.4	January 9, 2023	[11]
TCG PC Client Specific Platform TPM Profile for TPM 2.0 (PTP), Family “2.0”, Version 1.05 revision 14	1.05	September 4, 2020	[12]
Errata for PC Client Platform TPM Profile for TPM 2.0 Version 1.05 Revision 14 – version 1.0	1.0	September 4, 2020	[13]
TCG EK credential profile for TPM Family 2.0 Level 0. Specification Version 2.3 Revision	2.2	July 23, 2020	[45]
ST33KTPM2A/STSAFE-V100-TPM Trusted Platform Module for Automotive applications	V1	June 30, 2023	[41]
ST33KTPM2I - STSAFE-TPM for Consumer and Industrial applications	V1	June 28, 2023	[42]
ST33KTPM2X - Security recommendations	V1.2	June 2022	[44]

2.3.4 *Forms of delivery*

The TOE is delivered in form of complete chips which include the hardware loaded with the TPM firmware, the Endorsement Primary Keys and certificates, and the guidance documentation. The TOE is finished and the extended test or diagnostic features are irreversibly disabled.

The TOE is delivered in different packages. The product behaviour and the ordering codes are described in the products datasheet [41] and [42].

2.4 TOE lifecycle

The life cycle of the TOE as part of this evaluation includes

- phase 1 “Development” and
- phase 2 “Manufacturing”

as defined in the PP [14].

The phase 1 that includes TPM firmware development involves the site of

- ST RENNES (FRANCE)

for the embedded software development activities.

The phase 2 that includes the die manufacturing and the EK and EK certificate injections involves the sites of

- ST CROLLES (FRANCE) (Manufacturing)
- ST ROUSSET (FRANCE) (Test Manufacturing and EK/EK certificate injection)
- ST TOA PAYOH (SINGAPORE) (Test Manufacturing and EK/EK certificate injection)

The phase 2 ends with the delivery of the TOE.

3 CONFORMANCE CLAIM (ASE_CCL)

3.1 CC Conformance Claim

This security target is **conformant** to the Common Criteria version 3.1 R5.

This security target claims to be Common Criteria version 3.1 R5

- Part 1 **conformant**,
- Part 2 **extended** and
- Part 3 **conformant**.

The extended Security Function Requirement is defined in the protection profile.

This ST is conforming to assurance package EAL4 augmented with

- ALC_DVS.2
- ALC_FLR.1 and
- AVA_VAN.5

defined in CC Part 3.

3.2 PP Claim

This security target is in **strict conformance** to the PC Client Specific Trusted Platform Module Family 2.0 level 0 Revision 1.59, Version 1.3, released by the Trusted Computing Group dated 29 September 2021.

The protection profile is registered and certified by the “Agence Nationale de la Sécurité des Systèmes d’Information” (ANSSI) under the reference [ANSSI-CC-PP-2021/02].

3.3 Package claim

This security target claims conformance to an optional package “ECDAA PP-Module” defined in the TPM 2.0 Protection Profile [14].

The package has been directly merged in this ST: specific requirements are not explicitly referenced as being part of the optional package “ECDAA PP-Module”.

3.4 Conformance Rationale

This security target claims **strict conformance** to only one PP.

The Target of Evaluation (TOE) is a complete solution implementing the TCG Trusted Platform Module main specifications Version 2.0 level 0 revision 1.59 ([7], [8] ,[9] and [10]) and the TCG PC Client Specific Platform TPM Profile Specification, Version 1.05 [12] as defined in the PP [14] section 3.1.1. So, the TOE is **consistent** with the **TOE type** in the PP [14].

The **security problem** definition of this security target is **consistent** with the statement of the security problem definition in the PP [14], as the security target claims strict conformance to the PP [14] and no other threats, organizational security policies and assumptions are added.

The **security objective**s of this security target are **consistent** with the statement of the security objectives in the PP as the security target claims strict conformance to the PP and no other security objectives are added.

The **security requirements** of this security target are **consistent** with the statement of the security requirements in the PP [14] as the security target claims strict conformance to the PP [14]. All assignments and selections of the security functional requirements are done in the PP [14] and in this security target section 7.2.

3.5 Application notes

The evidence that the PP [14] is compliant with the application note [40] released by the ANSSI (French CC Certification scheme) and defining security requirements for post-delivery code loading is provided in this security target.

The functional requirement FCS_RNG.1 is a refinement of the FCS_RNG.1 defined in the PP [14] according to —Anwendungshinweise und Interpretationen zum Schema (AIS) respectively - Functionality classes for random number generators [38].

4 SECURITY PROBLEM DEFINITION (ASE_SPD)

The contents of the PP [14] applies to this chapter without any restriction or addition.

4.1 Assets

The assets of the TOE are defined in the PP [14] sections 5.1 and 9.3.1 Assets. These assets have to be protected while being executed as well as when the TOE is not in operation.

4.2 Threats

The threats to security are defined in the PP [14], sections 5.2 and 9.3.2 Threats. No other threats are added.

4.3 Organisational Security Policies

The organisational security policies are defined in the PP [14], sections 5.3 and 9.4 Organisational Security Policies, no other organisational security policies are added

4.4 Assumptions

The TOE environment is highly variable. In general, the TOE is assumed to be in an uncontrolled environment with no guarantee of the TOE's physical security.

The TOE assumptions to the IT environment are defined in the PP [14], section 5.4 and 9.5 Assumptions, no other assumptions are added.

5 SECURITY OBJECTIVES

This section shows the security objectives which are relevant for the TOE. For this section the PP [14] can be applied completely.

5.1 Security Objectives for the TOE

The security objectives of the TOE are defined and described in the PP [14], sections 6.1 and 9.6.1 Security Objectives for the TOE.

The security objectives from the Note 6, “Security requirements for post-delivery code loading” [40] released by ANSSI are also included in the TOE security objectives.

5.2 Security Objectives for the Operational Environment

The security objectives for the operational environment are described in the PP [14], sections 6.2 and 9.6.2 Security Objectives for the Operational Environment, no other security objectives for the operational environment are added

5.3 Security Objective Rationale

The security objectives rationale is described in the PP [14], sections 6.3 and 9.6.3 Security Objective Rationale.

The ANSSI Note 6 security objectives rationale is described in 5.4

5.4 ANSSI note 6 Security Objectives Equivalence

Table 3: ANSSI Note 6 Security objectives rationale

Objectives Note 6	Description	Security Objective or SFR equivalence
O.Secure_Load_ACode	The Loader of the Initial TOE shall check an evidence of authenticity and integrity of the loaded Additional Code. The Loader enforces that only the allowed version of the Additional Code can be loaded on the Initial TOE. The Loader shall forbid the loading of an Additional Code not intended to be assembled with the Initial TOE.	Covered by SFR FDP_ACF.1.2/States, iteration 2 from PP [14] Covered by SFR FDP_ACF.1.3/States iterations 1 & 2 from this security target
O.Secure_AC_Activation	Activation of the Additional Code and update of the Identification Data shall be performed at the same time in an Atomic way. All the operations needed for the code to be able to operate as in the Final TOE shall be completed before activation. If the Atomic Activation is successful, then the resulting product is the Final TOE, otherwise (in case of interruption, or incident which prevents the forming of the final TOE), the Initial TOE shall remain in its initial state of fail secure.	Covered by SFR FDP_ACF.1.2/States iteration 3
O.TOE_Identification	The Identification Data identifies the Initial TOE and Additional Code. The TOE provides means to store Identification Data in its non-volatile memory and guarantees the integrity of these data. After Atomic Activation of the Additional Code, the identification Data of the Final TOE allows identifications of the initial TOE and Additional Code. The user shall be able to uniquely identify Initial TOE and Additional Code(s) which are embedded in the Final TOE.	Covered by SFR FCO_NRO.1.2/M&R iteration 6

6 EXTENDED COMPONENTS DEFINITION (ASE_ECD)

The extended component “FCS_RNG Generation of random numbers” is defined in the PP [14], section 7.1. No other extended component is added in this security target.

7 SECURITY REQUIREMENTS (ASE_REQ)

7.1 Security Functional Requirements listed by the TPM 2.0 Protection Profile

The security functional requirements (SFRs) for the TOE are defined in the PP [14] section 8.1 and chapter 9. All assignments and selections of the Security Functional Requirements are done in the PP with the exception of the following SFRs that required to be completed in the security target.

7.2 Security Functional Requirements for the TOE

FMT_MSA.2 Secure security attributes

Hierarchical to: No other components.
Dependencies: [FDP_ACC.1 Subset access control, or
FDP_IFC.1 Subset information flow control]
FMT_MSA.1 Management of security attributes
FMT_SMR.1 Security roles

FMT_MSA.2.1 The TSF shall ensure that only secure values are accepted for: *security attributes of keys, PCRs, NV storage areas, counters and firmware*.

FCS_CKM.1/PKRSA Cryptographic key generation (primary keys)

Hierarchical to: No other components.
Dependencies: [FCS_CKM.2 Cryptographic key distribution, or
FCS_COP.1 Cryptographic operation]
FCS_CKM.4 Cryptographic key destruction

FCS_CKM.1.1/PKRSA The TSF shall generate cryptographic **primary *RSA*** keys in accordance with a specified cryptographic key generation algorithm *RSA key generator* and specified cryptographic key sizes *2048, 3072 and 4096 bits* that meet the following: *TPM library specification [7], [8], [9]* in combination with [SP800-108], and [IEEE1363], [RFC 3447].

Note: *The selection of the key sizes for the SFR FCS_CKM.1.1/PKRSA does not include 4096 bits in the protection profile. The key size has been added to avoid a second instance for a better readability.*

FCS_CKM.1/PKECC Cryptographic key generation (primary keys)

Hierarchical to: No other components.
Dependencies: [FCS_CKM.2 Cryptographic key distribution, or
FCS_COP.1 Cryptographic operation]
FCS_CKM.4 Cryptographic key destruction

FCS_CKM.1.1/PKECC The TSF shall generate cryptographic **primary *ECC*** keys in accordance with a specified cryptographic key generation algorithm *ECC key generator* and specified cryptographic key sizes *256 and 384 bits* that meet the following: *TPM library specification [7], [8], [9]*, in combination with [SP800-108].

FCS_CKM.1/PKAES Cryptographic key generation (primary keys)

Hierarchical to: No other components.
Dependencies: [FCS_CKM.2 Cryptographic key distribution, or
FCS_COP.1 Cryptographic operation]
FCS_CKM.4 Cryptographic key destruction

FCS_CKM.1.1/PKAES The TSF shall generate cryptographic **primary *symmetric*** keys in accordance with a specified cryptographic key generation algorithm *AES key generator* and specified

cryptographic key sizes *128, 192 & 256 bits,* that meet the following: *TPM library specification [7], [8], [9]* in combination with [SP800-108], .

FCS_CKM.1/RSA Cryptographic key generation (RSA keys)

Hierarchical to: No other components.
Dependencies: [FCS_CKM.2 Cryptographic key distribution, or FCS_COP.1 Cryptographic operation]
FCS_CKM.4 Cryptographic key destruction

FCS_CKM.1.1/RSA The TSF shall generate cryptographic **RSA** keys in accordance with a specified cryptographic key generation algorithm *RSA key generator* and specified cryptographic key sizes *2048, 3072 and 4096 bits* that meet the following: *TPM library specification [7], [8], [9]*, [RFC 3447] and [IEEE1363].

FCS_CKM.1/ECC Cryptographic key generation (ECC keys)

Hierarchical to: No other components.
Dependencies: [FCS_CKM.2 Cryptographic key distribution, or FCS_COP.1 Cryptographic operation]
FCS_CKM.4 Cryptographic key destruction

FCS_CKM.1.1/ECC The TSF shall generate cryptographic **ECC** keys in accordance with a specified cryptographic key generation algorithm *ECC key generator* and specified cryptographic key sizes *256 and 384 bits* that meet the following: *TPM library specification [7], [8], [9]*.

FCS_CKM.1/SYMM Cryptographic key generation (symmetric keys)

Hierarchical to: No other components.
Dependencies: [FCS_CKM.2 Cryptographic key distribution, or FCS_COP.1 Cryptographic operation]
FCS_CKM.4 Cryptographic key destruction

FCS_CKM.1.1/SYMM The TSF shall generate cryptographic **symmetric** keys in accordance with a specified cryptographic key generation algorithm *AES key generator* and specified cryptographic key sizes *128, 192 & 256 bits* that meet the following: *TPM library specification [7], [8], [9]*.

FCS_CKM.4 Cryptographic key destruction

Hierarchical to: No other components.
Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]

FCS_CKM.4.1 The TSF shall destroy cryptographic keys in accordance with a specified cryptographic key destruction method *key overwriting and NV memory zeroization* that meets the following: *none*.

FCS_COP.1/AES Cryptographic operation (symmetric encryption/decryption)

Hierarchical to: No other components.

Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/AES The TSF shall perform symmetric encryption and decryption in accordance with a specified cryptographic algorithm AES in the mode CFB, *CTR, OFB, CBC and ECB* and cryptographic key sizes 128*, 192 and 256* bits that meet the following: [FIPS 197] and [SP 800-38A]

FCS_COP.1/SHA Cryptographic operation (hash function)

Hierarchical to: No other components.

Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/SHA The TSF shall perform hash value calculation in accordance with a specified cryptographic algorithm SHA-1, SHA-256 and SHA-384 and cryptographic key sizes *none* that meet the following: FIPS 180-4.

FCS_COP.1/SHA3 Cryptographic operation (hash function)

Hierarchical to: No other components.

Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/SHA3 The TSF shall perform hash value calculation in accordance with a specified cryptographic algorithm SHA3-256 and SHA3-384 and cryptographic key sizes *none* that meet the following: FIPS 202.

FCS_COP.1/HMAC Cryptographic operation (HMAC calculation)

Hierarchical to: No other components.

Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/HMAC The TSF shall perform HMAC value generation and verification in accordance with a specified cryptographic algorithm HMAC with SHA-1, SHA-256 and SHA-384 and cryptographic key sizes *160, 256 and 384 bits* that meet the following: [FIPS 198-1] [26].

FCS_COP.1/HMAC/SHA3 Cryptographic operation (HMAC calculation)

Hierarchical to: No other components.
Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/HMAC/SHA3 The TSF shall perform HMAC value generation and verification in accordance with a specified cryptographic algorithm HMAC with SHA3-256 *and SHA3-384* and cryptographic key sizes *256 and 384 bits* that meet the following: [FIPS 198-1] [26].

FCS_COP.1/RSAED Cryptographic operation (asymmetric encryption/decryption)

Hierarchical to: No other components.
Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/RSAED The TSF shall perform asymmetric encryption and decryption in accordance with a specified cryptographic algorithm RSA without padding, RSAES-PKCS1-v1_5, RSAES-OAEP and cryptographic key sizes 2048 bits; 3072 bits and 4096 bits that meet the following: PKCS#1v2.1 [37].

FCS_COP.1/RSASign Cryptographic operation (RSA signature generation/verification)

Hierarchical to: No other components.
Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/RSASign The TSF shall perform signature generation and verification in accordance with a specified cryptographic algorithm RSASSA_PKCS1v1_5, RSASSA_PSS and cryptographic key sizes 2048 bits; 3072 bits and 4096 bits that meet the following: PKCS#1v2.1 [RFC 3447].

FCS_COP.1/ECDSA Cryptographic operation (ECC signature generation/verification)

Hierarchical to: No other components.
Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/ECDSA The TSF shall perform signature generation and verification in accordance with a specified cryptographic algorithm ECDSA with curves TPM_ECC_NIST_P256, TPM_ECC_NIST_P384, *TPM_ECC_BN_P256* and cryptographic key sizes 256 and 384 bits that meet the following: FIPS PUB 186-4 [24].

FCS_COP.1/ECDAA Cryptographic operation (ECDAA commit)

Hierarchical to: No other components.

Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/ECDAA The TSF shall perform signature generation in accordance with a specified cryptographic algorithm ECDAA with curve *TPM_ECC_NIST_P256, TPM_ECC_BN_P256, TPM_ECC_NIST_P384* and cryptographic key sizes 256 *and 384* that meet the following: [FIPS 186-4] for curves *TPM_ECC_NIST_P256 and TPM_ECC_NIST_P384 and* [ISO/IEC 15946-5] for curve *TPM_ECC_BN_P256*.

FCS_COP.1/ECDEC Cryptographic operation (decryption)

Hierarchical to: No other components.

Dependencies: [FDP_ITC.1 Import of user data without security attributes, or FDP_ITC.2 Import of user data with security attributes, or FCS_CKM.1 Cryptographic key generation]
FCS_CKM.4 Cryptographic key destruction

FCS_COP.1.1/ECDEC The TSF shall perform decryption of ECC key in accordance with a specified cryptographic algorithm ECDH with curve *TPM_ECC_NIST_P256*, *TPM_ECC_NIST_P384 and TPM_ECC_BN_P256* and cryptographic key sizes 256 and 384 bits that meet the following: TPM library specification *[7], [8], [9] and* [SP 800-56A] *[30].*

FIA_UID.1 Timing of identification

Hierarchical to: No other components.

Dependencies: No dependencies.

FIA_UID.1.1 The TSF shall allow
(1) to execute indication _TPM_Hash_Start, _TPM_Hash_Data and _TPM_Hash_End,
(2) to execute commands that do not require authentication,
(3) to access objects where the entity owner has defined no authentication requirements (authValue, authPolicy),
(4) none
on behalf of the user to be performed before the user is identified.

FIA_UID.1.2 The TSF shall require each user to be successfully identified before allowing any other TSF-mediated actions on behalf of that user, e.g. self-test.

FPT_TST.1 TSF testing

Hierarchical to: No other components.

Dependencies: No dependencies.

FPT_TST.1.1 The TSF shall run a suite of self tests

- at the request of the authorised user “World”
  (1) the TPM2_SelfTest command and of selected algorithms using the TPM2_IncrementalSelfTest command,
- at the conditions

(1) Initialisation state after reset and before the reception of the first command,
(2) prior to execution of a command using a not self-tested function,

- *none*

to demonstrate the correct operation of sensitive parts of the TSF.

FPT_TST.1.2 The TSF shall provide authorised users with the capability to verify the integrity of *TSF data*.

FPT_TST.1.3 The TSF shall provide authorised users with the capability to verify the integrity of the TSF.

FPT_FLS.1/FS Failure with preservation of secure state (fail state)

Hierarchical to: No other components.
Dependencies: No dependencies.

FPT_FLS.1.1/FS The TSF shall preserve a secure state by entering the Fail state when the following types of failures occur:

(1) If during TPM Restart or TPM Resume, the TPM fails to restore the state saved at the last Shutdown(STATE), the TPM shall enter Failure Mode and return TPM_RC_FAILURE.
(2) failure detected by TPM2_ContextLoad when the decrypted value of *sequence* is compared to the stored value created by TPM2_ContextSave(),
(3) failure detected by self-test according to FPT_TST.1,
(4) *failure of execution flow control and hardware failure*

FPT_PHP.3 Resistance to physical attack

Hierarchical to: No other components.
Dependencies: No dependencies.

FPT_PHP.3.1 The TSF shall resist physical manipulation and physical probing to the TSF by responding automatically such that the SFRs are always enforced.

FDP_ACC.2/States Complete access control (operational states)

Hierarchical to: FDP_ACC.1 Subset access control
Dependencies: FDP_ACF.1 Security attribute based access control

FDP_ACC.2.1/States The TSF shall enforce the TPM State Control SFP on all subjects and objects and all operations among subjects and objects covered by the SFP.

FDP_ACC.2.2/States The TSF shall ensure that all operations between any subject controlled by the TSF and any object controlled by the TSF are covered by an access control SFP.

FDP_ACF.1/States Security attribute based access control (operational states)

Hierarchical to:	No other components.
Dependencies:	FDP_ACC.1 Subset access control FMT_MSA.3 Static attribute initialisation

FDP_ACF.1.1/States The TSF shall enforce the TPM State Control SFP to objects based on the following

Subjects as defined in Table 7 [2] :

(1) Platform firmware with the security attributes platformAuth, platformPolicy and physical presence if supported by the TOE,

(2) all other subjects; their security attributes are irrelevant for this SFP,

Objects as defined in Table 8 and Table 9[3]:

(1) Shutdown BLOB with the security attribute validation status,

(2) Firmware update data with security attributes signature of the TPM manufacturer and digest,

(3) all other objects; their security attributes are irrelevant for this SFP.

FDP_ACF.1.2/States The TSF shall enforce the following rules to determine if an operation among controlled subjects and controlled objects is allowed:

(1) The *Platform firmware* is authorised to change the TPM state to FUM if the authenticity of the first digest or the signature could be successfully verified.

(2) While in FUM state the platform firmware is authorised to import or activate firmware data only after successful verification of its integrity and authenticity (see FDP_UIT.1/States).

(3) The FUM state shall only be left when *the TOE is reset after successful loading of the firmware data*.

(4) In the Init state the subject “World” is authorised to execute the commands, TPM2_Startup and the sequence _TPM_Hash_Start, _TPM_Hash_Data, and _TPM_Hash_End.

(5) In the Init state every subject is authorised to process the Resume operation on the Shutdown BLOB with state transition to Operational.

(6) In the Init state every subject is authorised to process the Restart operation on the Shutdown BLOB with state transition to Operational.

(7) In the Init state, if no Shutdown BLOB was generated or if the Shutdown BLOB is invalid (see attribute “Validation status”) every subject is authorised to process the TPM2_Startup command. In case of the parameter TPM_SU_CLEAR the TPM shall change the state to Operational and initialise its internal operational variables to default initialisation values (Reset), otherwise the TPM shall return an error and stay in the same state.

(8) In the Operational state, nobody is authorised to execute the command TPM2_Startup. For all other subjects, objects and operations, the access control rules of the Access Control SFP shall apply (see FDP_ACF.1/AC).

(9) The Operational state shall change to Self-Test state if one of the commands TPM2_Selftest or TPM2_IncrementalSelfTest is executed or when a test of a dedicated functionality is required (see FPT_TST.1). In the Self-Test state, nobody is authorised to execute any other TPM command.

(10) The Self-Test state shall be left only after finishing the intended test of the dedicated functionality. In case of a successful test result the state shall change to Operational, otherwise to Fail.

(11) In the Fail state, every subject is authorised to execute the commands TPM2_GetTestResult and TPM2_GetCapability.

(12) In the Fail state the subject World is authorised to send a _TPM_Init indication with state change to Init.

(13) Any subject is authorised to prepare the TPM for a power cycle using the TPM2_Shutdown command and to create a shutdown BLOB by TPM2_Shutdown(TPM_SU_STATE).

[2] See Table 7 in Protection Profile [13]

[3] See Table 8 and 9 in Protection Profile [13]

FDP_ACF.1.3/States The TSF shall explicitly authorise access of subjects to objects based on the following additional rules:

(1) the TPM authorises to enter FUM state if the firmware update data major version is equal to the major version of the loaded firmware

(2) the TPM authorises to enter FUM state if the firmware update data minor version is bigger than or equal to the minor version of the loaded firmware

(3) the TOE authorises to enter FUM state if the upgrade counter is strictly lower than the limit upgrade counter

(4) the TOE authorises to enter FUM state if the internal failure counter is strictly lower than the limit failure counter

(5) the TOE resets the upgrade counter once a firmware with a strictly higher version is loaded successfully

FDP_ACF.1.4/States The TSF shall explicitly deny access of subjects to objects based on the following additional rules:

(1) Once the TPM receives a TPM2_SelfTest command and before completion of all tests, the TPM shall return TPM_RC_TESTING for any command that uses a command that requires a test.

FMT_MSA.1/States Management of security attributes (operational states)

Hierarchical to: No other components.

Dependencies: [FDP_ACC.1 Subset access control, or
FDP_IFC.1 Subset information flow control]
FMT_SMR.1 Security roles
FMT_SMF.1 Specification of Management Functions

FMT_MSA.1.1/States TSF shall enforce the TPM state control SFP to restrict the ability to modify the security attributes TPM state

(1) FUM to Platform firmware,

(2) other than FUM to any role.

FMT_MSA.3/States Static attribute initialisation (operational states)

Hierarchical to: No other components.

Dependencies: FMT_MSA.1 Management of security attributes
FMT_SMR.1 Security roles

FMT_MSA.3.1/States The TSF shall enforce the TPM state control SFP to provide restrictive default values for security attributes that are used to enforce the SFP.

FMT_MSA.3.2/States The TSF shall allow nobody to specify alternative initial values to override the default values when an object or information is created.

FDP_UIT.1/States Data exchange integrity (operational states)

Hierarchical to: No other components.

Dependencies: [FDP_ACC.1 Subset access control, or
FDP_IFC.1 Subset information flow control]
[FTP_ITC.1 Inter-TSF trusted channel, or
FTP_TRP.1 Trusted path]

FDP_UIT.1.1/States The TSF shall enforce the TPM state control SFP to receive firmware update data in a manner protected from *modification, deletion, insertion, replay* errors.

FDP_UIT.1.2/States The TSF shall be able to determine on receipt of firmware update data, whether *modification, deletion, insertion, replay* has occurred.

FDP_ACC.1/Hier Subset access control (object hierarchy)

Hierarchical to: No other components.
Dependencies: FDP_ACF.1 Security attribute based access control

FDP_ACC.1.1/Hier The TSF shall enforce the TPM Object Hierarchy SFP on

Subjects
(1) Platform firmware,
(2) Platform Owner,
(3) Privacy administrator,
(4) Lockout administrator,
(5) USER,
(6) World

Objects
(5) PPS,
(6) EPS,
(7) SPS,
(8) PPO,
(9) EK,
(10) SRK
(11) Null Seed,
(12) object in a TPM hierarchy

Operations
(1) TPM2_CreatePrimary,
(2) TPM2_CreateLoaded
(3) TPM2_HierarchyControl,
(4) TPM2_Clear,
(5) TPM2_ClearControl,
(6) TPM2_HierarchyChangeAuth,
(7) TPM2_SetPrimaryPolicy,
(8) TPM2_Load,
(9) TPM2_LoadExternal,
(10) TPM2_ReadPublic,
(11) Use
(12) TPM2_ChangeEPS
(13) TPM2_ChangePPS
(14) *TPM2_RestoreEK*

FDP_ACF.1/Hier Security attribute based access control (object hierarchy)

Hierarchical to: No other components.
Dependencies: FDP_ACC.1 Subset access control
FMT_MSA.3 Static attribute initialisation

FDP_ACF.1.1/Hier The TSF shall enforce the TPM Object Hierarchy SFP to objects based on the following:
Subjects:
(1) Platform firmware with security attribute authorisation state gained by authentication with platformAuth or platformPolicy,
(2) Platform Owner with security attribute authorisation state gained by authentication with ownerAuth or ownerPolicy,
(3) Privacy administrator with security attribute authorisation state gained by authentication with endorsementAuth or endorsementPolicy,
(4) Lockout administrator with security attribute authorisation state,
(5) USER with authentication state gained with authValue or authPolicy,
(6) World with no security attributes,

Objects:

(1) EPS,
(2) PPS,
(3) SPS,
(4) EK,
(5) PPO,
(6) SRK,
(7) Null Seed,
(8) object in a TPM hierarchy with security attributes: state of the hierarchy, fixedParent, fixedTpm

FDP_ACF.1.2/Hier The TSF shall enforce the following rules to determine if an operation among controlled subjects and controlled objects is allowed:

(1) The subject World is authorised to create an EPS whenever the TPM is powered on and no EPS is present.
(2) The subject World is authorised to create an PPS whenever the TPM is powered on and no PPS is present.
(3) The subject World is authorised to create an SPS whenever the TPM is powered on and no SPS is present.
(4) The subject World is authorised to create a Null Seed whenever the TPM is reset.
(5) The Platform firmware with platformAuth, platformPolicy or physical presence if supported by the TOE and the lockout administrator with lockoutAuth is authorised to change the SPS to a new value from the RNG (TPM2_Clear). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_Clear command.
(6) The Platform firmware is authorised to create a Platform Primary Object under PPS. The physical presence is not required if it is not if supported by the TOE or disabled for TPM2_CreatePrimary or TPM2_CreateLoaded command.
(7) The Platform Owner is authorised to create a primary object (SRK) under SPS.
(8) The privacy administrator is authorised to create a primary object (EK) under EPS.
(9) The subject World is authorised to create temporary objects for no hierarchy (using the Null Seed).
(10) The Platform firmware with platformAuth, platformPolicy or physical presence if supported by the TOE and the lockout administrator with lockoutAuth are authorised to remove all TPM context associated with a specific owner (TPM2_Clear). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_ClearControl command.
(11) The Platform firmware with platformAuth, platformPolicy or physical presence if supported by the TOE and the lockout administrator with lockoutAuth are authorised to disable and enable the execution of TPM2_Clear by the command TPM2_ClearControl. The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_ClearControl command.
(12) The Platform firmware with platformAuth, platformPolicy or physical presence if supported by the TOE, the Platform Owner, the privacy administrator and the lockout administrator are authorised to change the authorisation secret for a hierarchy or lockout (TPM2_HierarchyChangeAuth). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_HierarchyChangeAuth command.
(13) The Platform firmware with platformAuth, platformPolicy or physical presence, if supported by the TOE the Platform Owner and the privacy administrator are authorised to set the authorisation policy for the platform hierarchy (platformPolicy), the storage hierarchy (ownerPolicy) and the endorsement hierarchy (endorsementPolicy) using the command TPM2_SetPrimaryPolicy. The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_SetPrimaryPolicy command.
(14) The Platform firmware is authorized to replace the current EPS with a value from RNG, disable EKs loaded by the TPM Vendor and to set the endorsement hierarchy controls to their default values (TPM2_ChangeEPS).

(15) The Platform firmware is authorized to replace the current PPS with a value from RNG and to set the platformPolicy to the default value (TPM2_ChangePPS)

(16) The Platform firmware is authorized to replace the current EPS with a value from RNG, to restore the EKs loaded by the TPM vendor and to set the endorsement hierarchy controls to their default values (TPM2_RestoreEK). The EKs are restored from the EKs values loaded by the TPM vendor in phase 2 (manufacturing and delivery) defined for case 1 in the Protection Profile [13] . The restored values are used to generate the EKs when the command TPM2_CreatePrimary uses the default creation templates defined in the TOE user guidance

FDP_ACF.1.3/Hier The TSF shall explicitly authorise access of subjects to objects based on the following additional rules: none.

FDP_ACF.1.4/Hier The TSF shall explicitly deny access of subjects to objects based on the following additional rules:

(1) No subject is authorised to use any object of a hierarchy if the corresponding hierarchy is disabled (i.e phEnable for platform hierarchy is CLEAR, shEnable for Storage hierarchy is CLEAR, ehEnable for EPS hierarchy is CLEAR).

FMT_MSA.1/Hier Management of security attributes (object hierarchy)

Hierarchical to: No other components.
Dependencies: [FDP_ACC.1 Subset access control, or
FDP_IFC.1 Subset information flow control]
FMT_SMR.1 Security roles
FMT_SMF.1 Specification of Management Functions

FMT_MSA.1.1/Hier TSF shall enforce the TPM Object Hierarchy SFP to restrict the ability to modify the security attributes fixedTPM and fixedParent to nobody.

FMT_MSA.3/Hier Static attribute initialisation (object hierarchy)

Hierarchical to: No other components.
Dependencies: FMT_MSA.1 Management of security attributes
FMT_SMR.1 Security roles

FMT_MSA.3.1/Hier The TSF shall enforce the TPM Object Hierarchy SFP to provide restrictive default values for security attributes that are used to enforce the SFP.

FMT_MSA.3.2/Hier The TSF shall allow the creator of an object in a TPM hierarchy to specify alternative initial values to override the default values when an object or information is created.

FMT_MSA.4/Hier Security attribute value inheritance (hierarchy)

Hierarchical to: No other components.
Dependencies: [FDP_ACC.1 Subset access control, or
FDP_IFC.1 Subset information flow control]

FMT_MSA.4.1/Hier The TSF shall use the following rules to set the value of security attributes:

(1) The Platform firmware with platformAuth, platformPolicy or physical presence if supported by the TOE is authorised to enable and to disable the use of the platform hierarchy and its associated NV storage (TPM2_HierarchyControl changing phEnable or phEnableNV). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_HierarchyControl command.

(2) The Platform firmware with platformAuth, platformPolicy or physical presence if supported by the TOE and Platform Owner with ownerAuth or ownerPolicy are authorised to enable and to disable the use of a Storage hierarchy (TPM2_HierarchyControl changing shEnable). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_HierarchyControl command.

(3) The Platform firmware with platformAuth, platformPolicy or physical presence if supported by the TOE and privacy administrator with endorsementAuth or

endorsementPolicy are authorised to enable and to disable the use of a Endorsement hierarchy (TPM2_HierarchyControl changing ehEnable). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_HierarchyControl command.

(4) The only way to enable platform hierarchy is power-on of the TPM.

(5) The Platform firmware with platformAuth, platformPolicy, or physical presence if supported by the TOE is authorised to enable the use of the Endorsement hierarchy and the Storage hierarchy (TPM2_HierarchyControl). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_HierarchyControl command

FDP_ACF.1/AC Security attribute based access control (access control)

Hierarchical to: No other components.

Dependencies: FDP_ACC.1 Subset access control
FMT_MSA.3 Static attribute initialisation

FDP_ACF.1.1/AC The TSF shall enforce the Access Control SFP to objects based on the following Subjects:

(1) Platform firmware with security attribute authorisation state gained by authentication with platformAuth, platformPolicy or physical presence if supported by the TOE,

(2) Platform firmware with security attribute authorisation state gained by authentication with ownerAuth or ownerPolicy,

(3) Privacy administrator with security attribute authorisation state gained by authentication with endorsementAuth or endorsementPolicy,

(4) Lockout administrator with security attribute authorisation state,

(5) USER with authentication state gained with userAuth or authPolicy,

(6) DUP with authentication state gained with authPolicy,

(7) ADMIN with authentication state gained with userAuth or authPolicy,

(8) World with no security attributes,

Objects:

(1) User key with security attributes TPM_ALG_ID, TPMA_OBJECT,

(2) TPM objects,

(3) Clock with security attributes: resetCount, restartCount, safe-flag,

(4) Data with security attribute “externally provided”.

FDP_ACF.1.2/AC The TSF shall enforce the following rules to determine if an operation among controlled subjects and controlled objects is allowed:

(1) The Platform firmware platformAuth, platformPolicy or with physical presence if supported by the TOE and the Platform Owner are authorised to control the persistence of loadable objects in TPM memory (TPM2_EvictControl). The physical presence is not required if it is not supported by the TOE or disabled for TPM2_EvictControl command.

(2) The Platform firmware platformAuth, platformPolicy or with physical presence if supported by the TOE and the Platform Owner are authorised to advance the value and to adjust the rate of advance of the TPMs clock (TPM2_ClockSet, TPM2_ClockRateAdjust). The physical presence is not required if it is not supported by the TOE or disabled for the TPM2_ClockSet respective TPM2_ClockRateAdjust command.

(3) Any subject is authorised to get the current value of time, clock, resetCount and restartCount and safe (TPM2_ReadClock).

(4) A subject with the role USER endorsed by the Privacy administrator or the keyHandle identifier of a loaded key that can perform digital signatures is authorised to get the current value of time and clock (TPM2_GetTime)

(5) No subject is authorised to set the clock to a value less than the current value of clock using the TPM2_ClockSet command.

(6) No subject is authorised to set the clock to a value greater than its maximum value (0xFFFF000000000000) using the TPM2_ClockSet command.

(7) A subject with the role USER is authorised to generate digital signatures using the command TPM2_Sign for externally provided data (hash). The user authorisation shall be done based on the required authorisation of the key that will perform signing. The key attributes shall allow the signing operation for externally provided data.

(8) Any subject is authorised to verify digital signatures using the command TPM2_VerifySignature.

(9) Any subject is authorised to request data from the random number generator using the command TPM2_GetRandom.

(10) Any subject is authorised to add additional information to the state of the random number generator using the command TPM2_StirRandom.

(11) Any subject is authorised to perform RSA encryption using the command TPM2_RSA_Encrypt for externally provided data. The key attributes shall allow the encrypt operation for externally provided data.

(12) A subject with the role USER is authorised to perform RSA decryption using the command TPM2_RSA_Decrypt for externally provided data. The user authorisation shall be done based on the required authorisation of the key that will be used for decryption. The key attributes shall allow the decrypt operation for externally provided data.

(13) Any subject is authorised to generate ECC ephemeral key pairs using the command TPM2_ECDH_KeyGen.

(14) A subject with the role USER is authorised to recover a value that is used in ECC based key sharing protocols using the command TPM2_ECDH_ZGen. The user authorisation shall be done based on the required authorisation of the involved private key.

(15) Any subject is authorised to request the parameters of an identified ECC curve using the command TPM2_ECC_Parameters.

(16) The subject USER is authorised to start a HMAC sequence using the command TPM2_HMAC_Start.

(17) The subject World is authorised to start a hash or event sequence using the command TPM2_HashSequenceStart.

(18) The subject USER is authorised to add data to a hash, event or HMAC sequence using the command TPM2_SequenceUpdate.

(19) The subject USER is authorised to add the last part of data (if any) to a hash or HMAC sequence using the command TPM2_SequenceComplete.

(20) The subject USER is authorised to add the last part of data (if any) to an event sequence using the command TPM2_EventSequenceComplete.

(21) Any subject is authorised to perform hash operations on a data buffer using the command TPM2_Hash.

(22) A subject with the role USER is authorised to perform HMAC operations on a data buffer. The user authorisation shall be done based on the required authorisation of the involved symmetric key.

(23) A subject with the role USER is authorised to generate HMACs using the command TPM2_HMAC for externally provided data (hash). The user authorisation shall be done based on the required authorisation of the key that will perform the HMAC. The key attributes shall allow the signing operation for externally provided data.

FDP_ACF.1.3/AC The TSF shall explicitly authorise access of subjects to objects based on the following additional rules: *none*

FDP_ACF.1.4/AC The TSF shall explicitly deny access of subjects to objects based on the following additional rules: *none*.

7.2.1 *Extended component FCS_RNG.1*

The protection profile [13] defines the extended family Random Number Generation (FCS_RNG) of the class FCS (Cryptographic support) in order to describe the generation of random numbers for cryptographic purposes.

FCS_RNG.1 Random number generation

Hierarchical to: No other components.

Dependencies: No dependencies.

FCS_RNG.1.1 The TSF shall provide a *deterministic* random number generator that implements: NIST SP 800-90A *Hash_DRBG*. [31]

FCS_RNG.1.2 The TSF shall provide random numbers that meet: Statistical test suites cannot practically distinguish the random numbers from output sequences of an ideal RNG.

In order to comply with the requirements defined in the standard AIS 20 [38], a refinement of the SFR FCS_RNG is provided below:

FCS_RNG.1 Random number generation

Hierarchical to: No other components

Dependencies: No dependencies

FCS_RNG.1.1 The TSF shall provide a *deterministic* random number generator *AIS20 Class DRG.3 according* to [38] that implements:

(DRG.3.1) if initialized with a random seed using a PTRNG of class PTG.2 as random source, the internal state of the RNG shall have at least 100 bit of min-entropy and implements NIST SP 800-90A *Hash_DRBG* [31] and FIPS 180-4 [24].

(DRG.3.2) The RNG provides forward secrecy

(DRG.3.3) The RNG provides backward secrecy even if the current internal state is known

FCS_RNG.1.2 The TSF shall provide random numbers that meet

(DRG.3.4) *The RNG initialized with a random seed before the first use of the RNG after each product power up and reseeded after 2^{20} requests generates output for more than 2^{34} strings of bit length 128 that are mutually different with probability of $w>1-2^{-16}$*

(DRG.3.5) Statistical test suites cannot practically distinguish the random numbers from output sequences of an ideal RNG. The random numbers must pass FIPS 140-2 statistical test suite.

7.3 Security assurance requirements

The Security Assurance Requirements (SAR) for the TOE are the assurance components of Evaluation Assurance Level 4 (EAL4) as defined in CC part 3 and augmented with ALC_DVS.2 , ALC_FLR.1 and AVA_VAN.5.

The security assurance requirements defined in Table 4 are defined in section 8.2 of the PP [14] with the exception of the vulnerability assessment assurance component augmented to AVA_VAN.5 (AVA_VAN.4 in PP [14]) and development security assurance component augmented to ALC_DVS.2 (ALC_DVS.1 in PP [14]).

Table 4: Security assurance requirements for the TOE

Assurance Class	Assurance components
ADV: Development	ADV_ARC.1 Security architecture description
	ADV_FSP.4 Complete functional specification
	ADV_IMP.1 Implementation representation of the TSF
	ADV_TDS.3 Basic modular design
AGD: Guidance documents	AGD_OPE.1 Operational user guidance
	AGD_PRE.1 Preparative procedures
ALC: Life-cycle support	ALC_CMC.4 Production support, acceptance procedures and automation
	ALC_CMS.4 Problem tracking CM coverage
	ALC_DEL.1 Delivery procedures
	ALC_DVS.2 Sufficiency of security measures - **augmented**
	ALC_LCD.1 Developer defined life-cycle model
	ALC_FLR.1 Basic flow remediation - **augmented**
	ALC_TAT.1 Well-defined development tools
ASE: Security Target evaluation	ASE_CCL.1 Conformance claims
	ASE_ECD.1 Extended components definition
	ASE_INT.1 ST introduction
	ASE_OBJ.2 Security objectives
	ASE_REQ.2 Derived security requirements
	ASE_SPD.1 Security problem definition
	ASE_TSS.1 TOE summary specification
ATE: Tests	ATE_COV.2 Analysis of coverage
	ATE_DPT.1 Testing: security enforcing modules
	ATE_FUN.1 Functional testing
	ATE_IND.2 Independent testing - sample
AVA: Vulnerability assessment	AVA_VAN.5 Methodical vulnerability analysis - **augmented**

7.4 Security Requirements rationale

The security requirements rationale of the TOE are defined and described in the PP [14], sections 8.3 and 9.8 Security Requirements rationale.

7.4.1 Sufficiency of SFR

The SFRs FCS_CKM.1/PKRSA, FCS_CKM.1/PKECC and FCS_CKM.1/PKAES fulfil the same objectives as the SFR FCS_CKM.1/PK defined in the PP [14] Table 11.

The SFR FCS_COP.1/SHA3 fulfils the same objectives as the SFR FCS_COP.1/SHA defined in the PP [14] Table 11.

The SFR FCS_COP.1/HMAC/SHA3 fulfils the same objectives as the SFR FCS_COP.1/HMAC defined in the PP [14] Table 11.

7.4.2 Dependency rationale

The SFRs FCS_CKM.1/PKRSA, FCS_CKM.1/PKECC and FCS_CKM.1/PKAES fulfil the same dependency rationale as the SFR FCS_CKM.1/PK defined in the PP [14] Table 12.

The SFR FCS_COP.1/SHA3 fulfils the same dependency rationale as the SFR FCS_COP.1/SHA defined in the PP [14] Table 12.

The SFR FCS_COP.1/HMAC/SHA3 fulfil the same dependency rationale as the SFR FCS_COP.1/HMAC defined in the PP [14] Table 12.

7.5 Security Assurance rationale

The security assurance requirements rationale of the TOE are defined and described in the section 8.3.3 Assurance rationale.

8 TOE SUMMARY SPECIFICATION

The product overview is described in section 2.2.

In the following section, the security functionality and the assurance measures of the TOE are described.

8.1 TOE Security Features

This section contains the definition and description of the security features (SF) of the TOE. The TOE provides five security features (SF) to meet the security functional requirements. The security features are:

- SF_CRY: Cryptographic Support
- SF_I&A: Identification and Authentication
- SF_G&T General and Test
- SF_OBH Object Hierarchy
- SF_TOP TOE Operation

8.1.1 SF_CRY - Cryptographic Support

There are several functions within the TOE related to cryptographic support: generation of random numbers, generation of asymmetric key pairs, RSA and ECC digital signature (generation and verification), RSA, ECC and AES data encryption and decryption, key destruction, the generation of hash values and the generation and verification of MAC values.

The TOE supports the generation of cryptographic keys in accordance with the specified cryptographic key generation algorithm *RSA key generator* and *ECC key generator* and specified cryptographic key sizes RSA 2048, 3072 and 4096 bits that meet the following: [35] and optional [33] and ECC with key sizes of 256 and 384 bits that meet *[7], [8], [9]*,and optional [33].

RSA key generator:

- Endorsement Key generated with default template defined in [45] is securely written in the TOE during the manufacturing process
- Other keys are generated according to *[7], [8], [9] using the DRBG as random generator*

ECC key generator

- Endorsement Key generated with default template defined in [45] is securely written in the TOE during the manufacturing process
- Other keys are generated according to *[7], [8], [9] using the DRBG as random generator*

The covered security functional requirements are FCS_CKM.1/PKRSA, FCS_CKM.1/PKECC, FCS_CKM.1/RSA and FCS_CKM.1/ECC.

The TOE supports the generation of symmetric cryptographic keys in accordance with the specified cryptographic key generation algorithm *AES key generator* and specified cryptographic key sizes 128, 192 and 256 bits that meet *[7], [8], [9]* and optional [33].

The covered security functional requirements are FCS_CKM.1/PKAES and FCS_CKM.1/SYMM.

The TOE supports the destruction of cryptographic keys by erasure of volatile memory areas containing cryptographic keys in accordance with FIPS PUB 140-2 [22].

The covered security functional requirement is FCS_CKM.4.

The TOE performs the encryption and decryption in accordance with the specified cryptographic algorithm AES in the CFB, CTR, OFB, CBC, ECB modes and cryptographic key size of 128, 192 and 256 bits that meet [FIPS 197] and [SP 800-38A].

The covered security functional requirement is FCS_COP.1/AES. The TOE performs the hash value calculation in accordance with the specified cryptographic algorithm SHA-1, SHA-256 and SHA-384 that meets [FIPS 180-4] beside SHA3-256 and SHA3-384 that meet [FIPS 202] .

The covered security functional requirement is FCS_COP.1/SHA.

The TOE performs HMAC value calculation and verification in accordance with the specified cryptographic algorithm HMAC with SHA-1, SHA-256, SHA-384, SHA3-256 and SHA3-384 and cryptographic key sizes 160, 256 and 384 bits that meet [FIPS 198-1] and [FIPS 180-4]

The covered security functional requirements are FCS_COP.1/HMAC and FCS_COP.1/HMAC/SHA3.

The TOE performs asymmetric encryption and decryption in accordance with the specified cryptographic algorithm RSA without padding, RSAES-PKCS1-v1_5, RSAES-OAEP and cryptographic key sizes 2048, 3072 and 4096 bits that meet [RFC 3447].

The covered security functional requirement is FCS_COP.1/RSAED.

The TOE performs signature generation and signature verification in accordance with the specified cryptographic algorithm RSASA_PKCS1v1_5, RSASSA_PSS and cryptographic key sizes 2048, 3072 and 4096 bits that meet [RFC 3447].

The covered security functional requirement is FCS_COP.1/RSASign.

The TOE performs signature generation and signature verification in accordance with the specified cryptographic algorithm ECDSA with curves TPM_ECC_NIST_P256, TPM_ECC_NIST_P384 and TPM_ECC_BN_P256 and cryptographic key sizes 256 and 384 bits that meet TPM library specification [TPM2.0 Part1 r159] section C.4.

The covered security functional requirement is FCS_COP.1/ECDSA.

The TOE performs signature generation in accordance with the specified cryptographic algorithm ECDAA with curves TPM_ECC_NIST_P256, TPM_ECC_NIST_P384 and TPM_ECC_BN_P256 and cryptographic key sizes 256 and 384 bits that meet TPM library specification [TPM2.0 Part1 r159], section C4.2.

The covered security functional requirement is FCS_COP.1/ECDAA.

The TOE performs decryption of ECC key in accordance with the specified cryptographic algorithm ECDH with curves TPM_ECC_BN_P256, TPM_ECC_NIST_P256 and TPM_ECC_NIST_P384 and cryptographic key sizes 256 and 384 bits that meet TPM library specification *[7], [8], [9] and* [SP 800-56A] section 6.1.1.2.

The covered security functional requirement is FCS_COP.1/ECDEC.

The TOE provides a deterministic random number generator (DRBG) including a true random generator, which is used for the seeding of the DRBG, to provide the random numbers. The TOE provides random numbers that fulfils the requirements from the functional class DRG.3 of [AIS 20] and [SP 800-90Ar1]. The DRBG is based on a HASH_DRBG with SHA256.

The covered security functional requirement is FCS_RNG.1.

The SF_CRY Cryptographic Support covers the following security functional requirements:

- FCS_CKM.1/PKRSA,
- FCS_CKM.1/PKECC,
- FCS_CKM.1/PKAES,
- FCS_CKM.1/RSA,
- FCS_CKM.1/ECC,

- FCS_CKM.1/SYMM,
- FCS_CKM.4,
- FCS_COP.1/AES, FCS_COP.1/SHA,
- FCS_COP.1/HMAC,
- FCS_COP.1/HMAC/SHA3
- FCS_COP.1/RSAED,
- FCS_COP.1/RSASign,
- FCS_COP.1/ECDSA,
- FCS_COP.1/ECDAA,
- FCS_COP.1/ECDEC and
- FCS_RNG.1.

8.1.2 SF_I&A - Identification and Authentication

The TPM provides two mechanisms for the identification and authentication capability to authorize the use of a Protected Object and Protected Capability. Note that the TCG TPM Library specification refers to the identification and authentication process and access control as authorization. The first authentication mechanisms is the proof of knowledge of a shared secret (password or secret for HMAC) assigned to the entity as authValue. The second mechanism is the authentication of the user and verification of an intended state of the TPM and its environment encoded in authPolicy and assigned to the entity.

The TOE provides a mechanism to generate secrets that meet uniform distribution of random variable generating the value, and is able to enforce the use of TSF generated secrets for nonce values for authorization sessions unknown authValues

The covered security functional requirement is FIA_SOS.2.

The TOE use different rules to set the value of security attributes. The covered security functional requirement is FMT_MSA.4/AUTH.

The TOE provides the management functionality of the TSF data by user authorization. The covered security functional requirement is FMT_MTD.1/AUTH.

TOE detects when the maximal tries of unsuccessful authentication attempts occur for objects and NV Index where DA is active and blocks the authorizations for a defined time.

The covered security functional requirement is FIA_AFL.1/Recover.

The TOE detects when one unsuccessful authentication attempt occurs using lockoutAuth in the command TPM2_DictionaryAttackLockReset and blocks the TPM2_DictionaryAttackLockReset command for a defined time.

The covered security functional requirement is FIA_AFL.1/Lockout.

The TOE detects when a defined number of successful authentication events exceeds pinLimit for an NV index with the attribute TPM_NT_PIN_PASS and blocks further authorization events.

The covered security functional requirement is FIA_AFL.1/PINPASS.

The TOE detects when a defined number of unsuccessful authentication events exceeds pinLimit for an NV index with the attribute TPM_NT_PIN_FAIL and blocks further authorization events.

The covered security functional requirement is FIA_AFL.1/PINFAIL.

The TOE allows access to a defined number of commands and objects for the user to be performed before the user is authenticated/identified.

The covered security functional requirements are FIA_UID.1 and FIA_UAU.1.

The TOE provides different authentication mechanisms to support user authentication and authenticate any user's claimed identity according to the different rules. The TOE provides re- authentication of the user for multiple command processing.

The covered security functional requirements are FIA_UAU.5 and FIA_UAU.6.

The TOE associate security attributes with subjects acting on the behalf of that user. The TOE enforces different rules on the initial association of user security attributes with subjects acting on the behalf of users and enforces different rules governing changes to the user security attributes associated with subjects acting on the behalf of users.

The covered security functional requirement is FIA_USB.1.

The SF_I&A - Identification and Authentication covers the following security functional requirements:

- FIA_SOS.2,
- FIA_MSA.4/AUTH,
- FMT_MTD.1/AUTH,
- FIA_AFL.1/Recover,
- FIA_AFL.1/Lockout,
- FIA_AFL.1/PINPASS
- FIA_AFL.1/PINFAIL
- FIA_UID.1,
- FIA_UAU.1,
- FIA_UAU.5,
- FIA_UAU.6 and
- FIA_USB.1.

8.1.3 SF_G&T - General and Test

The TOE provides the roles: Platform firmware, Platform owner, Privacy Administrator, Lockout Administrator, User, Admin, DUP and World and associates users with roles. The roles are enforced within the TOE because there are specific commands and specific keys bond to different token.

The covered security functional requirement is FMT_SMR.1. The TOE performs different management functions.

The covered security functional requirement is FMT_SMF.1.

The TOE ensures that only secure values are accepted for security attributes. The covered security functional requirement is FMT_MSA.2.

The TOE provides reliable time stamps as number of milliseconds the TOE has been powered since initialization of the Clock value.

The covered security functional requirement is FPT_STM.1

The TOE ensures that any previous information content of a resource is made unavailable upon the deallocation of the resource from defined objects.

The covered security functional requirement is FDP_RIP.1.

The TOE supports a suite of self tests during startup and at the request of an authorized user world to demonstrate the correct operation of sensitive parts of the TSF and to verify the integrity of stored TSF executable code and parts of TSF data.

The covered security functional requirement is FPT_TST.1.

The TOE preserves a secure state by entering the Fail state when a failure during TPM Restart or Resume occurs, a failure is detected by TPM2_ContextLoad or the self test, of any crypto operations including RSA encryption, RSA decryption, AES encryption, AES decryption, SHA-1, RNG, RSA signature generation, HMAC generation or failure of any commands or internal operations and authorization occurs.

The covered security functional requirement is FPT_FLS.1/FS.

The TOE preserves a secure state by shutdown, when detecting a physical attack or an environmental condition which is out of spec value.

The covered security functional requirement is FPT_FLS.1/SD.

The TOE resists physical manipulation and physical probing to the TSF by responding automatically such that the SFRs are always enforced.

The TOE supports the functions for protection and detection of physical manipulation and probing:

- Protection by an active shield that commands an automatic reaction on die integrity violation detection.
- Intrinsic countermeasures for cryptographic algorithm against side channel attacks like timing attacks (TA), SPA and DPA.
- Detection of abnormal behavior of the following operational conditions:
  - High voltage supply
  - Glitches
  - Out of range temperature
- Detection of abnormal TOE behavior:
  - TRNG failure
  - Errors on memories and registers
  - CPU and MPU errors
  - Faults on crypto processors

The TOE implements a set of countermeasures that reduce the exploitability of physical probing.

The covered security functional requirements are FPT_PHP.3, FDP_ITT.1 and FPT_ITT.1.

The SF_G&T - General and Test covers the following security functional requirements:

- FMT_SMR.1,
- FMT_SMF.1,
- FMT_MSA.2,
- FPT_STM.1,
- FDP_RIP.1,
- FPT_TST.1,
- FPT_FLS.1/FS,
- FPT_FLS.1/SD and
- FPT_PHP.3

- FDP_ITT.1
- FPT_ITT.1

8.1.4 SF_OBH - Object Hierarchy

The TOE supports different states during his lifecycle as described in [TPM2.0 PP] section 8.1.4.1 -TPM Operational States in detail.

The TOE enforces the TPM State Control SFP on all subjects and objects and all operations among subjects and objects covered by the SFP. The TOE ensure that all operations between any subject controlled by the TSF and any object controlled by the TSF are covered by an access control SFP and enforces different access control rules on controlled subjects and objects.

The covered security functional requirements are FDP_ACC.2/States and FDP_ACF.1/States.

The TOE enforces the TPM state control SFP to restrict the ability to modify the security attributes TPM state and to provide restrictive default values for security attributes that are used to enforce the SFP. The TOE enforces the TPM state control SFP to receive firmware update data in a manner protected from errors and determines on receipt of firmware update data, whether error has occurred.

The covered security functional requirements are FMT_MSA.1/States, FMT_MSA.3/States and FDP_UIT.1/States.

The TOE supports three different hierarchies, the platform hierarchy, the storage hierarchy and the endorsement hierarchy. The root of each TPM hierarchy is defined by a primary seed which is a random value persistently stored in the TOE. A hierarchy may be disabled.

The TOE monitors user data stored in containers controlled by the TSF for data modifications and modification of hierarchy on all objects, based on the different attributes.

The covered security functional requirement is FDP_SDI.1.

The TOE enforces the TPM Object Hierarchy SFP on defined subjects, objects and operations and enforces different rules to determine if an operation among controlled subjects and controlled objects is allowed and deny access of subjects to objects based on different rules.

The covered security functional requirements are FDP_ACC.1/Hier and FDP_ACF.1/Hier.

The TOE enforces the TPM Object Hierarchy SFP to not allow the modification of the security attributes fixedTPM and fixedParent.

The covered security functional requirement is FMT_MSA.1/Hier.

The TOE enforces the TPM Object Hierarchy SFP to provide restrictive default values for security attributes that are used to enforce the SFP and allows the creator of an object in a TPM hierarchy to specify alternative initial values to override the default values when an object or information is created.

The covered security functional requirement is FMT_MSA.3/Hier.

The TOE enforces different rules to set the value of security attributes. The covered security functional requirement is FMT_MSA.4/Hier.

The TOE allows the import and export of data as an object of a hierarchy.

The TOE enforces the Data Export and Import SFP on subjects, objects and operations. The Data Export and Import SFP enforce different rules to determine if an operation between a controlled subject and controlled object is allowed.

The covered security functional requirements are FDP_ACC.1/ExIm and FDP_ACF.1/ExIm.

The TOE enforce the Data Export and Import SFP to restrict the ability to use the security attribute authorization data to every subject, to provide restrictive default values for security attributes that are used to enforce the SFP and to prevent to override the default values when an object or information is created.

The covered security functional requirements are FMT_MSA.1/ExIm and FMT_MSA.3/ExIm

The TOE enforces the Data Export and Import SFP when exporting user data, controlled under the SFP(s), outside of the TOE and to export the user data with the user data's associated security attributes. The TOE ensure that the security attributes, when exported outside the TOE, are unambiguously associated with the exported user data and different rules are enforced when user data is exported from the TOE.

The covered security functional requirement is FDP_ETC.2/ExIm.

The TOE enforces the Data Export and Import SFP when importing user data, controlled under the SFP(s), outside of the TOE. The correct interpretation, association and use of the security attributes associated with the imported user data are ensured and different rules are enforced when user data is imported from outside the TOE.

The covered security functional requirement is FDP_ITC.2/ExIm.

The TOE enforces the Data Export and Import SFP to transmit user data in a manner protected from unauthorised disclosure and to transmit and receive user data in a manner protected from modification errors. The TOE is able to determine on receipt of user data, whether modification has occurred.

The covered security functional requirements are FDP_UCT.1/ExIm and FDP_UIT.1/ExIm.

The TOE enforces the Measurement and Reporting SFP on subjects, objects and operations. The Measurement and Reporting SFP enforce different rules to determine if an operation among controlled subjects and controlled objects is allowed.

The covered security functional requirements are FDP_ACC.1/M&R and FDP_ACF.1/M&R.

The TOE enforces the Measurement and Reporting SFP to restrict the ability to modify the security attributes PCR attributes, PCR extension algorithm and used hash algorithm to the subject Platform firmware, to provide restrictive default values for security attributes that are used to enforce the SFP, and to prevent to override the default values when an object or information is created.

The covered security functional requirements are FMT_MSA.1/M&R and FMT_MSA.3/M&R.

The TOE is able to generate evidence of origin for transmitted attestation structure and object creation tickets at the request of the originator and provide a capability to verify the evidence of origin of information to recipient given as soon as the recipient can verify the signature and has confidence to the key that is used to sign.

The covered security functional requirement is FCO_NRO.1/M&R.

The SF_OBH - Object Hierarchy covers the following security functional requirements:

- FDP_ACC.2/States,
- FDP_ACF.1/States,
- FMT_MSA.1/States,
- FMT_MSA.3/States,
- FDP_UIT.1/States,
- FDP_SDI.1,
- FDP_ACC.1/Hier,
- FDP_ACF.1/Hier,
- FMT_MSA.1/Hier,
- FMT_MSA.3/Hier,
- FMT_MSA.4/Hier,
- FDP_ACC.1/ExIm,
- FDP_ACF.1/ExIm,

- FMT_MSA.1/ExIm,
- FMT_MSA.3/ExIm,
- FDP_ETC.2/ExIm,
- FDP_ITC.2/ExIm,
- FDP_UCT.1/ExIm,
- FDP_UIT.1/ExIm,
- FDP_ACC.1/M&R,
- FDP_ACF.1/M&R,
- FMT_MSA.1/M&R,
- FMT_MSA.3/M&R and
- FCO_NRO.1/M&R

8.1.5 SF_TOP - TOE Operation

The TOE enforces the Access Control SFP on different subjects, objects and operations and enforces different rules to determine if an operation among controlled subjects and controlled objects is allowed. The TOE explicitly authorize access of subjects to objects based on different additional rules and explicitly deny access of subjects to objects based on the different additional rules.

The covered security functional requirements are FDP_ACC.1/AC and FDP_ACF.1/AC

The TOE enforces the Access Control SFP to restrict the ability to query and modify different security attributes to specific subjects, to provide restrictive default values for security attributes that are used to enforce the SFP and to specify alternative initial values to override the default values when an object or information is created.

The covered security functional requirements are FMT_MSA.1/AC and FMT_MSA.3/AC.

The TOE enforces the Access Control SFP to transmit user data in a manner protected from unauthorised disclosure. The TOE provides a communication channel between itself and another trusted IT product that is logically distinct from other communication channels and provides assured identification of its end points and protection of the channel data from modification or disclosure. The TOE initiates communication via the trusted channel and permits another trusted IT product to initiate communication via the trusted channel.

The covered security functional requirements are FDP_UCT.1/AC and FTP_ITC.1/AC.

The TSF shall restrict the ability to disable and enable the functions TPM2_Clear to the subjects Platform firmware and Lockout administrator.

The covered security functional requirement is FMT_MOF.1/AC.

The TSF shall enforce the NVM SFP on different subjects, objects and operations and enforces different rules to determine if an operation among controlled subjects and controlled objects is allowed.

The covered security functional requirements are FDP_ACC.1/NVM and FDP_ACF.1/NVM.

The TOE enforces the NVM SFP to restrict the ability to query and modify the security attribute NV index attributes to the authorized role of the subject that executes the NVM related command and to provide restrictive default values when an object or information is created. The TOE prohibits to override the default values with alternative initial values when an object or information is created. The TOE enforces different rules to set the value of security attributes and restrict the ability to modify the authorization secret (authValue) for a NV index to the subject ADMIN.

The covered security functional requirements are FMT_MSA.1/NVM, FMT_MSA.3/NVM, FMT_MSA.4/NVM and FMT_MTD.1/NVM.

The TOE enforces the NVM SFP when importing user data, controlled under the SFP, and ignores any security attributes associated with the user data when imported from outside the TOE. Additionally the TOE enforces different rules when importing user data controlled under the SFP from outside the TOE. The TOE enforces the NVM SFP when exporting user data, controlled under the SFP(s), outside of the TOE.

The covered security functional requirements are FDP_ITC.1/NVM and FDP_ETC.1/NVM.

The TOE enforces the Credential SFP on different subjects, objects and operations and enforces different rules to determine if an operation among controlled subjects and controlled objects is allowed.

The covered security functional requirements are FDP_ACC.1/Cre and FDP_ACF.1/Cre.

The TOE enforces the Credential SFP to provide restrictive default values for security attributes that are used to enforce the SFP and prevents to override the default values when an object or information is created. The TOE enforces the Credential SFP to restrict the ability to use the security attributes HMAC in the credential BLOB to the subject USER.

The covered security functional requirements are FMT_MSA.1/Cre and FMT_MSA.3/Cre.

The TOE generates evidence of origin for transmitted TPM objects at the request of the originator and relates the information whether the object is resident in an authentic TPM of the originator of the information, and the name and the public area of the TPM object of the information to which the evidence applies. The TOE provides a capability to verify the evidence of origin of information to the initiator given based on a credential BLOB that was generated by the credential provider.

The covered security functional requirement is FCO_NRO.1/Cre

The SF_TOE - TOE Operation‖ covers the following security functional requirements:

- FDP_ACC.1/AC,
- FDP_ACF.1/AC,
- FMT_MSA.1/AC,
- FMT_MSA.3/AC,
- FDP_UCT.1/AC,
- FTP_ITC.1/AC,
- FMT_MOF.1/AC,
- FDP_ACC.1/NVM,
- FDP_ACF.1/NVM,
- FMT_MSA.1/NVM,
- FMT_MSA.3/NVM,
- FMT_MSA.4/NVM,
- FMT_MTD.1/NVM,
- FDP_ITC.1/NVM,
- FDP_ETC.1/NVM,
- FDP_ACC.1/Cre,
- FDP_ACF.1/Cre,
- FMT_MSA.1/Cre,
- FMT_MSA.3/Cre and
- FCO_NRO.1/Cre

8.1.6 *Assignment of Security Functional Requirements*

Table 5: TOE Security functions and Security Functional Requirements

Security Functional Requirement	SF_ CRY	SF_ I&A	SF_ G&T	SF_ OBH	SF_ TOP
FMT_SMR.1			X		
FMT_SMF.1			X		
FMT_MSA.2			X		
FPT_STM.1			X		
FDP_RIP.1			X		
FCS_RNG.1	X				
FCS_CKM.1/PKRSA	X				
FCS_CKM.1/PKECC	X				
FCS_CKM.1/PKAES	X				
FCS_CKM.1/RSA	X				
FCS_CKM.1/ECC	X				
FCS_CKM.1/SYMM	X				
FCS_CKM.4	X				
FCS_COP.1/AES	X				
FCS_COP.1/SHA	X				
FCS_COP.1/SHA3	X				
FCS_COP.1/HMAC	X				
FCS_COP.1/HMAC/SHA3	X				
FCS_COP.1/RSAED	X				
FCS_COP.1/RSASign	X				
FCS_COP.1/ECDSA	X				
FCS_COP.1/ECDAA	X				
FCS_COP.1/ECDEC	X				
FIA_SOS.2		X			
FMT_MSA.4/AUTH		X			
FMT_MTD.1/AUTH		X			
FIA_AFL.1/Recover		X			
FIA_AFL.1/Lockout		X			
FIA_AFL.1/PINPASS		X			
FIA_AFL.1/PINFAIL		X			
FIA_UID.1		X			
FIA_UAU.1		X			

FIA_UAU.5		X			
FIA_UAU.6		X			
FIA_USB.1		X			
FPT_TST.1			X		
FPT_FLS.1/FS			X		
FPT_FLS.1/SD			X		
FPT_PHP.3			X		
FDP_ITT.1			X		
FPT_ITT.1			X		
FDP_ACC.2/States				X	
FDP_ACF.1/States				X	
FMT_MSA.1/States				X	
FMT_MSA.3/States				X	
FDP_UIT.1/States				X	
FDP_SDI.1				X	
FDP_ACC.1/Hier				X	
FDP_ACF.1/Hier				X	
FMT_MSA.1/Hier				X	
FMT_MSA.3/Hier				X	
FMT_MSA.4/Hier				X	
FDP_ACC.1/ExIm				X	
FDP_ACF.1/ExIm				X	
FMT_MSA.1/ExIm				X	
FMT_MSA.3/ExIm				X	
FDP_ETC.2/ExIm				X	
FDP_ITC.2/ExIm				X	
FDP_UCT.1/ExIm				X	
FDP_UIT.1/ExIm				X	
FDP_ACC.1/M&R				X	
FDP_ACF.1/M&R				X	
FMT_MSA.1/M&R				X	
FMT_MSA.3/M&R				X	
FCO_NRO.1/M&R				X	
FDP_ACC.1/AC					X
FDP_ACF.1/AC					X
FMT_MSA.1/AC					X

FMT_MSA.3/AC					X
FDP_UCT.1/AC					X
FTP_ITC.1/AC					X
FMT_MOF.1/AC					X
FDP_ACC.1/NVM					X
FDP_ACF.1/NVM					X
FMT_MSA.1/NVM					X
FMT_MSA.3/NVM					X
FMT_MSA.4/NVM					X
FMT_MTD.1/NVM					X
FDP_ITC.1/NVM					X
FDP_ETC.1/NVM					X
FDP_ACC.1/Cre					X
FDP_ACF.1/Cre					X
FMT_MSA.1/Cre					X
FMT_MSA.3/Cre					X
FCO_NRO.1/Cre					X

8.2 Statement of compatibility

This section details the statement of compatibility between this Composite Security Target and the Platform Security Target of the chip ST33K1M5A and ST33K1M5M [48].

This statement is compliant to the requirements of [JIL CPE].

8.2.1 Compatibility of Security Objectives

There is no conflict between the security objectives of this Composite Security Target with the Platform Security Target [48].

Table 6: Platform Security Objectives Vs Composite TOE Security Objectives

Platform Security Objectives	Composite TOE Security Objectives
BSI.O.Leak-Inherent	O.Tamper_Resistance
BSI.O.Phys-Probing	O.Tamper_Resistance
BSI.O.Malfunction	O.Fail_Secure
BSI.O.Phys-Manipulation	O.General_Integ_Checks O.Fail_Secure
BSI.O.Leak-Forced	O.Tamper_Resistance
BSI.O.Abuse-Func	O.Tamper_Resistance
BSI.O.Identification	O.General_Integ_Checks
BSI.O.RND	O.Crypto_Key_Man
BSI.O.Authentication	Irrelevant SO
BSI.O.Cap-Avail-Loader	Irrelevant SO
BSI.O.Ctrl-Auth-Loader	Irrelevant SO
JIL.O.Prot-TSF-Confidentialityy	O.Crypto_Key_Man
JIL.O.Secure-Load-Acode	Irrelevant SO
JIL.O.Secure-AC-Activation	Irrelevant SO
JIL.O.TOE-Identification	O.General_Integ_Checks
O.Secure-Load-AMemImage	Irrelevant SO
O.MemImage-Identification	Irrelevant SO
AUG1.O.Add-Functions	O.Crypto_Key_Man
	O.Crypto_Key_Man
AUG4.O.Mem-Access	O.No_Residual_Info
O.Firewall	O.Fail_Secure

The composite TOE security objectives not listed in the table above are fulfilled by the TPM embedded software without dependency on Platform components.

8.2.2 *Compatibility of Security Objectives for the Environment*

There is no conflict between the security objectives for the Environment of this Composite Security Target and the Platform Security Target.

Table 7: Platform Security Objectives for the environment Vs Composite TOE Security Objectives for the environment

Platform Security Objectives for the Environment	Composite TOE Security Objectives for the Environment
BSI.OE.Resp-Appl	OE.Configuration
BSI.OE.Process-Sec-IC	OE.Configuration
BSI.OE.Lim-Block-Loader	OE.Configuration
BSI.OE.Loader-Usage	OE.Configuration
BSI.OE.TOE-Auth	OE.Configuration
OE.Composite-TOE-Id	OE.Configuration
OE.TOE-Id	OE.Configuration
OE.Enable-Disable-Secure-Diag	Not relevant Secure Diagnostic disabled
OE.Secure-Diag-Usage	

The composite TOE security objectives for the environment not listed in the table above are fulfilled by the TPM embedded software without dependency on Platform components.

8.2.3 *Compatibility of Security Functional Requirements*

There is no conflict between the Security Functional Requirements of this Composite Security Target with the Platform Security Target.

The Platform SFRs are classified based on the relevance for the composite TOE SFRs in three categories

- RP_SFR-SERV: Relevant Platform-SFRs being used by the Composite-ST to implement a security service with associated TSFI
- IP_SFR: irrelevant Platform SFR not used by the Composite ST
- RP_SFR-MECH: Relevant Platform-SFRs being used by the Composite-ST because of its security properties providing protection against attacks to the TOE as a whole and are addressed in ADV_ARC. These required security properties are a result of the security mechanisms and services that are implemented in the Platform TOE.

Table 8: Platform Security Functional Requirements Vs Composite TOE Security Functional Requirements

Platform SFRs	Relevance	Composite SFRs
FRU_FLT.2	RP_SFR-MECH	FPT_PHP.3, FPT_FLS.1/FS, FPT_FLS.1/SD
FPT_FLS.1	RP_SFR-MECH	FPT_FLS.1/FS, FPT_FLS.1/SD
FMT_LIM.1 / Test	IP_SFR	Internal test features of the IC platform are not accessible by the

		Composite TOE
FMT_LIM.2 / Test	IP_SFR	Internal test features of the IC platform are not accessible by the Composite TOE
FAU_SAS.1	IP_SFR	Test process before TOE delivery is not used by the composite SFRs
FDP_SDC.1	RP_SFR-MECH	FPT_PHP.3, FPT_FLS.1/FS, FPT_FLS.1/SD
FDP_SDI.2	RP_SFR-MECH	FPT_TST.1, FPT_FLS.1/FS, FPT_FLS.1/SD
FPT_PHP.3	RP_SFR-MECH	FPT_PHP.3
FDP_ITT.1	RP_SFR-MECH	FDP_ITT.1
FPT_ITT.1	RP_SFR-MECH	FPT_ITT.1
FDP_IFC.1	RP_SFR-MECH	FDP_ITT.1, FPT_ITT.1
FCS_RNG.1 /PTG.2	RP_SFR-SERV	FCS_RNG.1 (PTG.2 is used as input for DRG.3) FIA_SOS.2
	RP_SFR-MECH	FPT_ITT.1
	RP_SFR-MECH	FPT_ITT.1
	RP_SFR-MECH	FPT_ITT.1
	RP_SFR-MECH	FPT_ITT.1
	RP_SFR-MECH	FPT_ITT.1
FCS_COP.1	IP_SFR	
	RP_SFR-SERV	FCS_COP.1/AES
	IP_SFR	
	IP_SFR	
	RP_SFR-SERV	FCS_CKM.1/PK/RSA FCS_CKM.1/RSA FCS_COP.1/RSAED FCS_COP.1/RSASign
	RP_SFR-SERV	FCS_CKM.1/ECC FCS_COP.1/ECDSA FCS_COP.1/ECDAA FCS_COP.1/ECDEC
	IP_SFR	
	IP_SFR	

	RP_SFR-SERV	FCS_COP.1/SHA FCS_COP.1/HMAC/SHA FCS_CKM.1/PK/ECC FCS_CKM.1/PK/SYMM FCS_CKM.1/ECC FCS_CKM.1/SYMM
	RP_SFR-SERV	FCS_COP.1/SHA3 FCS_COP.1/HMAC/SHA3
	IP_SFR	
	RP_SFR-SERV	
	RP_SFR-SERV	FCS_RNG.1 (FCS_COP.1 /DRBG used for FCS_RNG.1/DRG.3) FCS_CKM.1/PK/RSA FCS_CKM.1/PK/ECC FCS_CKM.1/PK/SYMM FCS_CKM.1/RSA FCS_CKM.1/ECC FCS_CKM.1/SYMM
	RP_SFR-SERV	FCS_CKM.1./PK/RSA
	IP_SFR	
FDP_ACC.2 /Memories		FPT_FLS.1/FS, FPT_FLS.1/SD, FCS_CKM.4
FDP_ACF.1 /Memories		FPT_FLS.1/FS, FPT_FLS.1/SD, FCS_CKM.4
FMT_MSA.3 /Memories		FPT_FLS.1/FS, FPT_FLS.1/SD, FCS_CKM.4
FMT_MSA.1 /Memories		FPT_FLS.1/FS, FPT_FLS.1/SD, FCS_CKM.4
FMT_SMF.1 /Memories		FPT_FLS.1/FS, FPT_FLS.1/SD, FCS_CKM.4
FIA_API.1	IP_SFR	This platform SFRs are not relevant for the composite TOE since it only applies to TOE products coming with Flash Loader for software or data download by the user. In case of this composite TOE Flash Loader is permanently deactivated and the user software or data download is completed.
FMT_LIM.1 /Loader	RP_SFR-SERV	FPT_FLS.1/FS, FPT_FLS.1/SD
FMT_LIM.2	RP_SFR-SERV	FPT_FLS.1/FS,

/Loader		FPT_FLS.1/SD
FTP_ITC.1 /Loader	IP_SFR	This platform SFRs are not relevant for the composite TOE since it only applies to TOE products coming with Flash Loader for software or data download by the user. In case of this composite TOE Flash Loader is permanently deactivated and the user software or data download is completed.
FDP_UCT.1 /Loader	IP_SFR	
FDP_UIT.1 /Loader	IP_SFR	
FDP_ACC.1 /Loader	IP_SFR	
FDP_ACF.1 /Loader	IP_SFR	
FMT_MSA.3 /Loader	IP_SFR	
FMT_MSA.1 /Loader	IP_SFR	
FMT_SMR.1 /Loader	IP_SFR	
FIA_UID.1 /Loader	IP_SFR	
FIA_UAU.1 /Loader	IP_SFR	
FMT_SMF.1 /Loader	IP_SFR	
FPT_FLS.1 /Loader	IP_SFR	
FAU_SAR.1 /Loader	IP_SFR	
FAU_SAS.1 /Loader	IP_SFR	
FTP_ITC.1 / Sdiag	IP_SFR	This platform SFR is not relevant since the secure diagnostic is disabled irreversible for the composite TOE .
FAU_SAR.1 /Sdiag	IP_SFR	
FMT_LIM.1 / Sdiag	IP_SFR	
FMT_LIM.2 / Sdiag	IP_SFR	

8.2.4 *Compatibility of Security Assurance Requirements*

The Composite-ST requires EAL4 augmented by ALC_FLR.1, ALC_DVS.2 and AVA_VAN.5.

The CryptoLib-ST requires EAL5 augmented by ALC_FLR.1, ALC_DVS.2 and AVA_VAN.5.

The Platform-ST requires EAL6 augmented by ALC_FLR.1.

The table below listing the security assurance requirements of the composite TOE shows they are a subset of the Platform TOE and CryptoLib TOE assurance requirements.

Therefore, there is no conflict for the composite TOE

Table 9: Platform and CryptoLib Security Assurance Requirements Vs Composite TOE Security Assurance Requirements

Assurance components	Composite ST	CryptoLib	Platform
ADV_ARC	1	1	1
ADV_FSP	4	5	5
ADV_IMP	1	1	2
ADV_TDS	3	4	5
AGD_OPE	1	1	1
AGD_PRE	1	1	1
ALC_CMC	4	4	5
ALC_CMS	4	5	5
ALC_DEL	1	1	1
ALC_DVS	2	2	2
ALC_LCD	1	1	1
ALC_FLR	1	1	1
ALC_TAT	1	2	3
ASE_CCL	1	1	1
ASE_ECD	1	1	1
ASE_INT	1	1	1
ASE_OBJ	2	2	2
ASE_REQ	2	2	2
ASE_SPD	1	1	1
ASE_TSS	1	1	1
ATE_COV	2	2	3
ATE_DPT	1	3	3
ATE_FUN	1	1	2
ATE_IND	2	2	2
AVA_VAN	5	5	5

9 ACRONYMS

For the purpose of this document, the acronyms given in CC Parts 2 and 3 and the following apply.

Acronym	Description
AFL	Application Flash Loader
AuthData	Authentication Data or Authorisation Data, depending on the context
CA	Certificate Authority
CFB	Cipher Feedback mode
CML	Code Memory Loader
CRTM	Core Root of Trust for Measurement
CTR	Counter-mode encryption
DA	Dictionary Attack
DAA	Direct Autonomous Attestation
DRBG	Deterministic Random Bit Generator
EAL	evaluated assurance level
ECB	Electric Cookbook
ECC	Elliptic Curve Cryptography
ECDAA	ECC-based Direct Anonymous Attestation
ECDH	Elliptic Curve Diffie-Hellman
EK	Endorsement Key
EPS	Endorsement Primary Seed
ES	Embedded Software
FIPS	Federal Information Processing Standard
FU	Field Upgrade
FUM	Field Upgrade mode
HMAC	Hash Message Authentication Code
HW	Hardware Interface
I/O	Input/Output
IV	Initialisation Vector
KDF	key derivation function
MMIO	Memory Mapped I/O
MPU	Memory Protecting Unit
NIST	National Institute of Standards and Technology
NV	Non-volatile
NVM	Non-Volatile Memory
OAEP	Optimal Asymmetric Encryption Padding
PCR	platform configuration register(s)
PK	Primary Key
PP	Physical Presence, Protection Profile
PPO	Platform Primary Object
PPS	Platform Primary Seed
RNG	Random Number Generator
RSA	Algorithm for public-key cryptography. The letters R, S, and A represent the initials of the first public describers of the algorithm Rivest, Shamir and Adleman.
RTM	Root of Trust for Measurement
RTR	Root of Trust for Reporting

Acronym	Description
RTS	Root of Trust for Storage
SHA	Secure Hash Algorithm
SPS	Storage Primary Seed
SRK	Storage Root Key
TCB	Trusted Computing Base
TCG	Trusted Computing Group
TOE	Target of Evaluation
TPM	Trusted Platform Module
UTC	Universal Time Clock

Appendix A REFERENCES

The following materials are to be used in conjunction with or are referenced by this document.

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