



## **Certification Report**

# NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1

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#### **Foreword**

The Netherlands Scheme for Certification in the Area of IT Security (NSCIB) provides a third-party evaluation and certification service for determining the trustworthiness of Information Technology (IT) security products. Under this NSCIB, TrustCB B.V. has the task of issuing certificates for IT security products, as well as for protection profiles and sites.

Part of the procedure is the technical examination (evaluation) of the product, protection profile or site according to the Common Criteria assessment guidelines published by the NSCIB. Evaluations are performed by an IT Security Evaluation Facility (ITSEF) under the oversight of the NSCIB Certification Body, which is operated by TrustCB B.V. in cooperation with the Ministry of the Interior and Kingdom Relations.

An ITSEF in the Netherlands is a commercial facility that has been licensed by TrustCB B.V. to perform Common Criteria evaluations; a significant requirement for such a licence is accreditation to the requirements of ISO Standard 17025 "General requirements for the accreditation of calibration and testing laboratories".

By awarding a Common Criteria certificate, TrustCB B.V. asserts that the product or site complies with the security requirements specified in the associated (site) security target, or that the protection profile (PP) complies with the requirements for PP evaluation specified in the Common Criteria for Information Security Evaluation. A (site) security target is a requirements specification document that defines the scope of the evaluation activities.

The consumer should review the (site) security target or protection profile, in addition to this certification report, to gain an understanding of any assumptions made during the evaluation, the IT product's intended environment, its security requirements, and the level of confidence (i.e., the evaluation assurance level) that the product or site satisfies the security requirements stated in the (site) security target.

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## **Recognition of the Certificate**

Presence of the Common Criteria Recognition Arrangement (CCRA) and the SOG-IS logos on the certificate indicates that this certificate is issued in accordance with the provisions of the CCRA and the SOG-IS Mutual Recognition Agreement (SOG-IS MRA) and will be recognised by the participating nations.

## International recognition

The CCRA was signed by the Netherlands in May 2000 and provides mutual recognition of certificates based on the Common Criteria (CC). Since September 2014 the CCRA has been updated to provide mutual recognition of certificates based on cPPs (exact use) or STs with evaluation assurance components up to and including EAL2+ALC\_FLR.

For details of the current list of signatory nations and approved certification schemes, see <a href="http://www.commoncriteriaportal.org">http://www.commoncriteriaportal.org</a>.

#### **European recognition**

The SOG-IS MRA Version 3, effective since April 2010, provides mutual recognition in Europe of Common Criteria and ITSEC certificates at a basic evaluation level for all products. A higher recognition level for evaluation levels beyond EAL4 (respectively E3-basic) is provided for products related to specific technical domains. This agreement was signed initially by Finland, France, Germany, The Netherlands, Norway, Spain, Sweden and the United Kingdom. Italy joined the SOG-IS MRA in December 2010.

For details of the current list of signatory nations, approved certification schemes and the list of technical domains for which the higher recognition applies, see <a href="https://www.sogis.eu">https://www.sogis.eu</a>.



## 1 Executive Summary

This Certification Report states the outcome of the Common Criteria security evaluation of the NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1. The developer of the NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1 is NXP Semiconductors Germany GmbH located in Hamburg, Germany and they also act as the sponsor of the evaluation and certification

A Certification Report is intended to assist prospective consumers when judging the suitability of the IT security properties of the product for their particular requirements.

The TOE is a composite platform containing the Java Card OS embedded on the SN330 Secure Element with IC Dedicated Software. The usage of the TOE is focused on security critical applications in small form factors. One main usage scenario is the use in mobile phones, which can use the TOE to enable mobile payment or mobile ticketing with the phone based on the security of the TOE.

The TOE has been evaluated by TÜV Informationstechnik GmbH located in Essen, Germany. The evaluation was completed on 25-08-2025 with the approval of the ETR. The certification procedure has been conducted in accordance with the provisions of the Netherlands Scheme for Certification in the Area of IT Security [NSCIB].

The scope of the evaluation is defined by the security target [ST], which identifies assumptions made during the evaluation, the intended environment for the NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1, the security requirements, and the level of confidence (evaluation assurance level) at which the product is intended to satisfy the security requirements. Consumers of the NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1 are advised to verify that their own environment is consistent with the security target, and to give due consideration to the comments, observations and recommendations in this certification report.

The results documented in the evaluation technical report [ETR] <sup>1</sup> for this product provide sufficient evidence that the TOE meets the EAL5 augmented (EAL5+) assurance requirements for the evaluated security functionality. This assurance level is augmented with AVA\_VAN.5 (Advanced methodical vulnerability analysis), ALC\_DVS.2 (Sufficiency of security measures), ALC\_FLR.2 (Flaw reporting Procedures) and ASE\_TSS.2 (TOE summary specification with architectural design summary).

The evaluation was conducted using the Common Methodology for Information Technology Security Evaluation, CEM:2022 [CEM] for conformance to the Common Criteria for Information Technology Security Evaluation, CC:2022 [CC].

TrustCB B.V., as the NSCIB Certification Body, declares that the evaluation meets all the conditions for international recognition of Common Criteria Certificates and that the product will be listed on the NSCIB Certified Products list. Note that the certification results apply only to the specific version of the product as evaluated.

The Evaluation Technical Report contains information proprietary to the developer and/or the evaluator, and is not available for public review.



#### 2 Certification Results

## 2.1 Identification of Target of Evaluation

The Target of Evaluation (TOE) for this evaluation is the NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1 from NXP Semiconductors Germany GmbH located in Hamburg, Germany.

The TOE is comprised of the following main components:

Delivery item type	Identifier	Version
Hardware	NXP SN330 Secure Element	SN330_SE A0.1.000 J20
Software	JCOP-SE 8.9 integrating JCOP-eSE 8.9 including the Platform Core software (SMK, Shared code subsystem, System OS and Communications OS and any other Guest OS as well as preloaded Applet packages	R6.01.00.1.1

To ensure secure usage a set of guidance documents is provided, together with the NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1. For details, see section 2.5 "Documentation" of this report.

For a detailed and precise description of the TOE lifecycle, see the [ST], Chapter 1.5.

## 2.2 Security Policy

The TOE has the following features:

- Hardware-supported features
  - hardware to perform computations on multiprecision integers, which are suitable for public-key cryptography
  - hardware to calculate the Data Encryption Standard with up to three keys
  - hardware to calculate the Advanced Encryption Standard (AES) with different key lengths
  - hardware to support Cipher Block Chaining (CBC), Cipher Feedback (CFB) and Counter (CTR) modes of operation for symmetric-key cryptographic block ciphers
  - hardware to support Galois/Counter Mode (GCM) of operation for symmetric-key cryptographic block ciphers
  - o hardware to calculate Cyclic Redundancy Checks (CRC)
  - o hardware to serve with True Random Numbers
- Cryptographic algorithms and functionality
  - AES
  - o Triple-DES (3DES)
  - RSA for en-/decryption and signature generation and verification
  - RSA key generation
  - ECDSA signature generation and verification
  - o ECDH key exchange
  - ECC key generation
  - ECC point operations and key validation
  - o Diffie Hellman key exchange on Montgomery Curves over GF(p)
  - Key generation for the Diffie Hellman key exchange on Montgomery Curves over GF(p))



- o EdDSA key generation and signature verification
- o SHA-1, SHA-224, SHA-256, SHA-384, SHA-512 algorithms
- o HMAC algorithms
- Multi-precision arithmetic operations including exact division, modular addition, modular subtraction, modular multiplication, modular inversion, arithmetic comparison and exact addition and subtraction.
- Data Protection Module for a secure storage of the the sensitive data.
- Random number generation according to class DRG.3 or DRG.4 of AlS20 and initialized (seeded) by the hardware random number generator of the TOE.
- Java Card 3.1 functionality
- GlobalPlatform 2.3.1 functionality
- Additional standard functionality
  - Cryptographic Service Provider feature
- NXP proprietary functionality
  - Runtime Configuration Interface: Config Applet that can be used for configuration of the TOE.
  - OS Update Component: Proprietary functionality that can update JCOP OS, Crypto Lib, Flash Services Software or Updater OS. This component allows only NXP authorised updates to the product.
  - Restricted Mode: In Restricted Mode only very limited functionality of the TOE is available such as reading logging information or resetting the Attack Counter.
  - o Error Detection Code (EDC) API

## 2.3 Assumptions and Clarification of Scope

#### 2.3.1 Assumptions

The assumptions defined in the Security Target are not covered by the TOE itself. These aspects lead to specific Security Objectives to be fulfilled by the TOE-Environment. For detailed information on the security objectives that must be fulfilled by the TOE environment, see section 5.2 of the [ST].



#### 2.3.2 Clarification of scope

The evaluation did not reveal any threats to the TOE that are not countered by the evaluated security functions of the product.

The following components of the platform are not part of the TOE:

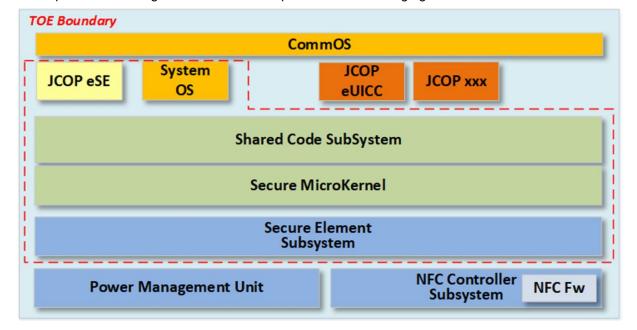
- NFC Controller Subsystem
- · Power Management Unit
- JCOP eUICC
- JCOP xxx (optional)
- CommOS

The following functionality is also present without specific security claims:

- eUICC features hosted in eUICC domain outside the boundaries of the TOE
- Programmable Timeout for SMB with Limitations
- CPLC data made available through SystemInfo
- NXP Proprietary Bytecode Compression Applets installed Pre-Issuance by NXP may
  make use of proprietary optimised bytecodes, which group common sequences of
  standard bytecodes to provide exactly the same operations, whilst saving Applet code
  space.
- Compliance to Secure Element configuration, Common Implementation Configuration, UICC Configuration, and UICC Configuration Contactless Extension.
- MIFARE is subject of separate MIFARE certification scheme
- Felica Lib is subject of separate Felica certification scheme

#### 2.4 Architectural Information

The top-level block diagram of the TOE is depicted in the following figure.





#### 2.5 Documentation

The following documentation is provided with the product by the developer to the customer:

Identifier	Version
JCOP User Guidance Manual (UGM)	1.8.0
JCOP UGM System Management Addendum	1.8.0
JCOP-eSE User Guidance Manual	1.8.0
JCOP-eSE UGM Addendum	1.8.0
JCOP UGM Addendum for CSP	1.8.0
JCOP UGM Addendum - Amd I SEMS Application	1.8.0
Errata Sheet	1.8.0

## 2.6 IT Product Testing

Testing (depth, coverage, functional tests, independent testing): The evaluators examined the developer's testing activities documentation and verified that the developer has met their testing responsibilities.

#### 2.6.1 Testing approach and depth

The developer performed extensive testing on functional specification, subsystem and SFR-enforcing module level. All parameter choices were addressed at least once. All boundary cases identified were tested explicitly, and additionally the near-boundary conditions were covered probabilistically. The testing was largely automated using industry standard and proprietary test suites. Test scripts were used extensively to verify that the functions return the expected values.

The underlying hardware test results are extendable to composite evaluations, because the underlying platform is operated according to its guidance and the composite evaluation requirements are met.

#### 2.6.2 Independent penetration testing

Based on a list of potential vulnerabilities applicable to the TOE in its operational environment created during vulnerability analysis the evaluators devised the attack scenarios for penetration tests when they were of the opinion, that those potential vulnerabilities could be exploited in the TOE's operational environment. While doing this, also the aspects of the security architecture were considered for penetration testing. Source code reviews of the provided implementation representation accompanied the development of test cases and were used to find input for testing. The code inspection also supported the testing activities because they enabled the evaluator to verify implementation aspects that could hardly be covered by test cases.

The total test effort expended by the evaluator was 125 person days (~17.9 weeks). During that test campaign, 0% of the total test time was spent on physical attacks, 0% on overcoming sensors and filters, 31 person days (24.8%) on perturbation attacks, 30 person days (24.8%) on retrieving keys with FA, 56 person days (43.75%) on side-channel attacks, 0% on exploitation of test features, 0% on attacks on RNG, 1 person day (0.8%) on ill-formed Java Card application, 4 person days (3.2%) on software attacks, and 3 person days (2.4%) on application isolation.

#### 2.6.3 Test configuration

The configuration of the sample used for independent evaluator testing and penetration testing was the same as described in the [ST].



#### 2.6.4 Test results

The testing activities, including configurations, procedures, test cases, expected results and observed results are summarised in the *[ETR]*, with references to the documents containing the full details.

The developer's tests and the independent functional tests produced the expected results, giving assurance that the TOE behaves as specified in its *[ST]* and functional specification.

No exploitable vulnerabilities were found with the independent penetration tests.

The algorithmic security level of cryptographic functionality has not been rated in this certification process, but the current consensus on the algorithmic security level in the open domain, i.e., from the current best cryptanalytic attacks published, has been taken into account.

Not all key sizes specified in the [ST] have sufficient cryptographic strength for satisfying the AVA\_VAN.5 "high attack potential". The TOE supports a wider range of key sizes (see [ST]), including those with sufficient algorithmic security level to exceed 100 bits as required for high attack potential (AVA VAN.5).

The strength of the implementation of the cryptographic functionality has been assessed in the evaluation, as part of the AVA\_VAN activities.

For composite evaluations, please consult the [ETRfC] for details.

#### 2.7 Reused Evaluation Results

There has been extensive reuse of the ALC aspects for the sites involved in the development and production of the TOE, by use of 10 site certificates and Site Technical Audit Reports.

#### 2.8 Evaluated Configuration

The TOE is defined uniquely by its name and version number NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1.

#### 2.9 Evaluation Results

The evaluation lab documented their evaluation results in the *[ETR]*, which references an ASE Intermediate Report and other evaluator documents. To support composite evaluations according to *[COMP]* a derived document *[ETRfC]* was provided and approved. This document provides details of the TOE evaluation that must be considered when this TOE is used as platform in a composite evaluation.

The verdict of each claimed assurance requirement is "Pass".

Based on the above evaluation results the evaluation lab concluded the NXP JCOP-eSE 8.9 R6 on SN330 Secure Element, version JCOP-eSE 8.9 R6.01.00.1.1, to be CC Part 2 extended, CC Part 3 conformant, and to meet the requirements of EAL 5 augmented with AVA\_VAN.5, ALC\_DVS.2, ALC\_FLR.2 and ASE\_TSS.2. This implies that the product satisfies the security requirements specified in Security Target [ST].

The Security Target claims 'demonstrable' conformance to the Protection Profile [PP0099].

The Security Target claims 'strict' conformance to the Protection Profile [PP0104]

#### 2.10 Comments/Recommendations

The user guidance as outlined in section 2.5 "Documentation" contains necessary information about the usage of the TOE. Certain aspects of the TOE's security functionality, in particular the countermeasures against attacks, depend on accurate conformance to the user guidance of both the software and the hardware part of the TOE. There are no particular obligations or recommendations for the user apart from following the user guidance. Please note that the documents contain relevant details concerning the resistance against certain attacks.

In addition, all aspects of assumptions, threats and policies as outlined in the Security Target not covered by the TOE itself must be fulfilled by the operational environment of the TOE.



The customer or user of the product shall consider the results of the certification within his system risk management process. For the evolution of attack methods and techniques to be covered, the customer should define the period of time until a re-assessment for the TOE is required and thus requested from the sponsor of the certificate.

The strength of the cryptographic algorithms and protocols was not rated in the course of this evaluation. This specifically applies to the following proprietary or non-standard algorithms, protocols and implementations: None.

Not all key sizes specified in the [ST] have sufficient cryptographic strength to satisfy the AVA\_VAN.5 "high attack potential". To be protected against attackers with a "high attack potential", appropriate cryptographic algorithms with sufficiently large cryptographic key sizes shall be used (references can be found in national and international documents and standards).





## 3 Security Target

The NXP JCOP-eSE 8.9 R6 on SN330 Secure Element Security Target Rev. 1.3 — 6 August 2025 [ST] is included here by reference.

Please note that, to satisfy the need for publication, a public version [ST-lite] has been created and verified according to [ST-SAN].

## 4 Definitions

This list of acronyms and definitions contains elements that are not already defined by the CC or CEM:

AES Advanced Encryption Standard

CBC Cipher Block Chaining (a block cipher mode of operation)
CBC-MAC Cipher Block Chaining Message Authentication Code

CFB Cipher Feedback

CTR Counter

DES Data Encryption Standard
CPLC Card Production Life Cycle
CRT Chinese Remainder Theorem
CSP Cryptographic Service Provider

DES Data Encryption Standard

DRG Deterministic Random Generator

ECB Electronic Code Book (a block cipher mode of operation)

ECC Elliptic Curve Cryptography

ECDAA Elliptic Curve Direct Anonymous Attestation
ECDSA Elliptic Curve Digital Signature Algorithm

ECDH Elliptic Curve Diffie Hellman

EDC Error Detection Code

EdDSA Elliptic Curve Edwards-curve Digital Signature Algorithm

eUICC embedded Universal Integrated Circuit Card

GCM Galois/Counter Mode

GF Galois Field
GP Global Platform

GCM Galois/Counter Mode

GSMA Groupe Speciale Mobile Association

IM4 Image4

IT Information Technology

ITSEF IT Security Evaluation Facility

JIL Joint Interpretation Library

MAC Message Authentication Code



MNO Mobile Network Operators

NFC Near-Field Communication

NSCIB Netherlands Scheme for Certification in the area of IT security

PP Protection Profile

RSA Rivest-Shamir-Adleman Algorithm

SHA Secure Hash Algorithm

SMB Secure Mailbox

TOE Target of Evaluation





## 5 Bibliography

This section lists all referenced documentation used as source material in the compilation of this report.

[CC]	Common Criteria for Information Technology Security Evaluation, Parts 1 to 5, CC:2022 Revision 1, November 2022
[CEM]	Common Methodology for Information Technology Security Evaluation, CEM:2022 Revision 1, November 2022
[COMP]	Joint Interpretation Library, Composite product evaluation for Smart Cards and similar devices, Version 1.6, April 2024
[ETR]	EVALUATION TECHNICAL REPORT SUMMARY (ETR SUMMARY) version 1, Project/Certification ID: 8122598088/NSCIB-2400084-01, 06 August 2025
[ETRfC]	EVALUATION TECHNICAL REPORT FOR COMPOSITE EVALUATION (ETR COMP) version 1, Project/Certification ID: 8122598088/NSCIB-2400084-01, 06 August 2025
[HW-CERT]	NXP SN330 A0 Series - Secure Element SN330_SE A0.1.000 J10, certificate NSCIB-CC-2400072-01 issued 12-12-2024, expiry date 12-12-2029
[HW-ETRfC]	Evaluation Technical Report Summary (ETR Summary) for Composite Evaluation (ETR COMP) for the SN330_SE A0.1.000 J20, version 2, 2025-05-28, reported by Assurance continuity Maintenance Report NSCIB-CC-2400072-01-MA1
[HW-ST]	SN330 A0 Series - Secure Element, Security Target Lite v1.3, 2025-01-10
[JIL-AAPS]	JIL Application of Attack Potential to Smartcards, Version 3.2.1, February 2024
[JIL-AMS]	Attack Methods for Smartcards and Similar Devices, Version 2.5, May 2022 (sensitive with controlled distribution) Must be retained for all smartcard-related TOEs
[NSCIB]	Netherlands Scheme for Certification in the Area of IT Security, Version 2.6, 02 August 2022
[PP_0099]	Java Card System - Open Configuration Protection Profile, version 3.2, July 2024, registered under the reference BSI-CC-PP-0099-V3-2024
[PP_0104]	Protection Profile "Cryptographic Service Provider (CSP)", version 0.9.8, 19 February 2019, registered under the reference BSI-CC-PP-0104-2019
[ST]	NXP JCOP-eSE 8.9 R6 on SN330 Secure Element Security Target Rev. 1.3 — 6 August 2025
[ST-lite]	NXP JCOP-eSE 8.9 R6 on SN330 Secure Element Security Target Lite Rev. 1.1 — 6 August 2025
[ST-SAN]	ST sanitising for publication, CC Supporting Document CCDB-2006-04-004, April 2006

(This is the end of this report.)